

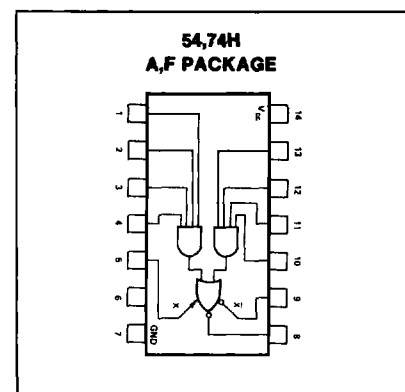
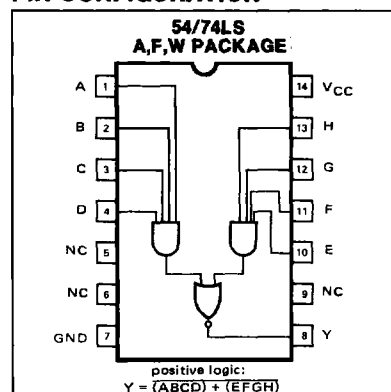
2-WIDE 4-INPUT AND-OR-INVERT GATE

54/74LS

SPEED/PACKAGE AVAILABILITY

54H F,W 74H A,F
54LS F,W 74LS A,F

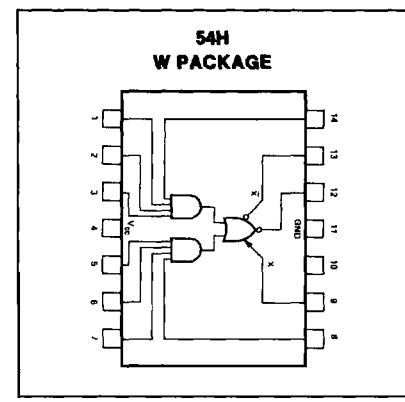
PIN CONFIGURATION



SWITCHING CHARACTERISTICS $V_{CC} = 5V$, $T_A = 25^\circ C$

TEST CONDITIONS	54/74H			54/74LS			
	C _L = 25pF R _L = 280Ω EXPANDER PINS ARE OPEN			C _L = 15pF R _L = 2kΩ			
PARAMETER	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Propagation delay time t _{PLH} Low-to-high		7 C _X = 15pF 11.4	11		12	20	ns
t _{PHL} High-to-low		6.5 C _X = 15pF 7.7	11		11.5	20	ns

Load circuit and typical waveforms are shown at the front of section.



10101

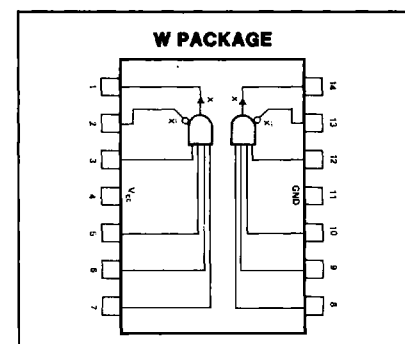
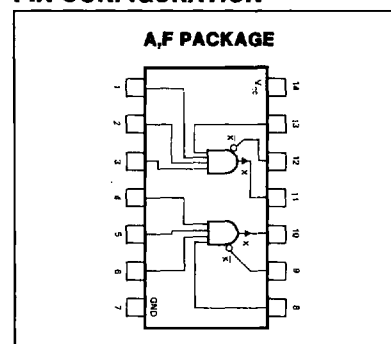
DUAL 4-INPUT EXPANDER

54/74LS

SPEED/PACKAGE AVAILABILITY

54 F,W 74 A,F
54H F,W 74H A,F

PIN CONFIGURATION



DUAL 4-INPUT EXPANDER

54/7460

SWITCHING CHARACTERISTICS $V_{CC} = 5V, T_A = 25^{\circ}C$

TEST CONDITIONS			54/74			54/74H			UNIT
PARAMETER	FROM INPUT	TO OUTPUT	MIN	TYP	MAX	MIN	TYP	MAX	
Propagation delay time t_{PLH} Low-to-high		through 54/7450, 54/7453		15	30		N/A		ns
t_{PHL} High-to-low				10	20		N/A		ns

Load circuit and typical waveforms are shown at the front of section.

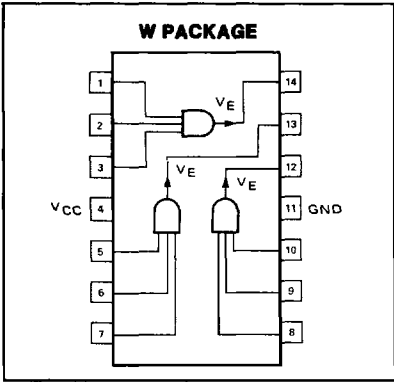
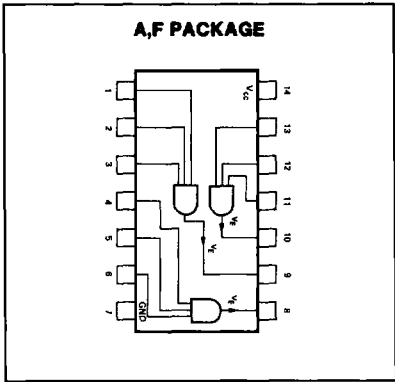
TRIPLE 3-INPUT EXPANDER

54/7461

(FOR USE WITH 54/74H52)

SPEED/PACKAGE AVAILABILITY
 54H F,W 74H A,F

PIN CONFIGURATION



3-2-2-3 INPUT AND-OR EXPANDER

54/7462

(FOR USE WITH 54/74H50, 54/74H53, 54/74H55)

SPEED/PACKAGE AVAILABILITY
 54H F,W 74H A,F

PIN CONFIGURATION

