

PRELIMINARY DATA SHEET

SKY13330-397LF: 0.1-6.0 GHz SP2T Switch

Applications

- WCDMA band and mode switching
- Antenna switch for multimode systems
- Wireless LANs (802.11 a/b/g/n)

Features

- Broadband frequency range: 0.1 GHz to 6.0 GHz
- Low insertion loss:
 - 0.35 dB @ 2 GHz
 - 0.55 dB @ 6 GHz
- High IP1dB: +39 dBm
- No external DC blocking capacitors required
- Positive low control voltage range: 1.65 to 3.3 V
- DC supply voltage: 2.3 V to 5.5 V
- Small, QFN (12-pin, 2 x 2 mm) package (MSL1, 260 °C per JEDEC J-STD-020)



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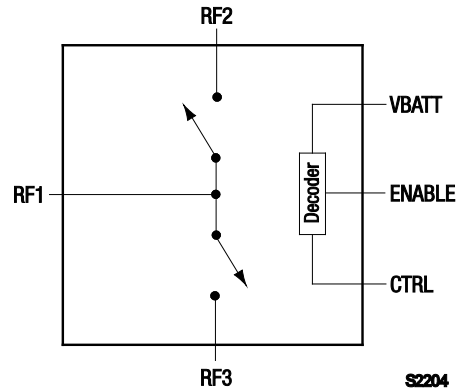


Figure 1. SKY13330-397LF Block Diagram

Description

The SKY13330-397LF is a CMOS, Silicon-On-Insulator (SOI) Single-Pole, Double-Throw (SP2T) switch. The high linearity and low insertion loss of the SKY13330-397LF make it an ideal choice for WCDMA handset and data card applications.

The switch is a “reflective short” on the isolated port.

The SKY13330-397LF SP2T switch is provided in a compact Quad Flat No-Lead (QFN) 2 x 2 mm package. A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

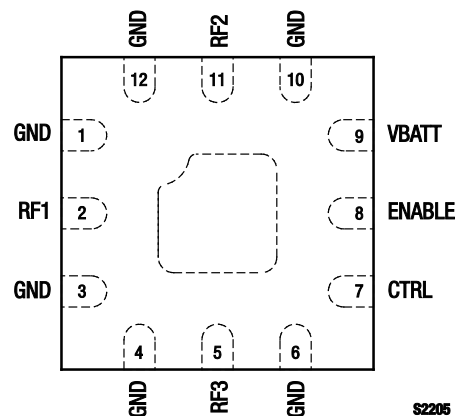


Figure 2. SKY13330-397LF Pinout – 12-Pin QFN (Top View)

Table 1. SKY13330-397LF Signal Descriptions

Pin #	Name	Description	Pin #	Name	Description
1	GND	Ground	7	CTRL	DC control pin. See Table 4.
2	RF1	RF port 1	8	ENABLE	Enable pin. See Table 4.
3	GND	Ground	9	VBATT	DC power supply
4	GND	Ground	10	GND	Ground
5	RF3	RF port 3	11	RF2	RF port 2
6	GND	Ground	12	GND	Ground

Note: Exposed pad must be properly grounded using a low impedance path.

Table 2. SKY13330-397LF Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Minimum	Maximum	Units
Supply voltage	B _{ATT}		5.5	V
Input power	P _{IN}		+40	dBm
Storage temperature	T _{STG}	−40	+125	°C
Operating temperature	T _{OP}	−20	+85	°C

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Functional Description

The SKY13330-397LF is designed for medium to high power WCDMA handset and data card switching applications. The device can also be used in a variety of other applications that require high performance RF switching such as WLANs (a/b/g/n).

The high IP0.1dB and advance proprietary fabrication process enable exceptional WCDMA harmonic and Adjacent Channel Power (ACP) performance. Excellent insertion loss and isolation are maintained over WCDMA bands 1-6 and 8-11. An internal negative voltage generator and decoder eliminate the need for external DC blocking capacitors on the RF ports.

Switching is controlled by one voltage input (CTRL). Depending on the logic voltage level applied to the control pin, the RF1 pin is connected to one of two switched RF outputs (RF2 or RF3) using a low insertion loss path, while the path between the RF1 pin and the other RF output pin is in a high isolation state.

An internal decoder is used to provide the correct logic to the switch.

For proper switching operation, the SKY13330-397LF must be set to a state for a minimum of 10 μ s before changing to a different state.

Shutdown mode is enabled by connecting the ENABLE pin to logic low. Shutdown mode reduces the overall current consumption of the device to 5 μ A, typical. When exiting shutdown mode, the switch has a 25 μ s startup time before switching occurs.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY13330-397LF are provided in Table 2. Electrical specifications are provided in Table 3.

The state of the SKY13330-397LF is determined by the logic provided in Table 4.

Typical performance characteristics of the SKY13330-397LF are illustrated in Figures 3 through 6.

Table 3. SKY13330-397LF Electrical Specifications (Note 1)

($V_{BATT} = 2.3 \text{ V to } 5.5 \text{ V}$, $CTRL = 0/1.65 \text{ V to } 3.3 \text{ V}$, $T_{OP} = +25 \text{ }^{\circ}\text{C}$, $P_{IN} = 0 \text{ dBm}$, Characteristic Impedance [Z_0] = $50 \text{ } \Omega$, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
RF Specifications						
Insertion loss	IL	RF1 to RF2/RF3: 0.1 to 1.0 GHz 1.0 to 2.2 GHz 2.2 to 2.5 GHz 2.5 to 5.0 GHz 5.0 to 6.0 GHz		0.30 0.35 0.35 0.45 0.55	0.35 0.40 0.45 0.55 0.65	dB dB dB dB dB
Isolation	Iso	RF1 to RF2/RF3: 0.1 to 1.0 GHz 1.0 to 2.2 GHz 2.2 to 2.5 GHz 2.5 to 5.0 GHz 5.0 to 6.0 GHz	35.0 28.0 26.0 18.0 16.0	37.0 30.0 28.0 20.0 18.5		dB dB dB dB dB
Return loss	IS11I	RF1 to RF2/RF3, 0.1 to 6.0 GHz		20		dB
0.1 dB Input Compression Point	IP0.1dB	RF1 to RF2/RF3, 0.8 to 2.5 GHz		+36.5		dBm
1 dB Input Compression Point	IP1dB	RF1 to RF2/RF3, 0.8 to 2.5 GHz		+39		dBm
WCDMA harmonics for bands 1 to 6, and 9 to 11	2fo to 7fo	$P_{OUT} \leq +28 \text{ dBm}$, resolution bandwidth = 1 MHz, WCDMA modulated carrier RMC12.2		-45	-35	dBm
WCDMA ACLR1 for bands 1 to 6, and 9 to 11	ACP1	$P_{OUT} \leq +28 \text{ dBm}$, Mode 1 HSDPA	+45			dBc
Switching speed		50% V_{CTL} to 10/90% RF "on" time 50% V_{CTL} to 90/10% RF "off" time 10/90% RF rise time 90/10% RF fall time		2.3 2.0 1.1 1.8		μs μs μs μs
Dwell time (Note 2)			10	12		μs
Startup time (Note 3)		Shutdown state to any RF switch state		25		μs
DC Specifications						
Control voltage (Note 4): high low	V_{CTL_HIGH} V_{CTL_LOW}		1.65 0		3.3 0.3	V V
Supply voltage	V_{BATT}		2.3		5.5	V
Supply current	I_{BATT}	$V_{BATT} = 2.3 \text{ to } 5.5 \text{ V}$		75		μA
Control current	I_{CTRL}	$CTRL = 1.8 \text{ V}$		5		μA
Shutdown mode supply current	I_{OFF}	$ENABLE = 0 \text{ V}$, $V_{BATT} = 2.3 \text{ V}$		2.5		μA

Note 1: Performance is guaranteed only under the conditions listed in this Table.

Note 2: Dwell time refers to the amount of time the switch is required to stay in any one state before switching to another state. Refer to the Functional Description section of this document for more information.

Note 3: Startup time refers to the amount of time it takes for the switch to be fully operational when coming out of shutdown mode ($EN = 0$).

Note 4: Control voltage must be $\leq V_{DD}$ at all times.

Table 4. SKY13330-397LF Truth Table

CTRL	ENABLE	Mode
1	1	RF1 to RF2
0	1	RF1 to RF3
X	0	Shutdown

Note: 1 = 1.65 to 3.3 V

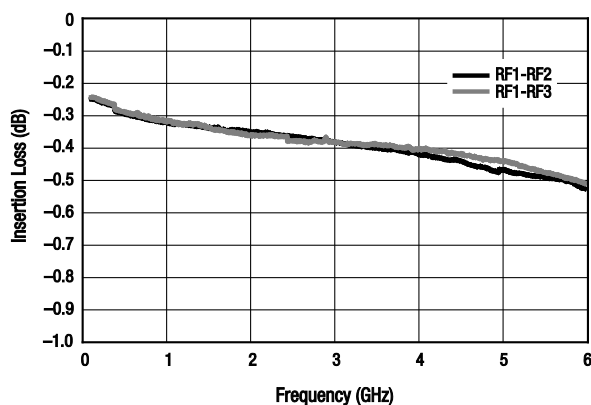
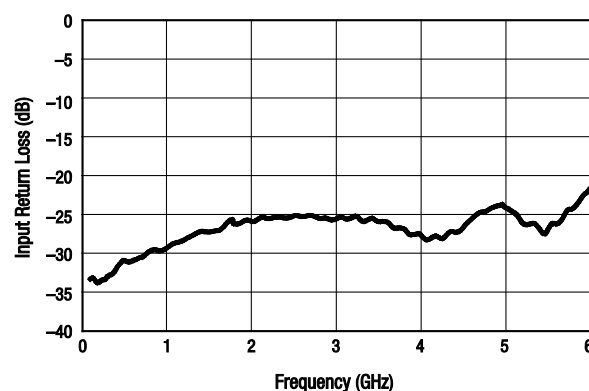
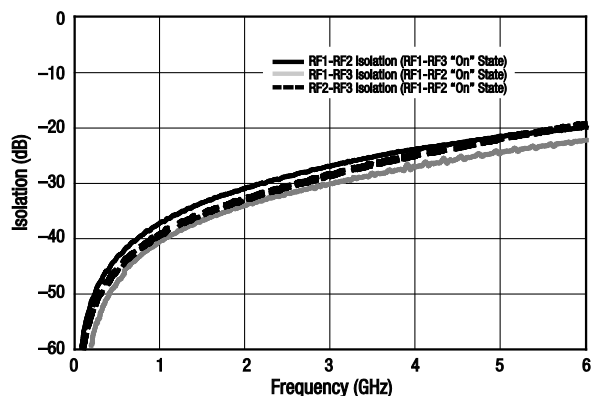
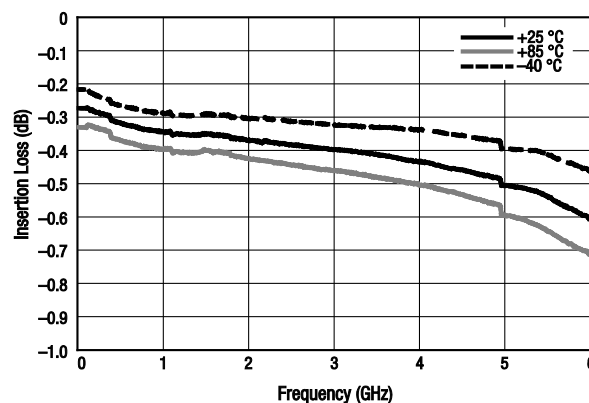
0 = -0 to 0.3 V (control voltage must be $\leq V_{DD}$ at all times)

X = don't care

Any state other than described in this Table places the switch into an undefined state. An undefined state will not damage the device.

Typical Performance Characteristics

($V_{BATT} = 2.3 \text{ V to } 5.5 \text{ V}$, CTRL = 0/1.65 V to 3.3 V, $T_{OP} = +25^\circ\text{C}$, $P_{IN} = 0 \text{ dBm}$, Characteristic Impedance [Z_0] = 50 Ω , Unless Otherwise Noted)

**Figure 3. Insertion Loss vs Frequency****Figure 4. Input Return Loss vs Frequency****Figure 5. Input to Output Isolation vs Frequency****Figure 6. Insertion Loss vs Frequency Over Temperature**

Evaluation Board Description

The SKY13330-397LF Evaluation Board is used to test the performance of the SKY13330-397LF SP2T Switch.

An Evaluation Board schematic diagram is provided in Figure 7. An assembly drawing for the Evaluation Board is shown in Figure 8.

Package Dimensions

The PCB layout footprint for the SKY13330-397LF is provided in Figure 9. Typical case markings are shown in Figure 10. Package dimensions for the 12-pin QFN are shown in Figure 11, and tape and reel dimensions are provided in Figure 12.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

THE SKY13330-397LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

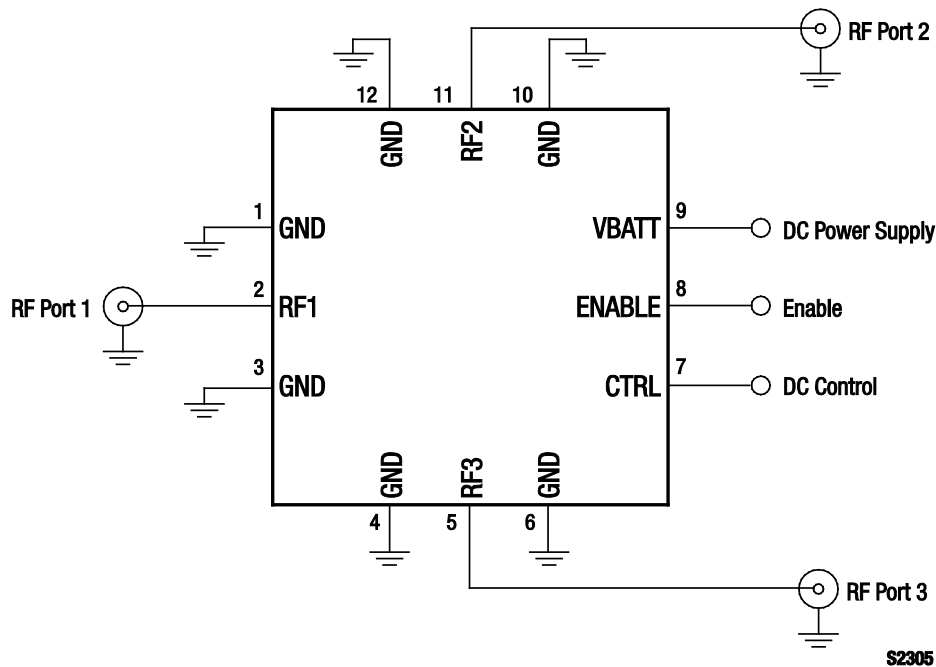
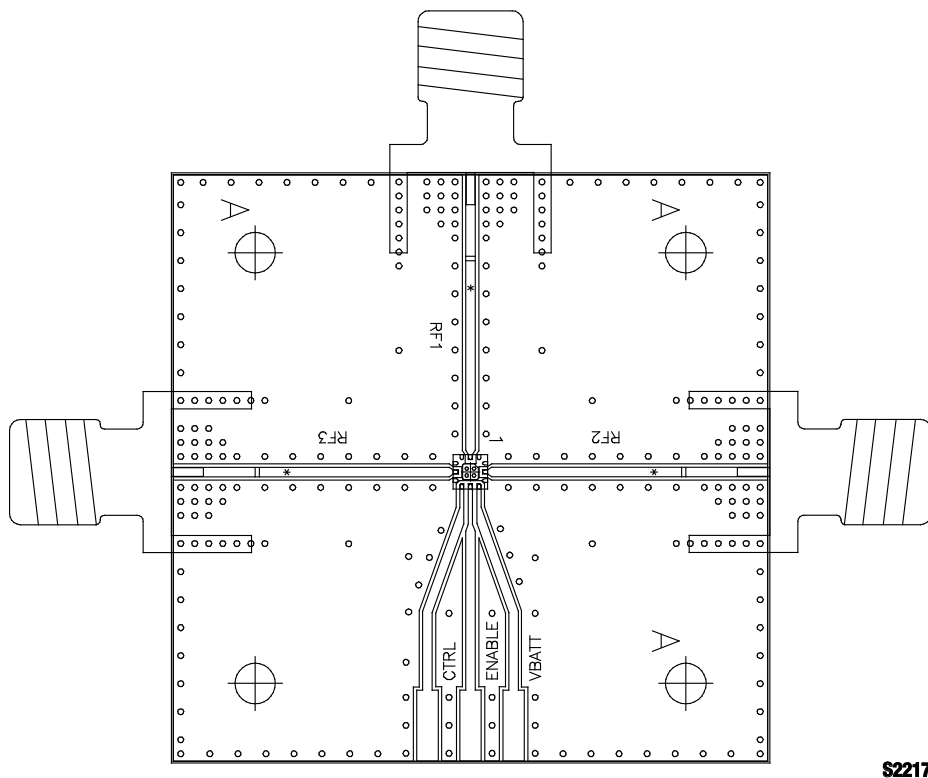
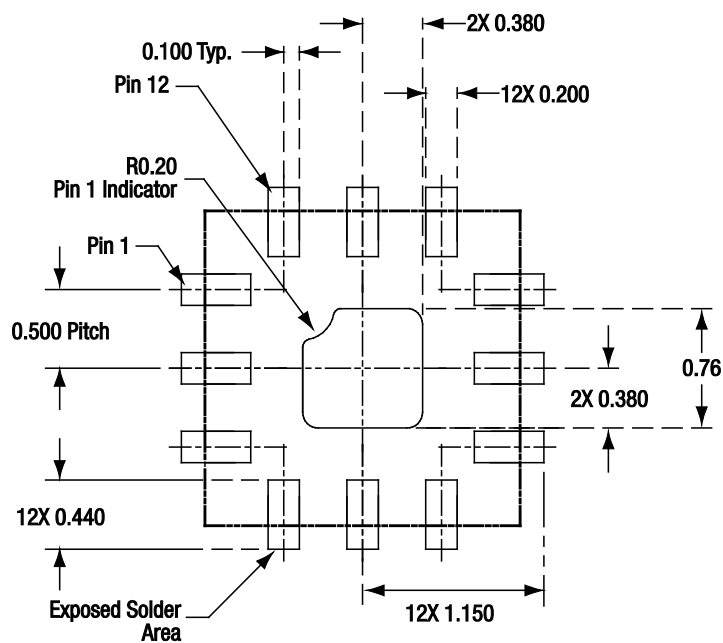


Figure 7. SKY13330-397LF Evaluation Board Schematic



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Figure 8. SKY13330-397LF Evaluation Board Assembly Diagram

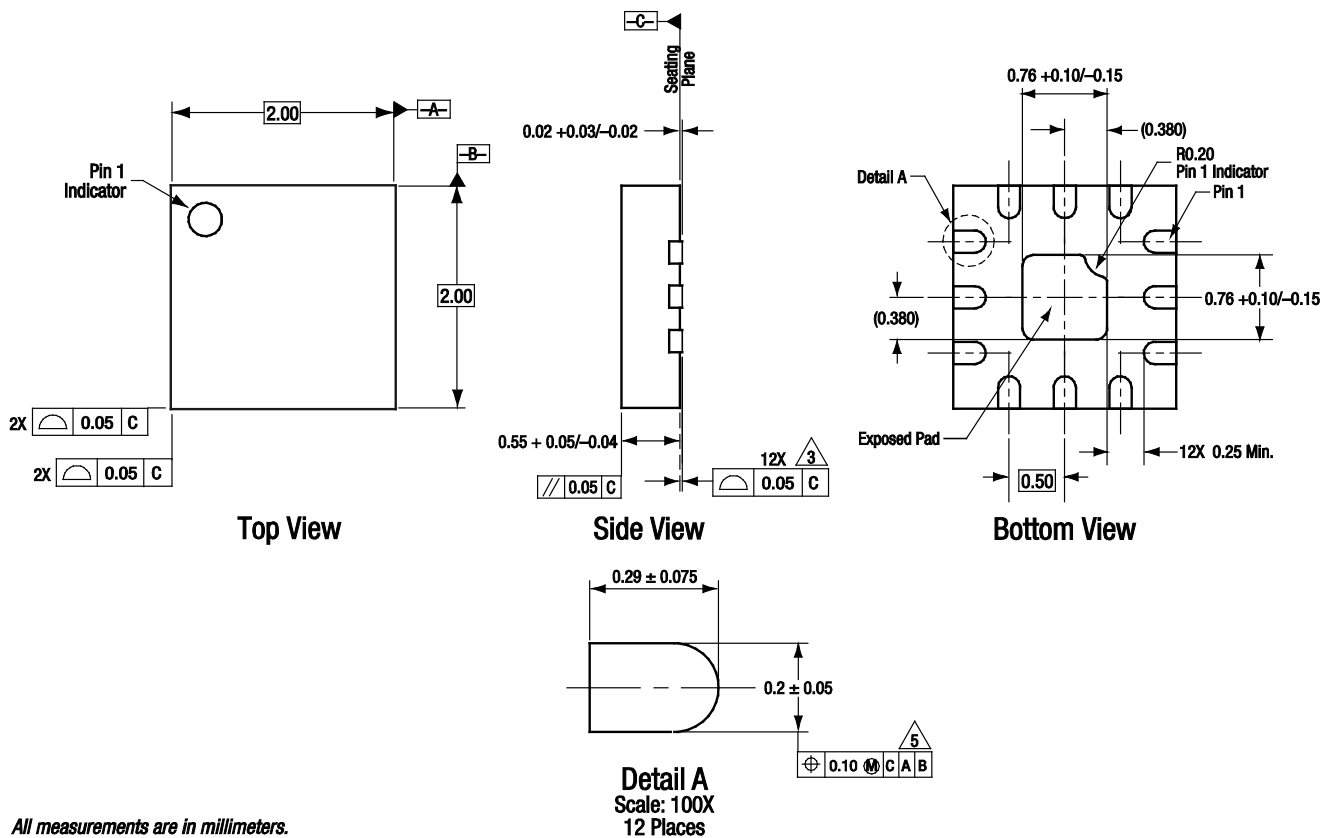


All measurements in millimeters

S2511

Figure 9. SKY13330-397LF PCB Layout Footprint
(Top View)

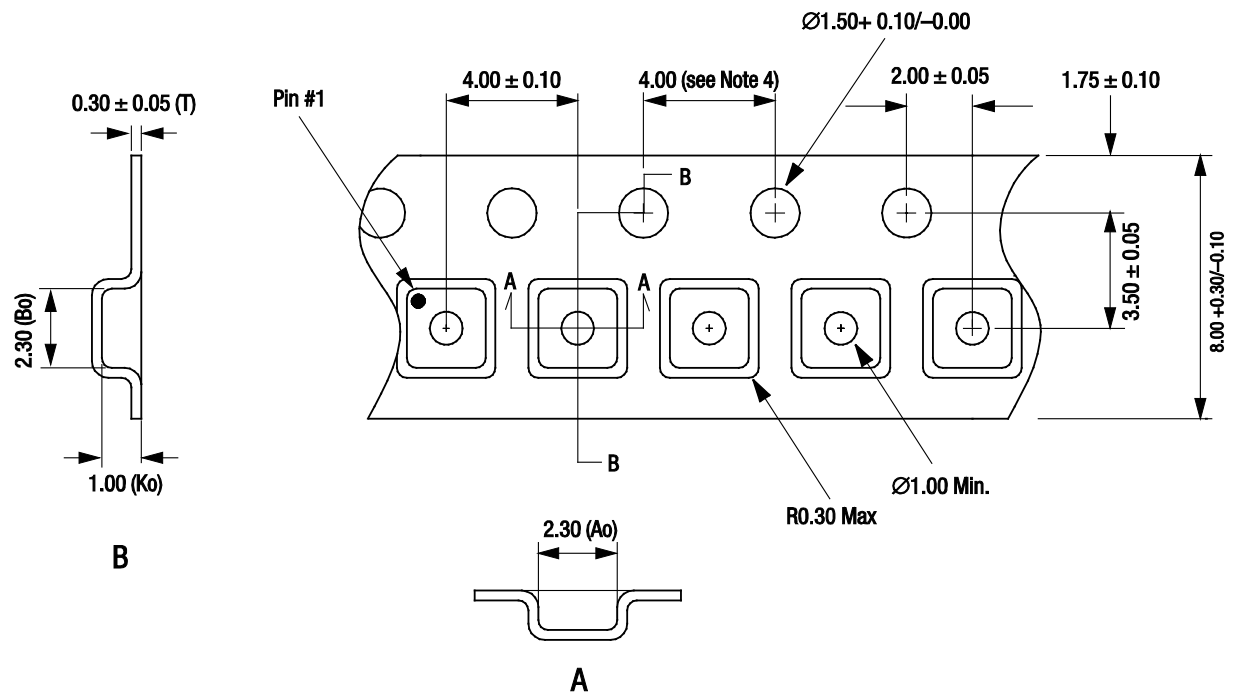
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**Figure 10. Typical Case Markings
(Top View)**

All measurements are in millimeters.
 Dimensioning and tolerancing according to ASME Y14.5M-1994.
 Coplanarity applies to the terminals and all other bottom surface metalization.
 Dimension applies to metalized terminal. If the terminal has a radius on its end,
 the width dimension should not be measured in that radius area.
 Contact areas are NiPdAu.

S2512

Figure 11. SKY13330-397LF 12-Pin QFN Package Dimensions



Notes:

1. Carrier tape: black conductive polystyrene.
2. Cover tape material: transparent conductive HSA.
3. Cover tape size: 5.40 mm width.
4. Ten sprocket hole pitch cumulative tolerance = ± 0.20 mm.
5. All measurements are in millimeters.

S1601

Figure 12. SKY13330-397LF Tape and Reel Dimensions

Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number
SKY13330-397LF SP2T Switch	SKY13330-397LF	SKY13330-397LF-EVB

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