

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on) \text{ max}}$	$I_D$ $T_A = 25^\circ\text{C}$
-20V	35m $\Omega$ @ $V_{GS} = -4.5\text{V}$	-6.0A
	45m $\Omega$ @ $V_{GS} = -2.5\text{V}$	-5.2A

## Features and Benefits

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- ESD protected Up To 3kV
- **Lead, Halogen, and Antimony Free, RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Description and Applications

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Motor Control
- Power management functions
- Analog Switch

## Mechanical Data

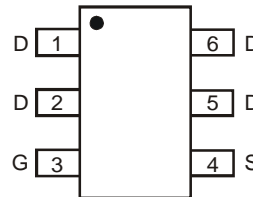
- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – MatteTin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.0013 grams (approximate)



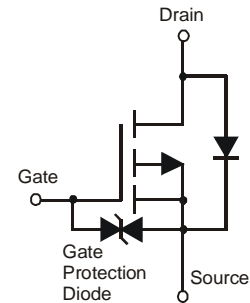
ESD PROTECTED TO 3kV



Top View



Top View  
Pin-Out



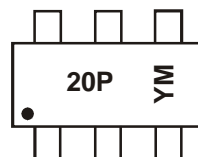
Equivalent Circuit

## Ordering Information (Note 3)

Part Number	Case	Packaging
DMP2035UVT-7	TSOT26	3,000/Tape & Reel
DMP2035UVT-13	TSOT26	10,000/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free.
  2. Diodes Inc.'s "Green" policy can be found on our website at <http://www.diodes.com>.
  3. For packaging details, go to our website at <http://www.diodes.com>.

## Marking Information



20P = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: Y = 2011)  
 M = Month (ex: 9 = September)

### Date Code Key

Year Code	2011	2012	2013	2014	2015	2016	2017
	Y	Z	A	B	C	D	E

Month Code	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	-20	V
Gate-Source Voltage			V <sub>GSS</sub>	±12	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = -4.5V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-6.0 -4.8	A
	t < 10s	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-7.2 -5.7	A
Continuous Drain Current (Note 5) V <sub>GS</sub> = -2.5V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-5.2 -4.1	A
	t < 10s	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-6.2 -4.9	A
Maximum Continuous Body Diode Forward Current (Note 5)			I <sub>S</sub>	-2.0	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I <sub>DM</sub>	-24	A

**Thermal Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic			Symbol	Value	Units
Total Power Dissipation (Note 4)			P <sub>D</sub>	1.2	W
Thermal Resistance, Junction to Ambient (Note 4)	Steady State		R <sub>θJA</sub>	106	°C/W
	t < 10s			74	
Total Power Dissipation (Note 5)			P <sub>D</sub>	2.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R <sub>θJA</sub>	65	°C/W
	t < 10s			46	
Thermal Resistance, Junction to Case (Note 5)			Steady State	R <sub>θJC</sub>	11.8
Operating and Storage Temperature Range			T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C

**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 6)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1	μA	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 6)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.4	-0.7	-1.5	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
Gate Threshold Voltage Temperature Coefficient	ΔV <sub>GS(th)</sub> /ΔT <sub>J</sub>	—	2.5	—	mV/°C	I <sub>D</sub> = -250μA, Referenced to 25°C
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	23	35	mΩ	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.0A
		—	30	45		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -4.0A
		—	41	62		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2.0A
Forward Transfer Admittance	Y <sub>fs</sub>	—	18	—	S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -5.5A
Diode Forward Voltage (Note 5)	V <sub>SD</sub>	—	-0.7	-1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A
<b>DYNAMIC CHARACTERISTICS (Note 7)</b>						
Input Capacitance	C <sub>iss</sub>	—	1610	2400	pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	157	210		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	145	200		
Gate Resistance	R <sub>G</sub>	—	9.4	14.1	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge	Q <sub>g</sub>	—	15.4	23.1	nC	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V I <sub>D</sub> = -4A
Gate-Source Charge	Q <sub>gs</sub>	—	2.5	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	3.3	—		
Turn-On Delay Time	t <sub>D(on)</sub>	—	17	33	ns	V <sub>GS</sub> = -4.5V, V <sub>DS</sub> = -10V, R <sub>G</sub> = 6Ω, I <sub>D</sub> = -1A, R <sub>L</sub> = 10Ω
Turn-On Rise Time	t <sub>r</sub>	—	12	19		
Turn-Off Delay Time	t <sub>D(off)</sub>	—	94	150		
Turn-Off Fall Time	t <sub>f</sub>	—	42	64		
Reverse Recovery Time	t <sub>rr</sub>	—	14	25		
Reverse Recovery Charge	Q <sub>rr</sub>	—	4	8	nC	I <sub>F</sub> = -4.5A, di/dt = 100A/μS

- Notes: 4. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.  
5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.  
6. Short duration pulse test used to minimize self-heating effect.  
7. Guaranteed by design. Not subject to product testing.

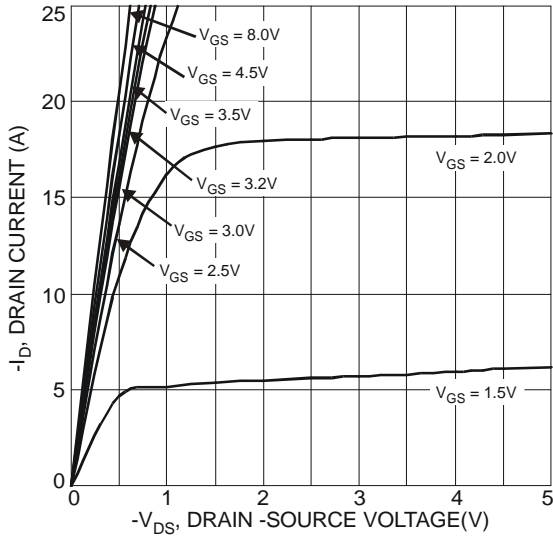


Fig. 1 Typical Output Characteristics

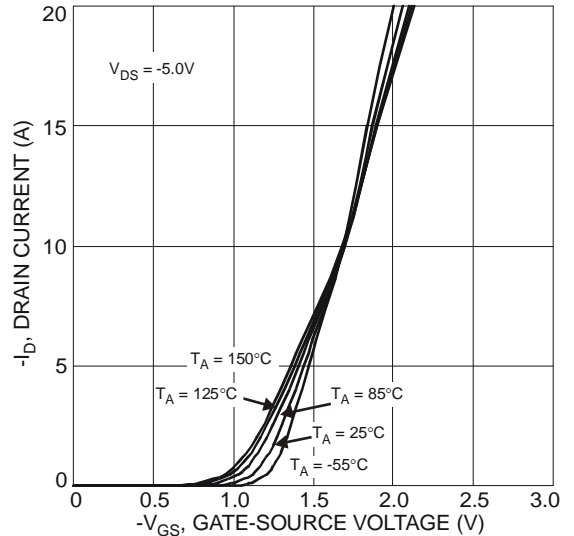


Fig. 2 Typical Transfer Characteristics

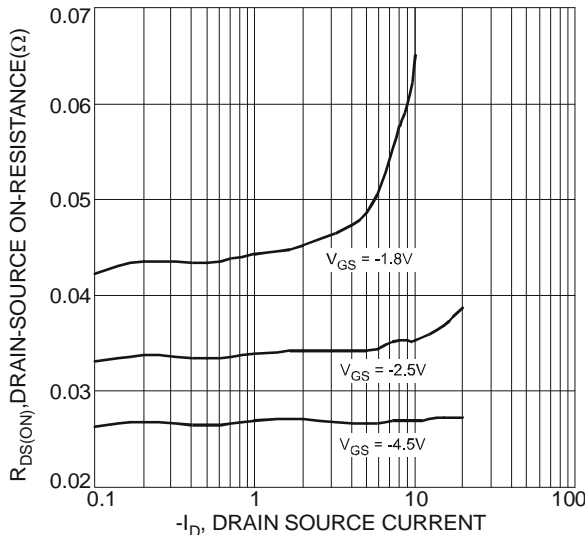


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

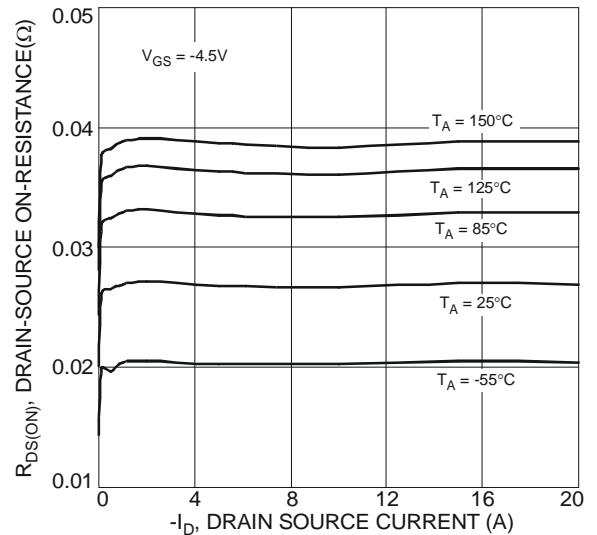


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

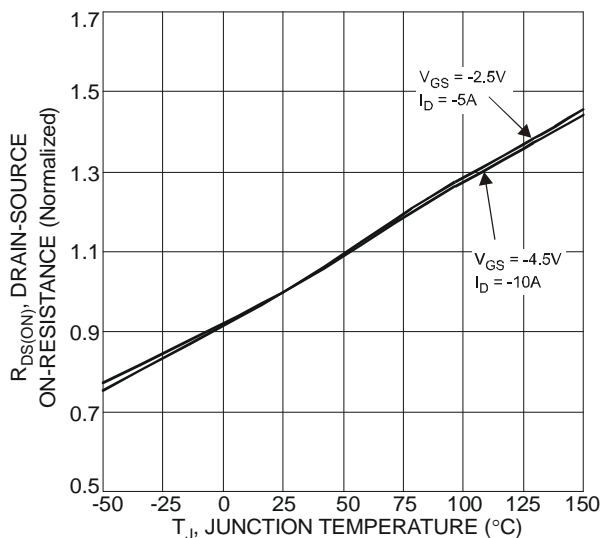


Fig. 5 On-Resistance Variation with Temperature

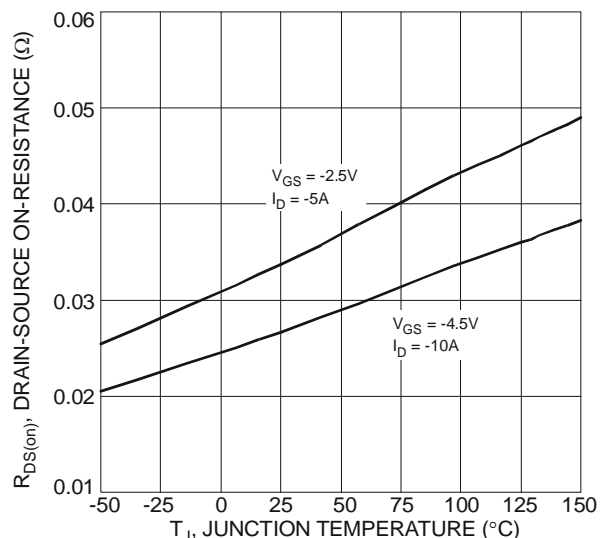


Fig. 6 On-Resistance Variation with Temperature

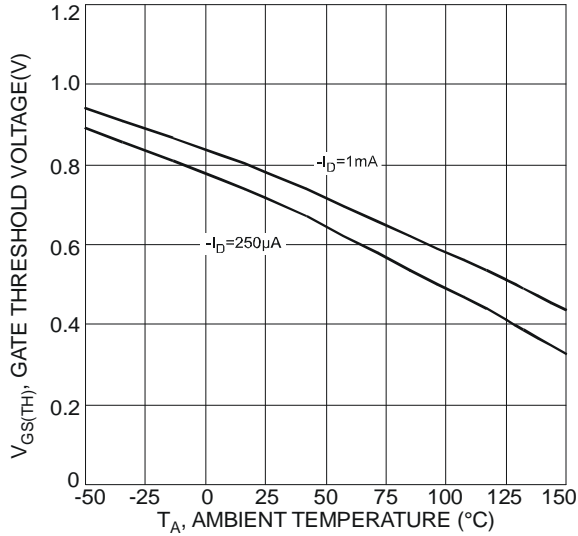


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

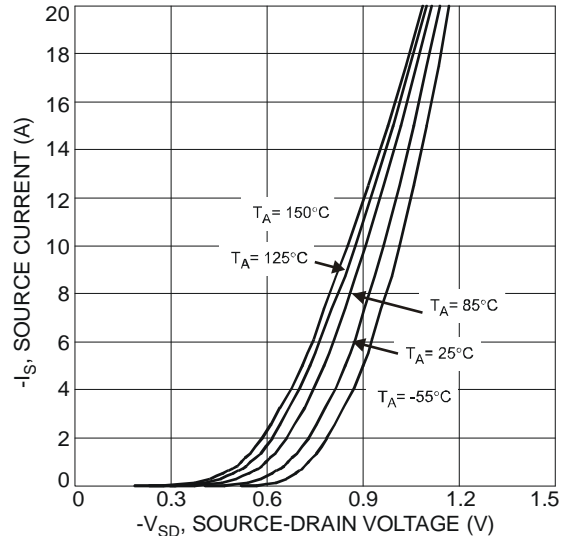


Fig. 8 Diode Forward Voltage vs. Current

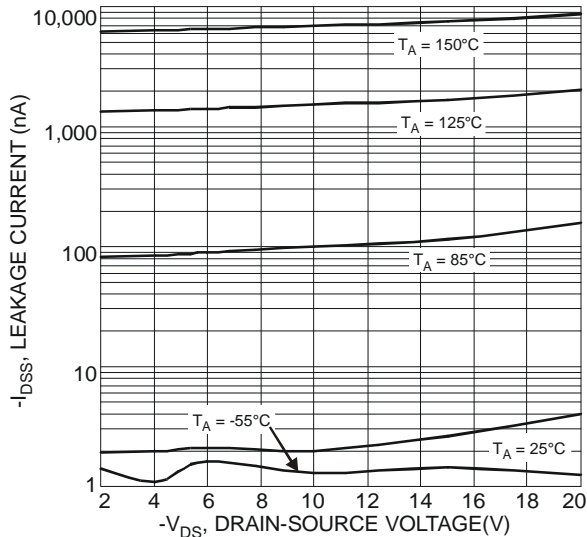


Fig. 9 Typical Drain-Source Leakage Current vs. Voltage

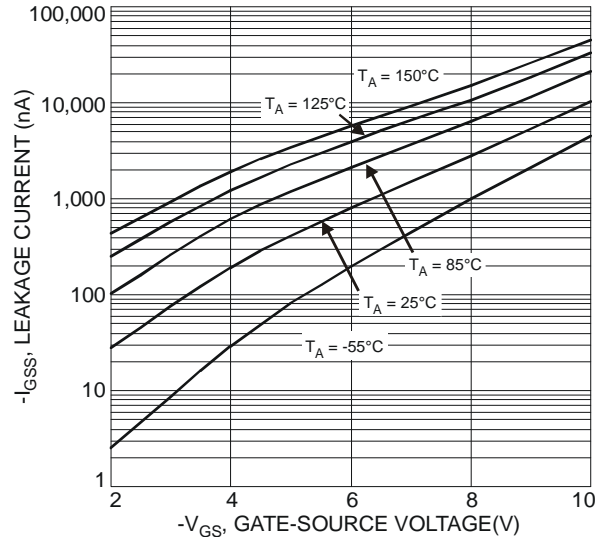


Fig. 10 Typical Gate-Source Leakage Current vs. Voltage

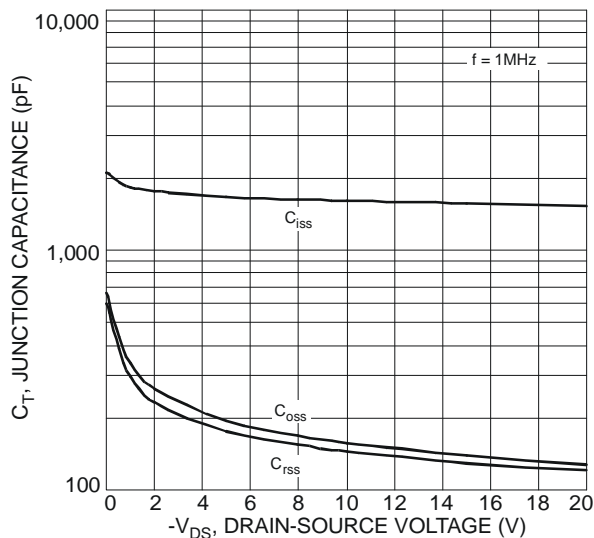


Fig. 11 Typical Junction Capacitance

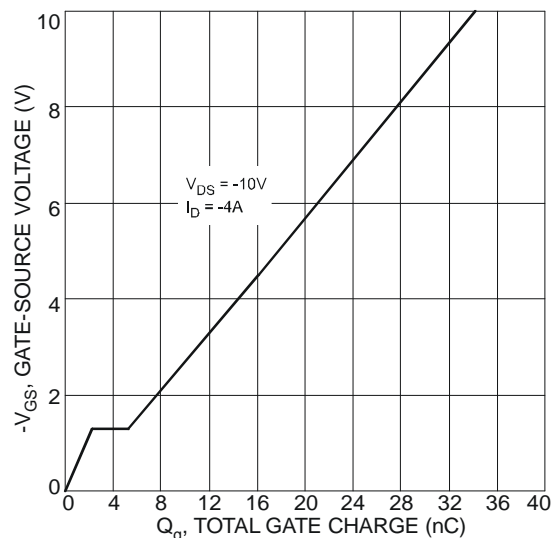


Fig. 12 Gate-Charge Characteristics

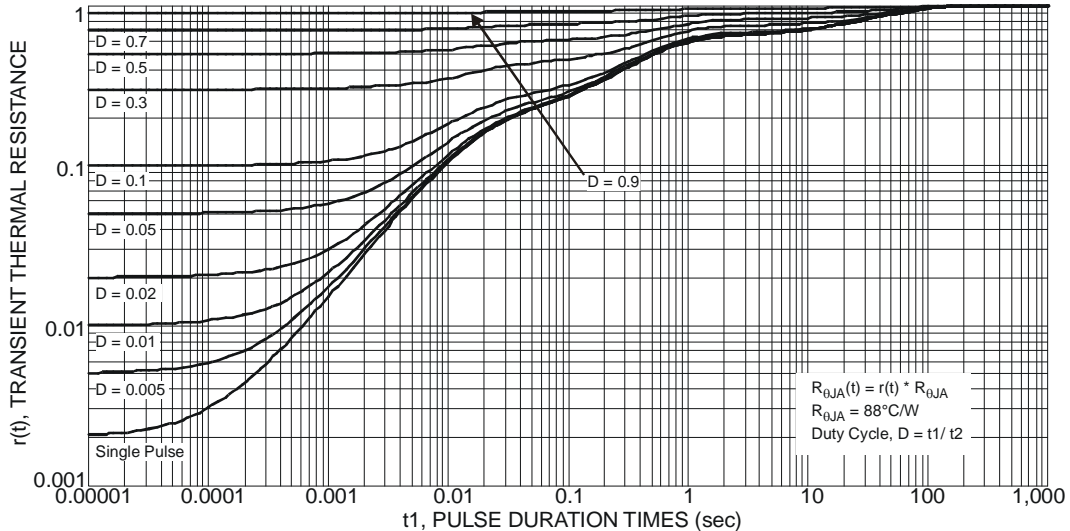
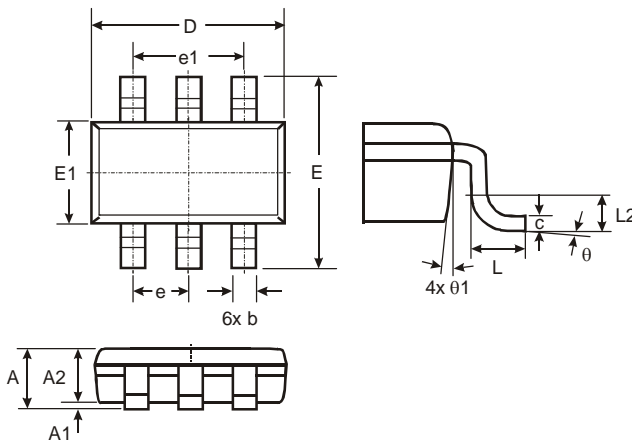


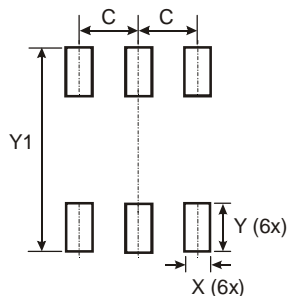
Fig. 13 Transient Thermal Resistance

**Package Outline Dimensions**



TSOT26			
Dim	Min	Max	Typ
A	–	1.00	–
A1	0.01	0.10	–
A2	0.84	0.90	–
D	–	–	2.90
E	–	–	2.80
E1	–	–	1.60
b	0.30	0.45	–
c	0.12	0.20	–
e	–	–	0.95
e1	–	–	1.90
L	0.30	0.50	–
L2	–	–	0.25
θ	0°	8°	4°
θ1	4°	12°	–
All Dimensions in mm			

**Suggested Pad Layout**



Dimensions	Value (in mm)
C	0.950
X	0.700
Y	1.000
Y1	3.199

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