

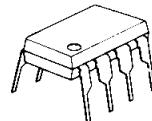
NJM3415

The NJM3415 integrated circuit is a high gain, high output current, high output voltage swing dual operational amplifier capable of driving 70mA.

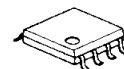
■ Package Outline

■ Absolute Maximum Ratings ($T_a=25^\circ\text{C}$)

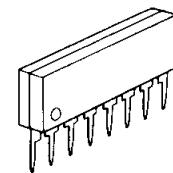
Supply Voltage	V^+ (V^+/V^-)	15V (or $\pm 7.5\text{V}$)
Differential Input Voltage	V_{ID}	15V
Input Voltage	V_I	$-0.3 \sim +15\text{V}$
Power Dissipation	P_D (D-Type) (M-Type) (L-Type)	500mW 300mW 800mW
Operating Temperature Range	T_{op}	$-20 \sim +75^\circ\text{C}$
Storage Temperature Range	T_{stg}	$-40 \sim +125^\circ\text{C}$



NJM3415D



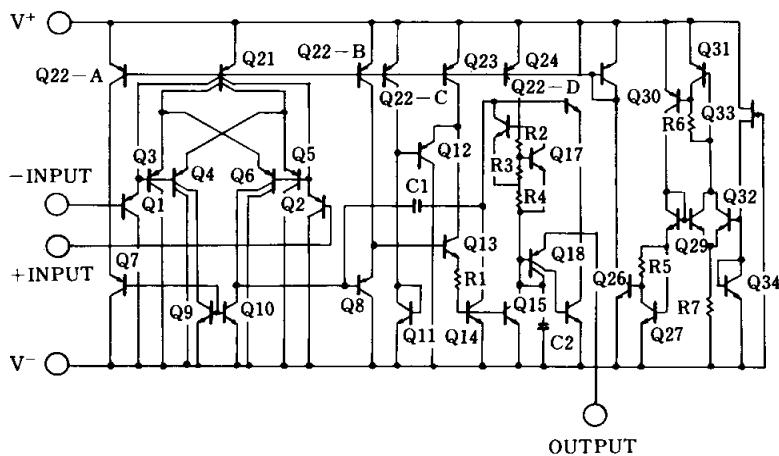
NJM3415M



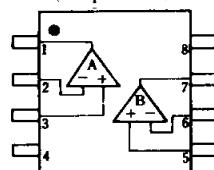
NJM3415L

■ Electrical Characteristics ($T_a=25^\circ\text{C}$, $V^+=8.6\text{V}$)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input Offset Voltage	V_{IO}	$R_S=0\Omega$	—	2	5	mV
Input Offset Current	I_{IO}	—	—	± 30	± 100	nA
Input Bias Current	I_B	—	—	100	500	nA
Large Signal Voltage Gain	A_V	$R_L=2\text{k}\Omega$	88	100	—	dB
Input Common Mode Voltage Range	V_{ICM}	$V^+=2$	—	—	—	V
Maximum Output Voltage Swing 1	V_{OM1}	$R_L \geq 2\text{k}\Omega$, $V^+=5\text{V}$	3.5	—	—	V
Maximum Output Voltage Swing 2	V_{OM2}	$I_O=70\text{mA}$, $V^+=5\text{V}$	3.2	—	—	V
Common Mode Rejection Ratio	CMR	—	80	90	—	dB
Supply Voltage Rejection Ratio	SVR	—	80	90	—	dB
Supply Current	I_{CC}	$R_L=\infty$	4.5	5.5	7.0	mA
Slew Rate	SR	—	—	1.0	—	$\text{V}/\mu\text{s}$
Unity Gain Bandwidth	GB	—	—	1.3	—	MHz
Operating Voltage Range	V^+	—	—	—	10	V

■ Equivalent Circuit ($\frac{1}{2}$ Shown)

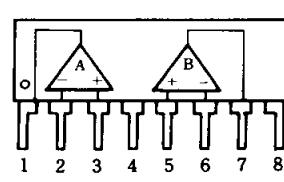
■ Connection Diagrams

D,M-Type
(Top View)

PIN FUNCTION

- 1 . OUTPUT
- 2 . A-INPUT
- 3 . A+INPUT
- 4 . GND
- 5 . B+INPUT
- 6 . B-INPUT
- 7 . B OUTPUT
- 8 . V+

L-Type



■ Typical Characteristics