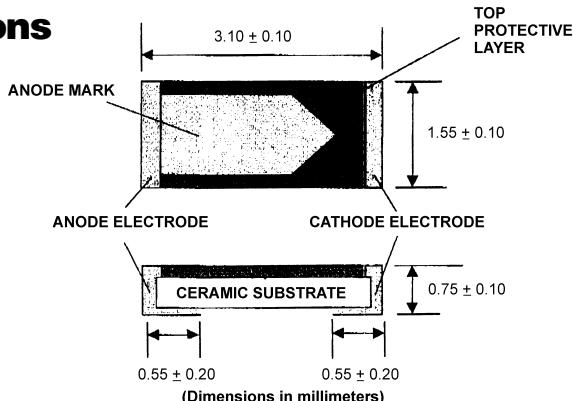


CD4148

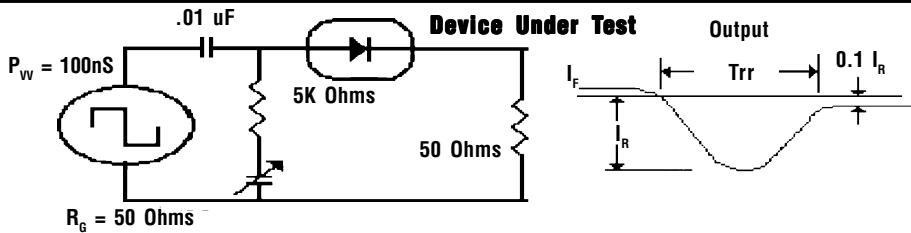

Mechanical Dimensions

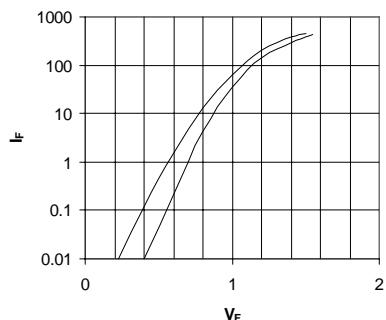
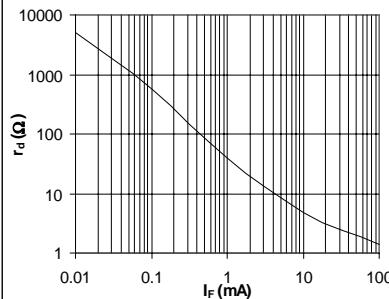


Features

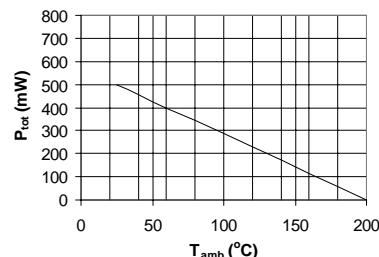
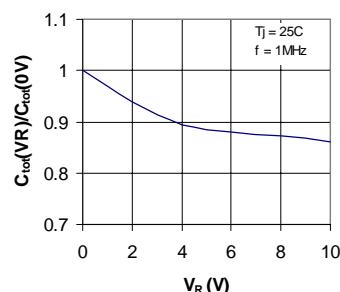
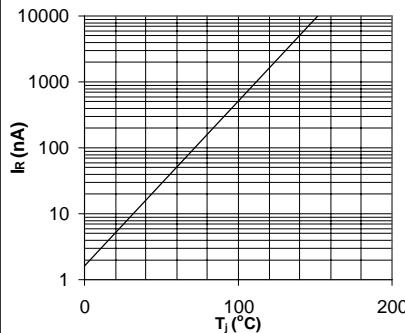
- INDUSTRY STANDARD 1206 PACKAGE/FOOTPRINT
- DUAL SIDE MOUNTING
- INTERNAL SOLDER CONSTRUCTION
- ELECTRICALLY IDENTICAL TO JEDEC 1N4148
- NO INTERMITTENT CONTACTS
- 350 mW POWER DISSIPATION

	CD4148	Units
Maximum Ratings	CD4148	
Peak Reverse Voltage... V_{RM}	100	Volts
RMS Reverse Voltage... $V_{R(rms)}$	75	Volts
Peak Forward Current... I_F	200	mAmps
Peak Forward Surge Current... I_{FSM}	2000	mAmps
Power Dissipation... P_D	350	mW
Operating Temperature... T_J	175	°C
Storage Temperature Range... T_{STRG}	-55 to 150	°C
Electrical Characteristics		
Maximum Forward Voltage... V_F @ $I_F = 10$ mA	1.0	Volts
Maximum DC Reverse Current... I_R @ $V_R = 75$ V	5.0	µAmps
Maximum Diode Capacitance... C_D	4	pF
Maximum Reverse Recovery Time... T_{RR}	4	ns

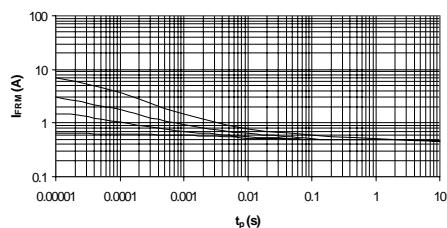


CD4148
Forward Characteristics

Dynamic forward resistance versus forward current

Admissible power dissipation versus ambient temperature

Valid provided that leads at a distance of 8mm from case are kept at ambient temperature


Relative capacitance versus reverse voltage

Leakage current versus junction temperature

Admissible repetitive peak forward current versus pulse duration

Valid provided that leads at a distance of 8mm from case are kept at ambient temperature



Ratings at
25 Deg. C ambient
temperature
unless otherwise
specified.

Single Phase Half
Wave, 60 Hz
Resistive or
Inductive Load.

For Capacitive
Load, Derate
Current by 20%.