TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (Ultra High speed U-MOSIII)

TPCP8001-H

High Efficiency DC / DC Converter Applications
Notebook PC Applications
Portable Equipment Applications

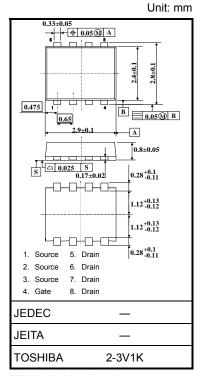
- Small footprint due to a small and thin package
- High speed switching
- Small gate charge: QSW = 3.6 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = $13 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 16 \mathrm{S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{V)}$
- Enhancement mode: $V_{th} = 1.1 \text{ to } 2.3 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage (R	$R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	30	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	7.2	Α	
Diam current	Pulsed (Note 1)	I_{DP}	28.8	ζ	
Drain power dissipati	on $(t = 5 s)$ (Note 2a)	P_{D}	1.68	W	
Drain power dissipati	on (t = 5 s) (Note 2b)	P _D	0.84	W	
Single-pulse avalance	he energy (Note 3)	E _{AS}	33.6	mJ	
Avalanche current		I _{AR}	7.2	Α	
Repetitive avalanche	energy Note 2a) (Note 4)	E _{AR}	0.066	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	

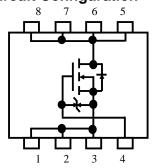
Note: For Notes 1 to 5, refer to the next page.

This transistor is an electrostatic-sensitive device. Handle with care.

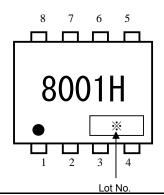


Weight: 0.017 g (typ.)

Circuit Configuration



Marking (Note 5)



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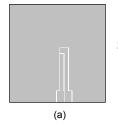
Thermal Characteristics

Characteristic	Symbol	Max	Unit	
Thermal resistance, channel to ambient $(t=5\;\text{s}) \tag{Note 2a}$	R _{th (ch-a)}	74.4	°C/W	
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	148.8	°C/W	

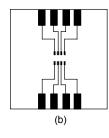
Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



 $\begin{aligned} & \text{FR-4} \\ 25.4 \times 25.4 \times 0.8 \\ & \text{(Unit: mm)} \end{aligned}$



FR-4 $25.4 \times 25.4 \times 0.8$ (Unit: mm)

Note 3: V_{DD} = 24 V, T_{ch} = 25°C (initial), L = 0.5 mH, R_G = 25 Ω , I_{AR} = 7.2A

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: • on the lower left of the marking indicates Pin 1.

* Weekly code: (Three digits)



Week of manufacture

(01 for first week of the year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)

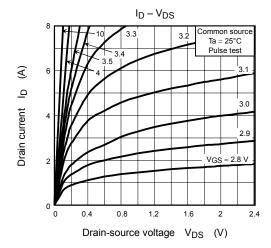


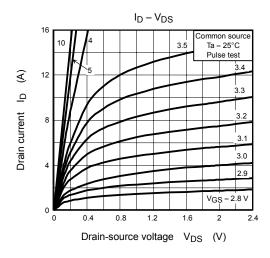
Electrical Characteristics (Ta = 25°C)

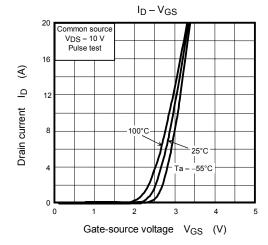
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА	
Drain cutoff curre	ent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μΑ	
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V	
Diain-source bre	akdown voltage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15 — — 11 — 23		ľ		
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.1	_	2.3	V	
Drain course ON	5		$V_{GS} = 4.5 \text{ V}, I_D = 3.6 \text{ A}$	_	19	25	mo	
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 3.6 A	_	13	16	mΩ	
Forward transfer	ward transfer admittance $ Y_{fs} $ $V_{DS} = 10 \text{ V}, I_D = 3.6 \text{ A}$		$V_{DS} = 10 \text{ V}, I_D = 3.6 \text{ A}$	8	16	_	S	
Input capacitance	9	C _{iss}		_	640	_		
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	75	_	pF	
Output capacitance		C _{oss}		_	300	_		
Switching time	Rise time	t _r	Ves 10 V	_	4	_	- ns	
	Turn-on time	t _{on}		_	8	_		
	Fall time	t _f		_	4	_		
	Turn-off time	t _{off}	$V_{DD} \simeq 15 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$	_	18	_		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.2 \text{ A}$	_	11	_		
			$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 7.2 \text{ A}$	_	6.3	_		
Gate-source charge 1		Q _{gs1}		_	2.2	_	nC	
Gate-drain ("Miller") charge		Q _{gd}	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.2 \text{ A}$	_	2.6	_		
Gate switch charge		Q _{SW}]	_	3.6	_		

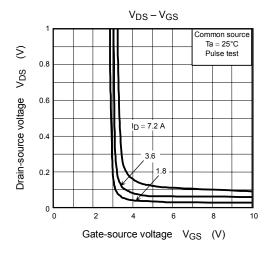
Source-Drain Ratings and Characteristics (Ta = 25°C)

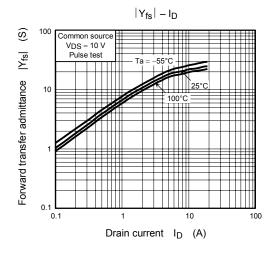
Character	istic		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	28.8	Α
Forward voltage (diode)			V _{DSF}	I _{DR} = 7.2 A, V _{GS} = 0 V	_	_	-1.2	V

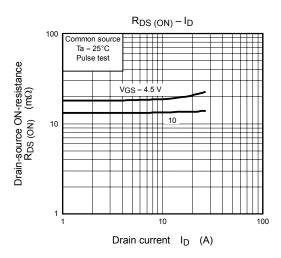




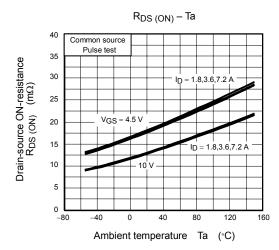


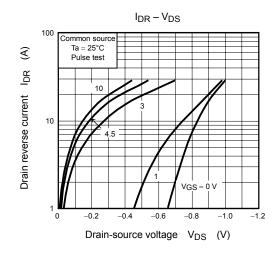


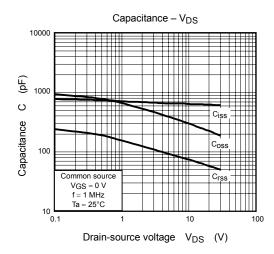


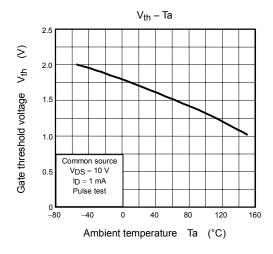


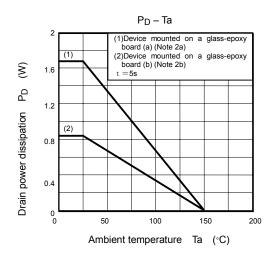
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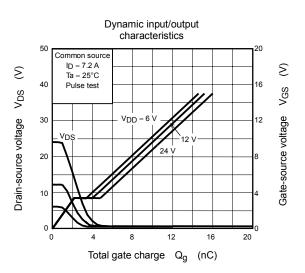


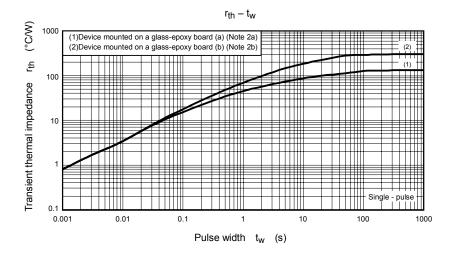




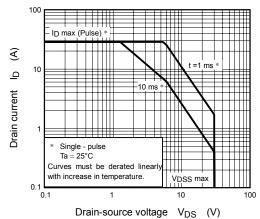












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Handbook" etc. 021023_A

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