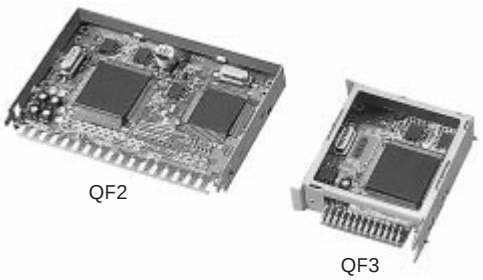
Type : **QF2 · QF3**

Part No.: **ENA6□□□□□□**

Type QF2, QF3 high performance QPSK channel decoder.
Suitable for Multimedia Application.



Features

- Compact size, Best fit on Set Top Box and PC-AT Board
- Easy Interface with Transport Decoder

Recommended Applications

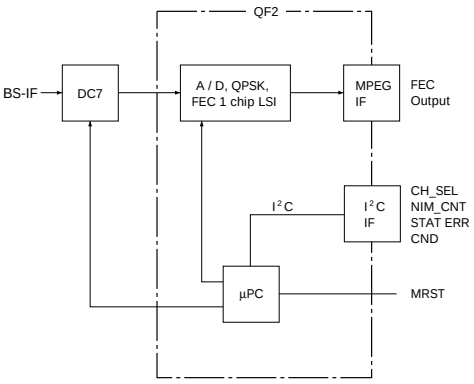
- DBS / CS Digital Set Top Box, PC-AT Board

■ Performance Specifications, Summary

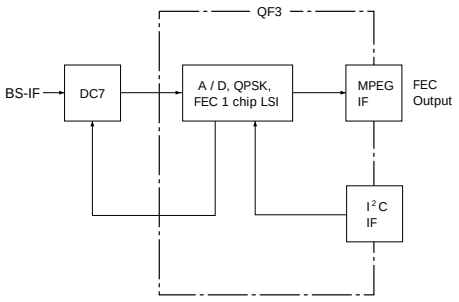
1. Input Signal	Analog I and Q baseband
2. Input Signal Level	Digital DBS Tuner (DC7) Output Compatible
3. Output Signal	8 Bits Parallel FEC Decorded
4. Output Level	CMOS (3.3 V) Level
5.Transmission Rate	4 to 60 M bps.
6. Host Interface Bus	$I^2C^{(R)}$
7. Power Supply	+ 3.3 V : TBD + 5 V : TBD + 12 V : TBD
8. Modulation Method	QPSK

■ Block Diagram

● QF2



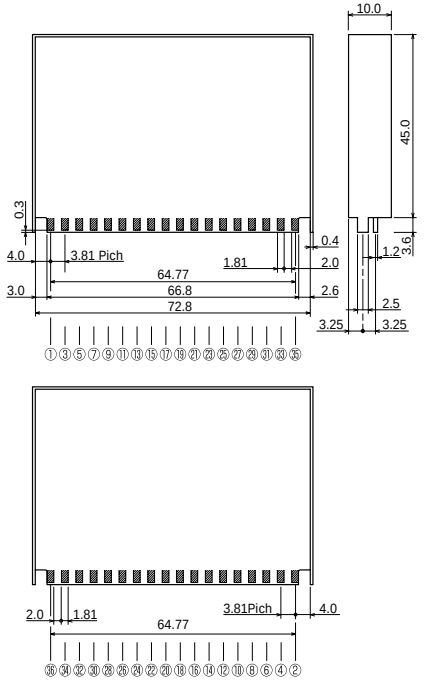
● QF3



Design and Specifications are subject to change without notice. Ask factory for technical specifications before purchase and/or use.
Whenever a doubt about safety issues arises from this product, please inform us immediately for technical consultation.

■ Dimensions in mm (not to scale)

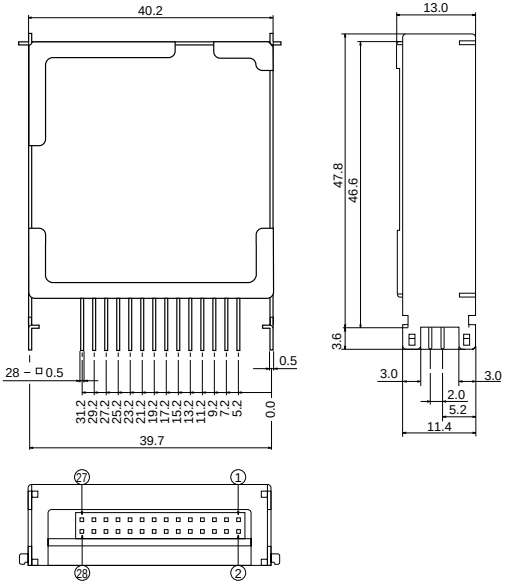
● QF2



■ TERMINAL SUPPLY VOLTAGE

No.	Terminal Name	Supply Voltage
1	V DC (A)	+ 5 V
2	GND	
3	I IN	
4	Q IN	
5	V DC (A)	+ 3.3 V
6	GND	
7	V DC (D)	+ 5 V
8	GND	
9	AGC	
10	AFC	
11	V DC (A)	+ 12 V
12	V DC (D)	+ 3.3 V
13	SDA	
14	SCL	
15	NRESET	
16	SD IN	
17	GND	
18	GND	
19	FSYNC	
20	GND	
21	SYBCLK	
22	NOERR	
23	PSYNC	
24	DEN	
25	OCLK	
26	CHD0	
27	CHD1	
28	CHD2	
29	CHD3	
30	CHD4	
31	CHD5	
32	CHD6	
33	CHD7	
34	ENABLE	
35	CLOCK	
36	DATA	

● QF3



■ TERMINAL SUPPLY VOLTAGE

No.	Terminal Name	Supply Voltage
1	V DC (digital)	+ 3.3 V
2	V DC (analog)	+12.0 V
3	CHD6	
4	NOERR	
5	CHD4	+ 3.3 V
6	CHD5	
7	CHD2	
8	CHD1	
9	CHD7	
10	CHD3	
11	OCLK	
12	CHDO	
13	DATAEN	
14	FSYNC	
15	DVB–SEL	
16	PSYNC	
17	V DC (digital)	+ 5.0 V
18	SYBCLK	
19	V DC (analog)	+ 3.3 V
20	NRESET	
21	I IN	
22	SCL (I^2C)	
23	Q IN	
24	SDA (I^2C)	
25	V DC (analog)	+ 5.0 V
26	AGC	
27	GND	
28	AFC	

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