

**Vishay Semiconductors** 

## 12 GHz Silicon NPN Planar RF Transistor

#### **Features**

- · Low power applications
- · Very low noise figure
- High transition frequency f<sub>T</sub> = 12 GHz
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **Applications**

For low noise applications such as preamplifiers, mixers and oscillators in analog and digital TV-systems (e.g., satellite tuners) up to microwave frequencies.

#### **Mechanical Data**

Typ:TSDF1220

Case: SOT143 Plastic case Weight: approx. 8.0 mg

**Pinning:** 1 = Collector, 2 = Emitter,

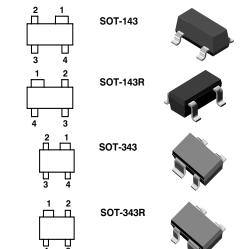
3 = Base, 4 = Emitter **Typ:** TSDF1220R

**Case:** SOT143R Plastic case **Weight:** approx. 8.0 mg

**Pinning:** 1 = Collector, 2 = Emitter,

3 = Base, 4 = Emitter







Electrostatic sensitive device.

Observe precautions for handling.

13629

Typ: TSDF1220W

Case: SOT343 Plastic case Weight: approx. 6.0 mg

**Pinning:** 1 = Collector, 2 = Emitter,

3 = Base, 4 = Emitter **Typ:** TSDF1220RW

Case: SOT343R Plastic case Weight: approx. 6.0 mg

Pinning: 1 = Collector, 2 = Emitter,

3 = Base, 4 = Emitter

#### **Parts Table**

Part	Ordering Code	Type Marking	Remarks
TSDF1220	TSDF1220-GS08	F20	Tape and Reel
TSDF1220R	TSDF1220R-GS08	20F	Tape and Reel
TSDF1220RW	TSDF1220RW-GS08	W2F	Tape and Reel
TSDF1220W	TSDF1220W-GS08	WF2	Tape and Reel

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## **Vishay Semiconductors**



#### **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Collector-base voltage		$V_{CBO}$	9	V
Collector-emitter voltage		V <sub>CEO</sub>	6	V
Emitter-base voltage		V <sub>EBO</sub>	2	V
Collector current		I <sub>C</sub>	40	mA
Total power dissipation	T <sub>amb</sub> ≤ 60 °C	P <sub>tot</sub>	200	mW
Junction temperature		T <sub>j</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C

#### **Maximum Thermal Resistance**

Parameter	Test condition	Symbol	Value	Unit
Junction to ambient air	1)	$R_{thJA}$	450	K/W

 $<sup>^{1)}</sup>$  on glass fibre printed board (25 x 20 x 1.5)  $\text{mm}^3$  plated with 35  $\mu\text{m}$  Cu

#### **Electrical DC Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Collector-emitter cut-off current	$V_{CE} = 12 \text{ V}, V_{BE} = 0$	I <sub>CES</sub>			100	μА
Collector-base cut-off current	$V_{CB} = 10 \text{ V}, I_{E} = 0$	I <sub>CBO</sub>			100	nA
Emitter-base cut-off current	V <sub>EB</sub> = 1 V, I <sub>C</sub> = 0	I <sub>EBO</sub>			2	μΑ
Collector-emitter breakdown voltage	$I_C = 1 \text{ mA}, I_B = 0$	V <sub>(BR)CEO</sub>	6			V
Collector-emitter saturation voltage	$I_C = 30 \text{ mA}, I_B = 3 \text{ mA}$	V <sub>CEsat</sub>		0.1	0.5	V
DC forward current transfer ratio	$V_{CE} = 5 \text{ V}, I_{C} = 20 \text{ mA}$	h <sub>FE</sub>	50	100	150	

#### **Electrical AC Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Transition frequency	$V_{CE} = 5 \text{ V}, I_{C} = 20 \text{ mA}, f = 1 \text{ GHz}$	f <sub>T</sub>		12		GHz
Collector-base capacitance	V <sub>CB</sub> = 1 V, f = 1 MHz	C <sub>cb</sub>		0.3		pF
Collector-emitter capacitance	V <sub>CE</sub> = 1 V, f = 1 MHz	C <sub>ce</sub>		0.35		pF
Emitter-base capacitance	V <sub>EB</sub> = 0.5 V, f = 1 MHz	C <sub>eb</sub>		0.5		pF
Noise figure	$V_{CE} = 5 \text{ V}, I_{C} = 3 \text{ mA}, Z_{S} = Z_{Sopt},$ $Z_{L} = 50 \Omega, f = 2 \text{ GHz}$	F		1.2		dB
Power gain	$V_{CE} = 5 \text{ V, } I_{C} = 20 \text{ mA, } Z_{S} = Z_{Sopt}, Z_{L} = 50 \Omega, f = 2 \text{ GHz}$	G <sub>pe</sub>		14		dB
Transducer gain	$V_{CE} = 5 \text{ V, } I_{C} = 20 \text{ mA},$ $Z_{0} = 50 \Omega, f = 2 \text{ GHz}$	S <sub>21e</sub>   <sup>2</sup>		12.5		dB
Third order intercept point at output	$V_{CE} = 5 \text{ V}, I_{C} = 20 \text{ mA}, f = 2 \text{ GHz}$	IP <sub>3</sub>		22		dBm

### **Vishay Semiconductors**

### **Typical Characteristics**

 $T_{amb}$  = 25 °C unless otherwise specified

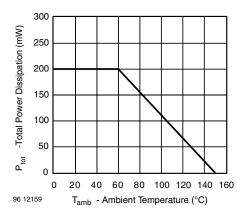


Figure 1. Total Power Dissipation vs. Ambient Temperature

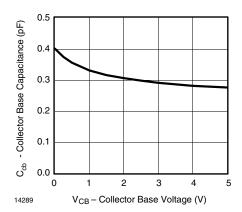


Figure 3. Collector Base Capacitance vs. Collector Base Voltage

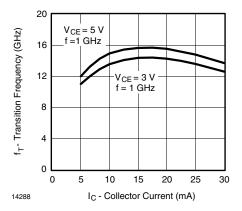


Figure 2. Transition Frequency vs. Collector Current

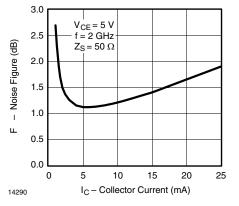
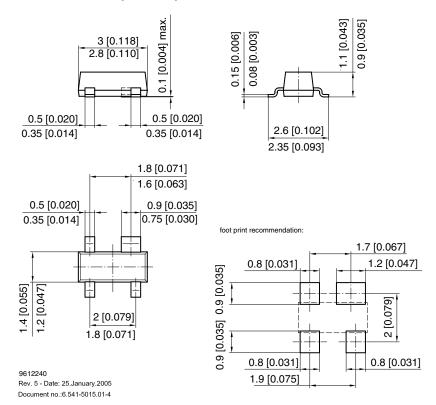


Figure 4. Noise Figure vs. Collector Current

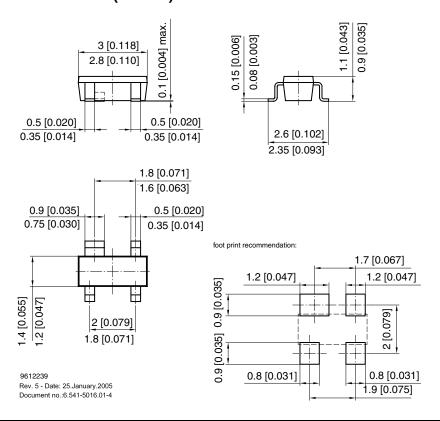
### **Vishay Semiconductors**



### Package Dimensions in mm (Inches): SOT143



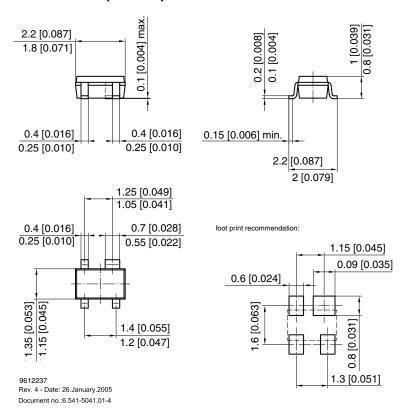
## Package Dimensions in mm (Inches): SOT143R



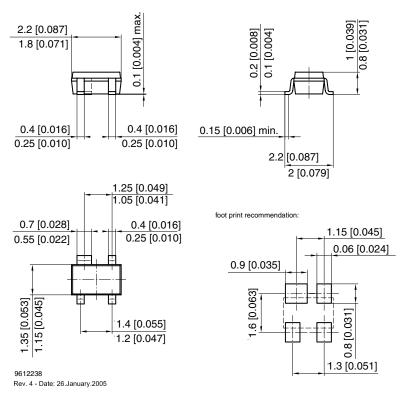


**Vishay Semiconductors** 

### Package Dimensions in mm (Inches): SOT343



## Package Dimensions in mm (Inches): SOT343R



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#### **Vishay Semiconductors**



## **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

> We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

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