

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62101PG,TD62101FG,TD62103PG,TD62103FG TD62104PG,TD62104FG,TD62105PG,TD62105FG

7CH DARLINGTON SINK DRIVER

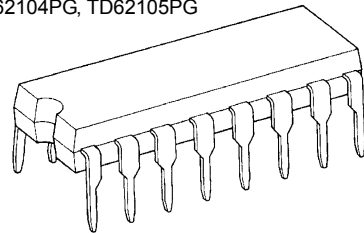
The TD62101PG / FG series are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs. The suffix (G) appended to the part number represents a Lead (Pb)-Free product.

Features

- Output current (single output) : 500 mA (max)
- High sustaining voltage output: 25 V (min)
- Inputs compatible with various types of logic.
- Package type-PG : DIP-16 pin.
- Package type-FG : SOP-16 pin.

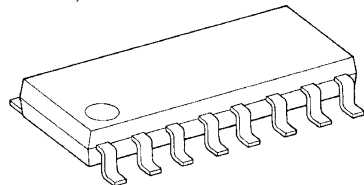
TYPE	INPUT BASE RESISTOR	DESIGNATION
TD62101PG / FG	External	General Purpose
TD62103PG / FG	2.7k Ω	TTL, 5 V CMOS
TD62104PG / FG	10.5k Ω	6~15 V CMOS, PMOS
TD62105PG / FG	20k Ω	12~25 V CMOS, PMOS

TD62101PG, TD62103PG
TD62104PG, TD62105PG



DIP16-P-300-2.54A

TD62101FG, TD62103FG
TD62104FG, TD62105FG



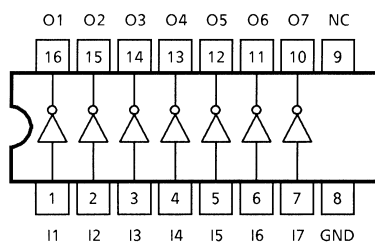
SOP16-P-225-1.27

Weight

DIP16-P-300-2.54A: 1.11 g (typ.)

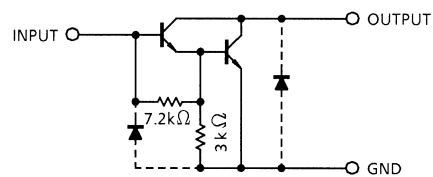
SOP16-P-225-1.27: 0.16 g (typ.)

Pin Connection (top view)



Schematics (each driver)

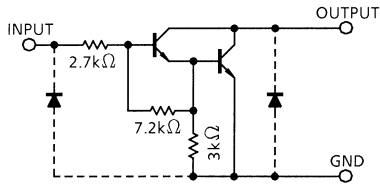
TD62101PG / FG



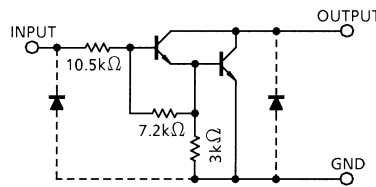
Note: The input and output parasitic diodes cannot be used as clamp diodes.

Schematics (each driver)

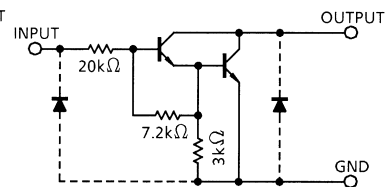
TD62103PG / FG



TD62104PG / FG



TD62105PG / FG



Note: The input and output parasitic diodes cannot be used as clamp diodes.

Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTICS		SYMBOL	RATING	UNIT
Output Sustaining Voltage		$V_{CE(SUS)}$	-0.5~25	V
Output Current		I_{OUT}	500	mA / ch
Input Voltage		V_{IN} (Note 1)	-0.5~30	V
Input Current		I_{IN} (Note 2)	25	mA
Power Dissipation	PG	P_D	1.0	W
	FG		0.625 (Note 3)	
Operating Temperature	PG	T_{opr}	-30~75	°C
	FG		-40~85	
Storage Temperature		T_{stg}	-55~150	°C

Note 1: Except TD62101PG / FG

Note 2: Only TD62101PG / FG

Note 3: On Glass Epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

Recommended Operating Conditions (Ta = -40~85°C and Ta = -30~75°C for only Type-P)

CHARACTERISTIC		SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Output Sustaining Voltage		$V_{CE(SUS)}$		0	—	25	V
Output Current		I_{OUT}	DC 1 Circuit	0	—	350	mA / ch
			$T_{pw} = 25 \text{ ms}$, Duty = 10% 7 Circuits, Ta = 85°C, Tj = 120°C	0	—	300	
Input Voltage	Except TD62101PG / FG	V_{IN}		0	—	20	V
Input Current	Only TD62101PG / FG	I_{IN}		—	—	10	mA
Power Dissipation	PG	P_D		—	—	0.44	W
	FG		(Note)	—	—	0.325	

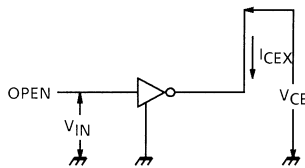
Note: On Glass Epoxy PCB (30 × 30 × 1.6 mm Cu 50%)

Electrical Characteristics (Ta = 25°C)

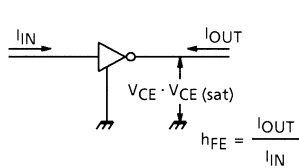
CHARACTERISTIC			SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Output Leakage Current		PG	I _{CEX}	1	V _{CE} = 25 V I _{IN} = 0	Ta = 75°C	—	—	100	μA
		FG				Ta = 85°C	—	—	100	
Collector–Emitter Saturation Voltage			V _{CE (sat)}	2	I _{OUT} = 350 mA, I _{IN} = 600 μA	—	1.3	2.2	V	
					I _{OUT} = 200 mA, I _{IN} = 400 μA	—	1.1	2.0		
					I _{OUT} = 100 mA, I _{IN} = 200 μA	—	1.0	1.8		
DC Current Transfer Ratio			h _{FE}	2	V _{CE} = 2 V, I _{OUT} = 350 mA	1000	—	—		
Input Current	Output On	TD62101PG / FG	I _{IN (ON)}	3	V _{IN} = 1.5 V, I _{OUT} = 350 mA	—	0.25	—	mA	
		V _{IN} = 1.75 V, I _{OUT} = 350 mA			—	1.00	—			
		TD62103PG / FG			V _{IN} = 2.4 V, I _{OUT} = 350 mA	—	0.4	0.7		
		TD62104PG / FG			V _{IN} = 13.5 V, I _{OUT} = 350 mA	—	1.2	1.7		
		TD62105PG / FG			V _{IN} = 20.0 V, I _{OUT} = 350 mA	—	1.0	1.5		
	Output Off	PG	I _{IN (OFF)}	4	I _{OUT} = 500 μA	Ta = 75°C	50	65	—	μA
		FG				Ta = 85°C	50	65		
Input Voltage	Output On	TD62103PG / FG	V _{IN (ON)}	5	V _{CE} = 2 V	I _{OUT} = 125 mA	—	—	2.1	V
		TD62104PG / FG					—	—	4	
		TD62105PG / FG					—	—	6.4	
		TD62103PG / FG				I _{OUT} = 250 mA	—	—	2.7	
		TD62104PG / FG					—	—	7	
		TD62105PG / FG					—	—	12	
		TD62103PG / FG				I _{OUT} = 350 mA	—	—	3.3	
		TD62104PG / FG					—	—	8.8	
		TD62105PG / FG					—	—	15	
Input Capacitance			C _{IN}	6	V _{IN} = 0, f = 1 MHz	—	15	—	pF	
Turn–On Delay			t _{ON}	7	V _{OUT} = 25 V, R _L = 70 Ω C _L = 15 pF	—	0.1	—	μs	
Turn–Off Delay			t _{OFF}			—	0.2	—		

Test Circuit

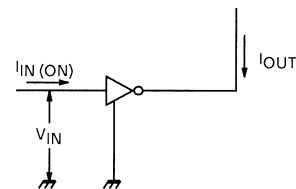
1. I_{CEX}



2. h_{FE} , $V_{CE}(\text{sat})$

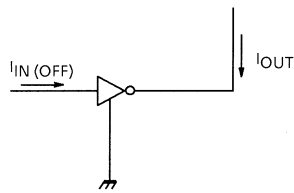


3. $I_{IN}(\text{ON})$

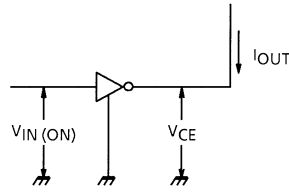


Test Circuit

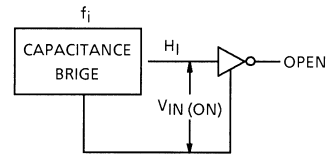
4. I_{IN} (OFF)



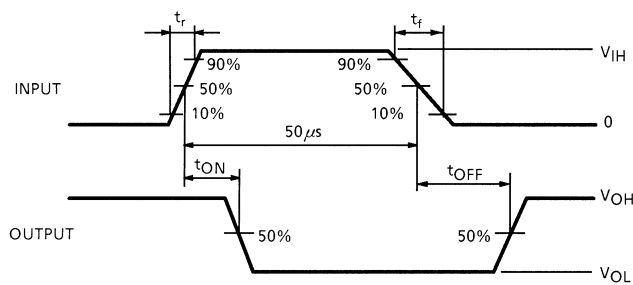
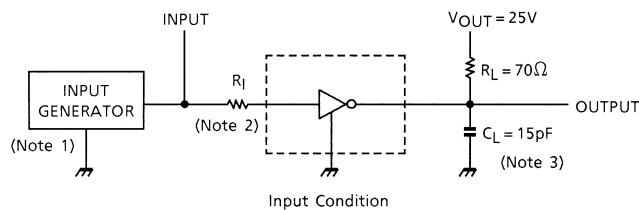
5. V_{IN} (ON)



6. C_{IN}



7. t_{ON} , t_{OFF}



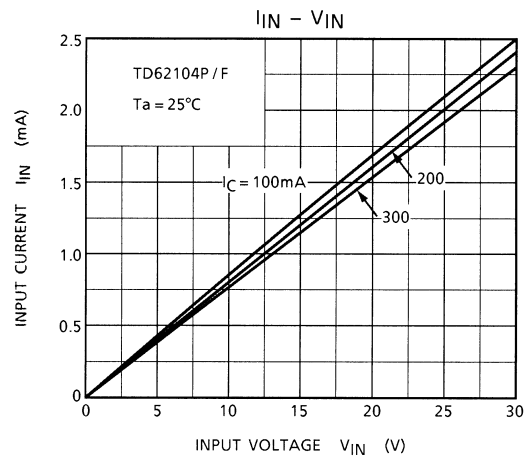
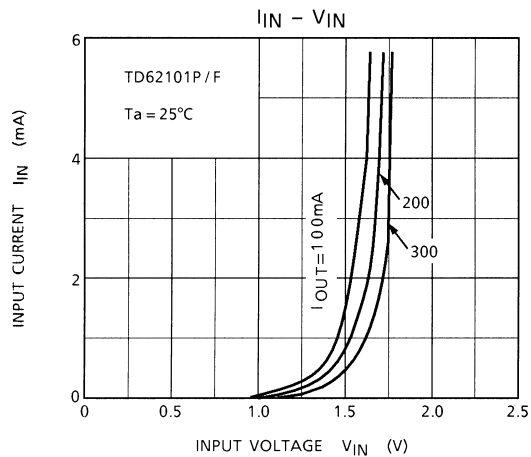
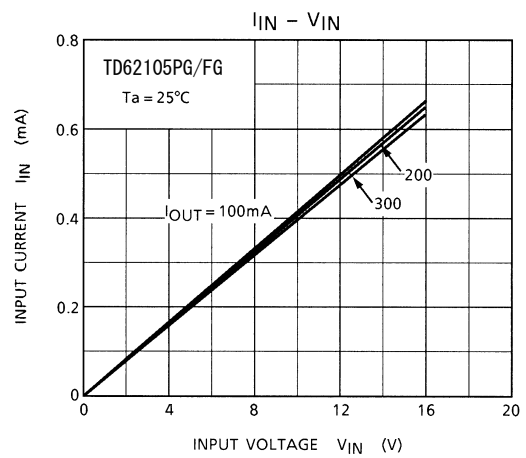
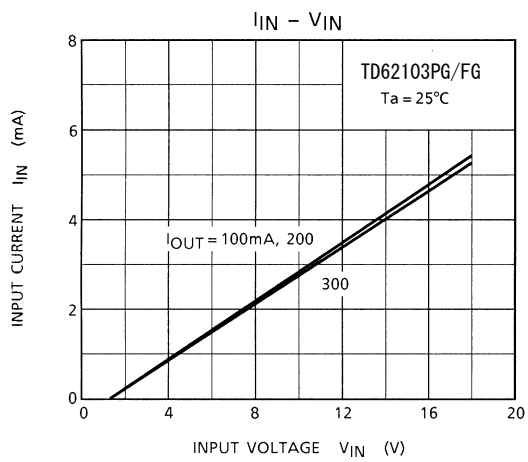
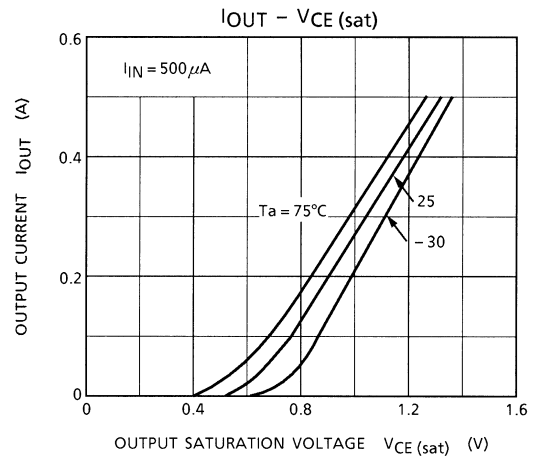
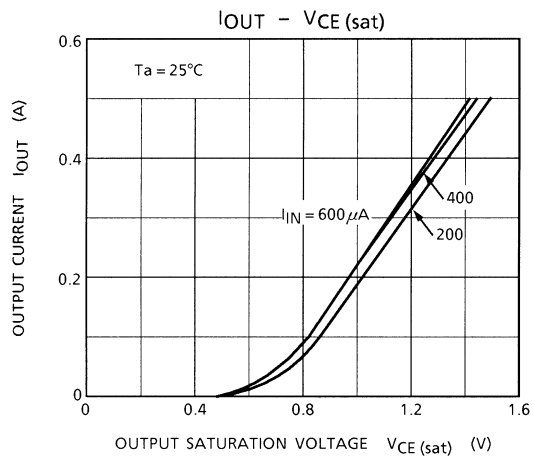
Note 1: Pulse Width 50 μ s, Duty Cycle 10%
Output Impedance 50 Ω , $t_r \leq 5$ ns, $t_f \leq 10$ ns
Note 2: See right.
Note 3: C_L includes probe and jig capacitance.

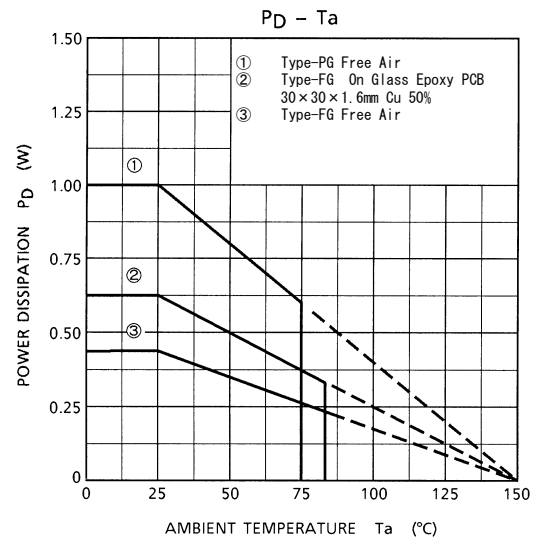
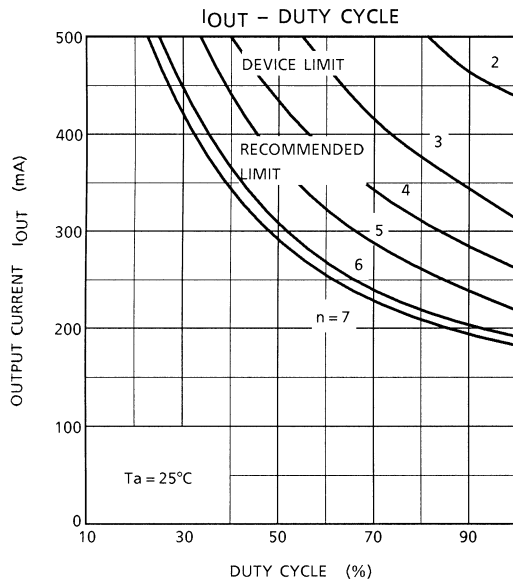
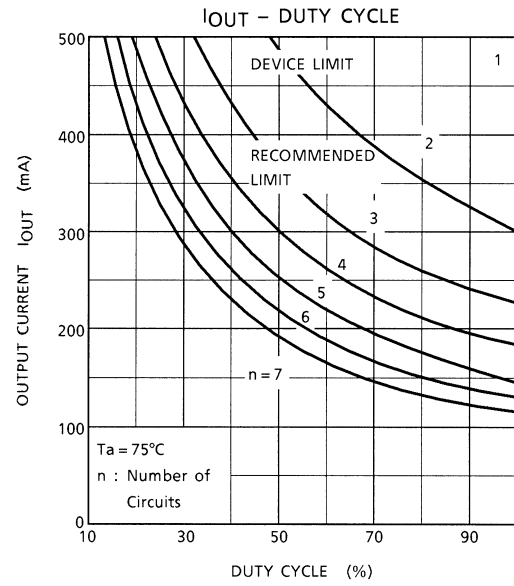
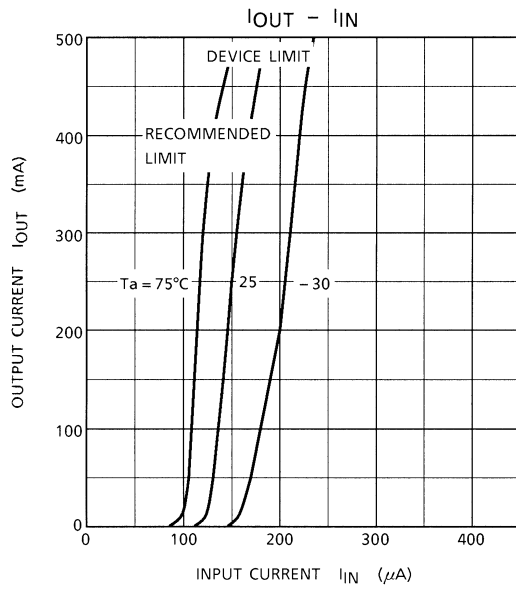
INPUT CONDITION

TYPE NUMBER	R_I	V_{IH}
TD62101PG / FG	2.7 k Ω	3 V
TD62103PG / FG	0 Ω	3 V
TD62104PG / FG	0 Ω	8 V
TD62105PG / FG	0 Ω	15 V

Precautions for Using

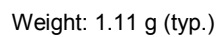
This IC does not include built-in protection circuits for excess current or overvoltage.
If this IC is subjected to excess current or overvoltage, it may be destroyed.
Hence, the utmost care must be taken when systems which incorporate this IC are designed.
Utmost care is necessary in the design of the output line, GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.





DIP16-P-300-2.54A

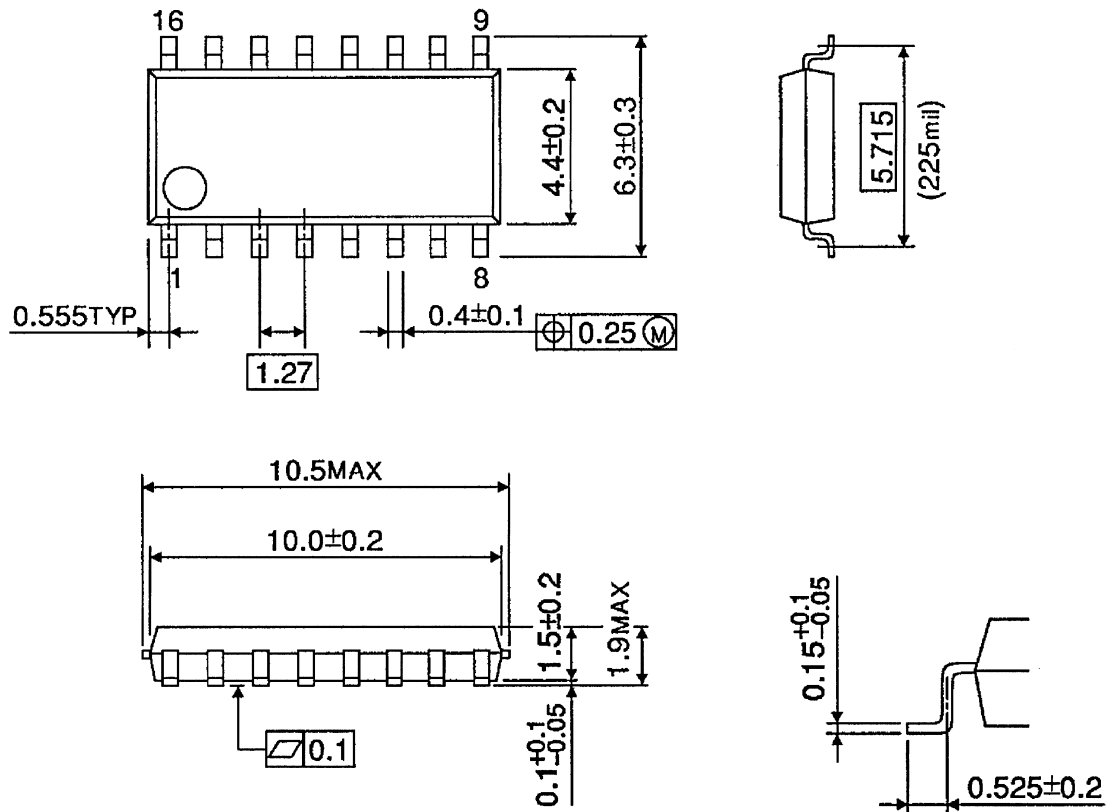
Unit: mm



Package Dimensions

SOP16-P-225-1.27

Unit: mm



Weight: 0.16 g (typ.)

Notes on Contents

1. Equivalent Circuits

The equivalent circuit diagrams may be simplified or some parts of them may be omitted for explanatory purposes.

2. Test Circuits

Components in the test circuits are used only to obtain and confirm the device characteristics. These components and circuits are not guaranteed to prevent malfunction or failure from occurring in the application equipment.

IC Usage Considerations

Notes on Handling of ICs

- (1) The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.
Exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
- (2) Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- (3) If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. IC breakdown may cause injury, smoke or ignition.
Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- (4) Do not insert devices in the wrong orientation or incorrectly.
Make sure that the positive and negative terminals of power supplies are connected properly. Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
In addition, do not use any device that is applied the current with inserting in the wrong orientation or incorrectly even just one time.
- (5) Carefully select external components (such as inputs and negative feedback capacitors) and load components (such as speakers), for example, power amp and regulator.
If there is a large amount of leakage current such as input or negative feedback condenser, the IC output DC voltage will increase. If this output voltage is connected to a speaker with low input withstand voltage, overcurrent or IC failure can cause smoke or ignition. (The over current can cause smoke or ignition from the IC itself.) In particular, please pay attention when using a Bridge Tied Load (BTL) connection type IC that inputs output DC voltage to a speaker directly.

Points to Remember on Handling of ICs**(1) Heat Radiation Design**

In using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (T_j) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into consideration the effect of IC heat radiation with peripheral components.

(2) Back-EMF

When a motor rotates in the reverse direction, stops or slows down abruptly, a current flows back to the motor's power supply due to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond absolute maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.

About solderability, following conditions were confirmed

- Solderability
 - (1) Use of Sn-37Pb solder Bath
 - solder bath temperature = 230°C
 - dipping time = 5 seconds
 - the number of times = once
 - use of R-type flux
 - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
 - solder bath temperature = 245°C
 - dipping time = 5 seconds
 - the number of times = once
 - use of R-type flux

RESTRICTIONS ON PRODUCT USE

060116EBA

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In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc. 021023_A
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