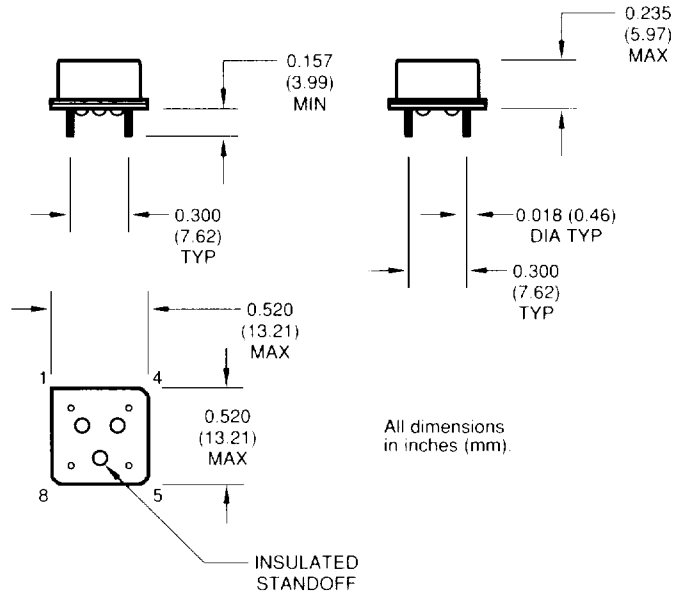


Half-Size HCMOS /TTL Compatible Oscillators



Part Marking and Numbering	MH	1	3	F	A	D
Product Series	MH					
Temperature Range	1: 0°C to +70°C		2: -40°C to +85°C		3: -10°C to +85°C	
	5: -10°C to +85°C		6: -20°C to +70°C		7: 0°C to +85°C	
Stability	1: ± 1000 ppm		2: ± 500 ppm		3: ± 100 ppm	
	5: ± 35 ppm		6: ± 25 ppm		7: ± 0/-200 ppm	
Output Type	F: Fixed E: Enable/Disable T: Tristate					
Symmetry/Logic Compatibility	A: 40/60		B: 45/55 TTL		C: 45/55 CMOS	
			D: 45/55 both			
Package/Lead Configurations	D: DIP; Nickel Header					
	G: Gull Wing; Nickel Header					

Pin Connections

PIN	FUNCTION
1	N/C, Enable/Disable or Tristate
4	Circuit/Case Ground
5	Output
8	+V _{dd}

ENABLE/DISABLE

Pin 1 high or floating: clock signal output
Pin 1 low: output disables to logic "1"

TRISTATE

Pin 1 high or floating: clock signal output
Pin 1 low: output disables to high impedance

Available Stabilities vs Temperature

T \ S	1	2	3	4	5	6	7
1	A	A	S	A	A	A	A
2	A	A	A	A	A	A	A
5	A	A	A	A	A	A	A
6	A	A	A	A	A	A	A
7	A	A	A	A	A	A	A

A = AVAILABLE S = STANDARD

Electrical Specifications

(Standard Operating Conditions 0°C to +70°C; V_{dd} = 5.0 ±10% V DC)

PARAMETERS	TTL LOAD		HCMOS LOAD		UNITS
	MIN.	MAX.	MIN.	MAX.	
Frequency Range	1.000	67.000	1.000	67.000	MHz
Output Load		10 TTL ¹		50 pF ²	
Symmetry*	40/60	60/40	40/60	60/40	%
Logic "0" Level		0.5		.1*V _{dd}	V
Logic "1" Level	V _{dd} -.5		.9*V _{dd}		V
Rise / Fall Time**		5		15	ns
Supply Current					
(1.000 to 40.000 MHz)		30		40	mA
(40.000 to 70.000 MHz)		55		60	mA

¹ See load circuit #1 on page 32; ² See load circuit #2 on page 32.

*Symmetry is measured at 1.4 V with TTL load, and at V_{dd}/2 with HCMOS load.

**Rise/fall times are measured between 0.5V and 2.4V with TTL load, and between 10% V_{dd} and 90% V_{dd} with HCMOS load.

Available Symmetry

FREQ. RANGE	STD.	OPTIONS
1.000 to 40.000 MHz	A	B, C, D
40.100 to 67.000 MHz	A	consult factory

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