

OPERATIONAL AMPLIFIER 173/173-1 **HIGH POWER DUAL**

M.S.KENNEDY CORP.

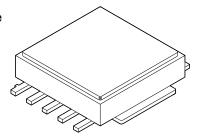
4707 Dey Road Liverpool, N.Y. 13088

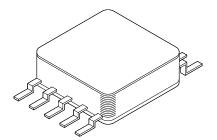
(315) 701-6751

MIL-PRF-38534

FEATURES:

- Extremely Compact Surface Mount Package
- Low Cost Dual High Power Amplifier
- Wide Supply Voltage Range: 5V to 40V
- High Output Current: 2A
- High Efficiency: |Vs-2.2V| at 2A
- Internal Current Limit
- Wide Common Mode Range (Includes Negative Supply Voltage)
- Low Distortion

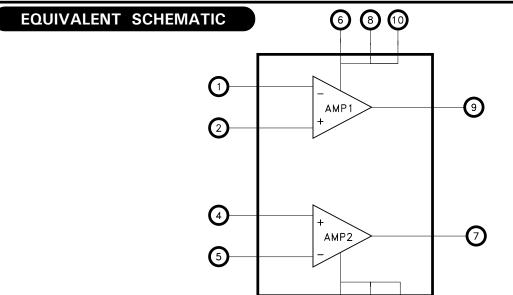




MSK173-1

MSK173 **DESCRIPTION:**

The MSK 173 is a high power dual operational amplifier. Each amplifier is capable of delivering two amps of current to the load. The MSK 173 is an excellent low cost alternative for bridge mode configurations since both amplifiers are packaged together and will track thermally. The wide common mode range includes the negative rail, facilitating single supply applications. It is possible to have a "ground based" input driving a single supply amplifier with ground acting as the second or "bottom" supply of the amplifier. The output stage is current limit protected to approximately 4.0 amps. The MSK 173 is packaged in an extremely space efficient 10 pin power SOIC package. The MSK 173-1 is packaged in a 10 pin flatpack. Consult the factory for other packaging options if desired.



All power pins must be electrically connected for proper operation.

TYPICAL APPLICATIONS

- Half and Full Bridge Motor Drives
- **Audio Power Amplifiers**
- Headphones
- Cellular Phones, PCMCIA, PDA
- Ideal for Single Supply Systems
 - 5V Peripheral
 - 12V Automotive
 - 28V Avionic

PIN-OUT INFORMATION

S = Heat Sink = Lid

1 -Input 1

(3)

1

+Input 1

3 -Vcc

+Input 2

-Input 2

10 +Vcc

Output 1

+Vcc

Output 2

+Vcc

HEAT SINK

The heat sink of the package and the lid are electrically connected to -Vcc. The heat sink tab must be connected to the system -Vcc.

ABSOLUTE MAXIMUM RATINGS

| V_{cc} | Total Supply Voltage 40V | T_{ST} | Storage Temperature65°C to +150°C |
|----------------|------------------------------------|----------|-----------------------------------|
| ± І оит | | | Lead Temperature 300°C |
| V_{IND} | Input Voltage (Differential) ± Vcc | Tc | Case Operating Temperature |
| VIN | Input Voltage | | (MSK173B,173B-14)55°C to + 125°C |
| | (Common Mode) + Vcc, -Vcc-0.5V | | (MSK173,173-1)40°C to +85°C |
| ΤJ | Junction Temperature 150°C | R_{TH} | Thermal Resistance (DC) |
| | | | Junction to Case |
| | | | MSK 173 |
| ELE | CTRICAL SPECIFICATIONS | | MSK 173-1 12.0°C/W |

ELECTRICAL SPECIFICATIONS

| Parameter | Test Conditions (1) | Group A | MSK173B MSK173B-1 | | MSK173 MSK173-1 | | | | |
|----------------------------------|---|----------|----------------------|-----------|--------------------|------|-----------|-------|-------|
| | | Subgroup | Min. | Тур. | Max. | Min. | Тур. | Max. | Units |
| STATIC | | | | | | | | | |
| Supply Voltage Range (2) | (Split Supply) | - | ±2.5 | ±15 | ± 20 | ±2.5 | ±15 | ±20 | V |
| | Total; VIN=0V | 1 | - | ±35 | ±50 | - | ± 35 | ±60 | mA |
| Quiescent Current | | 2 | 1 | ±50 | ± 75 | 1 | - | - | mA |
| | | 3 | - | ±30 | ±50 | - | - | - | mA |
| INPUT | | | | | | | | | |
| Office Valtage | | 1 | - | ±0.5 | ±10 | - | ± 2 | ±15 | mV |
| Offset Voltage | $V_{IN} = 0V$ | 2, 3 | - | ±2.0 | ± 15 | - | - | - | mV |
| Offset Voltage Drift (2) | VIN = OV | - | - | ± 20 | ±50 | - | ± 20 | - | μV/°C |
| Innut Bing Comment | Vcm = 0V | - | - | ±35 | ±500 | - | ±35 | ±1000 | nA |
| Input Bias Current (2) | Full Temp. | - | - | ± 75 | ±1000 | - | ± 75 | - | nA |
| Power Supply Rejection (2) | $\Delta Vcc = \pm 15V$ | - | 60 | 80 | - | 60 | 80 | - | dB |
| Common Mode Rejection (2) | Vcm = ± 10VDC | - | 60 | 85 | - | 60 | 85 | - | dB |
| Total Noise | $RL = 500\Omega$ $Av = 1$ $CL = 1500pF$ | - | - | 0.1 | 1.0 | - | 0.1 | 1.0 | mV |
| OUTPUT | | | | | | | | | |
| Output Voltage Swing | | 4 | ±14 | ±14.2 | - | ±14 | ±14.2 | - | V |
| Output Current Peak (2) | Vout = MAX | - | ±2.0 | ± 3.0 | - | ±2.0 | ± 3.0 | - | Α |
| Current Limit ② | | - | - | ±4.0 | - | - | ±4.0 | - | Α |
| Power Bandwidth (2) VOUT = 28VPP | | - | - | 13.6 | - | - | 13.6 | - | KHz |
| Crosstalk Iout = 1A f = 1KHz | | - | 60 | 68 | - | - | 68 | - | dB |
| Capacitive Load 2 | Av = + 1V/V | - | - | 0.22 | - | - | 0.22 | - | μF |
| TRANSFER CHARACTERISTICS | | | | | | | | | |
| Slew Rate | | 4 | 0.5 | 1.2 | - | 0.5 | 1.2 | - | V/μS |
| Open Loop Voltage Gain (2) | $F = 10Hz$ $RL = 500\Omega$ | - | 80 | 100 | - | 80 | 100 | - | dB |

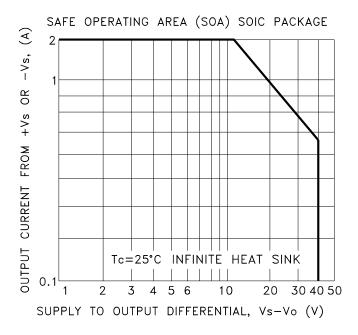
NOTES:

2

Unless otherwise noted ±Vcc = ±15VDC.
 Devices shall be capable of meeting the parameter, but need not be tested. Typical parameters are for reference only.
 Industrial grade devices shall be tested to subgroups 1 and 4 unless otherwise requested.
 Military grade devices ('B' suffix) shall be 100% tested to subgroups 1,2,3 and 4. Consult factory for availability of military grade devices.
 Subgroup 5 and 6 testing available upon request.
 Subgroup 1,4 Tc = +25°C Subgroup 2,5 Tc = +125°C Subgroup 3,6 TA = -55°C

APPLICATION NOTES

SAFE OPERATING AREA (SOA)



Safe operating area curves are a graphical representation of all of the power limiting factors involved in the output stage of an operational amplifier. Three major power limiting factors are; output transistor wire bond carrying capability, output transistor junction temperature and secondary breakdown effects. To see if your application is meeting or exceeding the limitations of the safe operating area curves, perform the following steps:

- 1. Find the worst case output power dissipation. For a split supply, purely resistive load application, this occurs when $Vout = 1/2 \ Vcc.$
- 2. Take the values of (Vcc-Vout) and the corresponding output current and find their intersection on the safe operating area curves.
- 3. Verify this point is below the safe operating area curves.

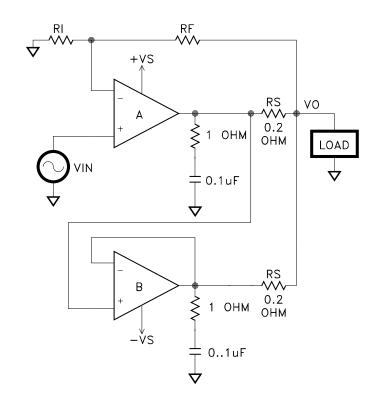
This is a simple task for purely resistive loads, for reactive loads the following table will save extensive analysis. Under transient conditions, capacitive and inductive loads up to the following maximum are usually safe.

| ± Vcc | Capacitive Load | Inductive Load | | | |
|-------|-----------------|----------------|--|--|--|
| 20V | 200uF | 7.5mH | | | |
| 15V | 500uF | 25mH | | | |
| 10V | 5mF | 35mH | | | |
| 5V | 50mF | 150mH | | | |

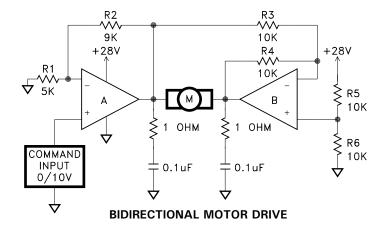
If the inductive load is driven near steady state conditions allowing the output to drop more than 6V below the supply rail while the amplifier is current limiting, the inductor should be capacitively coupled or the supply voltage must be lowered to meet the SOA criteria. It is a good practice to also connect reverse biased fast recovery diodes to the output for protection against sustained high energy flyback.

AMPLIFIER STABILITY

Since both output transistors in this amplifier are NPN, consideration must be taken when stabilizing the output. A one ohm resistor, 0.1uF capacitor snubber network should be added externally from the output to -Vcc on each amplifier. This configuration minimizes local output stage oscillations. As always, adequate power supply bypassing is a necessity for amplifier stability. A parallel combination of a 4.7uF electrolytic (for every amp of output current) and a 0.01uF ceramic disc capacitor should be connected as close as possible to the package power supply pins to ground.



PARALLEL CONNECTION yields single 4A amplifier



Rev. H 8/00

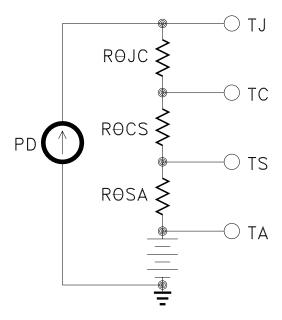
3

APPLICATION NOTES CONTINUED

HEAT SINKING

To determine if a heat sink is necessary for your application and if so, what type, refer to the thermal model and governing equation below.

Thermal Model:



Governing Equation:

 $TJ = PD x (R\theta JC + R\theta CS + R\theta SA) + TA$

Where

TJ = Junction TemperaturePD = Total Power Dissipation

 $R_{\theta JC} = Junction to Case Thermal Resistance$ $R_{\theta CS} = Case to Heat Sink Thermal Resistance$ $R_{\theta SA} = Heat Sink to Ambient Thermal Resistance$

TC = Case Temperature
TA = Ambient Temperature
TS = Sink Temperature

DEVICE SOLDERING

The MSK 173 is a highly thermally conductive device and the thermal path from the package to the internal junctions is very short. Standard surface mount techniques should be used when soldering the device into a circuit board. A hole can be cut in the printed circuit board to allow the heat sink of the package to be thermally bonded to an external heat sink for high power applications.

Example:

In our example the amplifier application requires each output to drive a 10 volt peak sine wave across a 20 ohm load for 0.5 amp of output current. For a worst case analysis we will treat the 0.5 amp peak output current as a D.C. output current. The power supplies are $\pm\,12\text{VDC}.$ The device is the SOIC version.

1.) Find Driver Power Dissipation

PD = [(quiescent current) x (+Vcc - (-Vcc))] + [(Vcc-Vo) x IouT] = (75mA) x (24V) + (2V) x (0.5A) + (2V) x (0.5A) = 1.8W + 2W = 3.8W

- 2.) For conservative design, set $T_J = +150$ °C.
- 3.) For this example, worst case TA = +25 °C
- 4.) R θ JC = 3.0 °C/W
- 5.) Rearrange governing equation to solve for R θ SA R θ SA = ((TJ TA)/PD) (R θ JC) (R θ CS) = ((150°C 25°C) / 3.8W) (3°C/W)

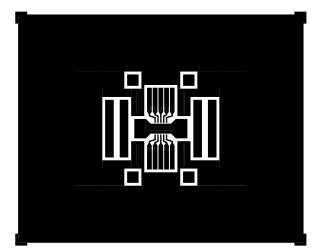
≅ 29.9°C/W

The heat sink in this example must have a thermal resistance of no more than 30°C/W to maintain a junction temperature of no more than $+150^{\circ}\text{C}$.

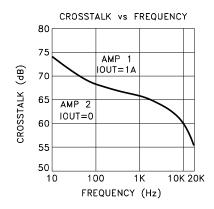
CASE CONNECTION

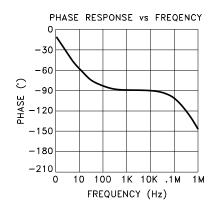
The heat sink and lid of the MSK 173 are electrically connected to the negative power supply rail. The user is urged to keep this in mind when designing the printed circuit card the MSK 173 will be placed in. There should be no printed circuit traces making contact with the case of the device except for -Vcc. The -Vcc plane or ground for single supply systems, can be used to pull heat away from the device and must be connected electrically to the heat sink.

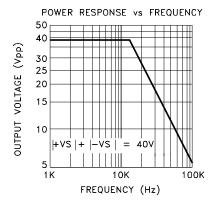
UNIVERSAL EVALUATION PC BOARD LAYOUT

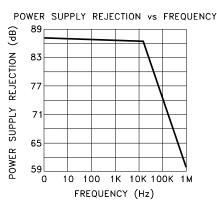


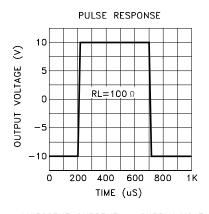
TYPICAL PERFORMANCE CURVES

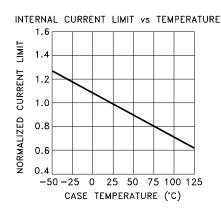


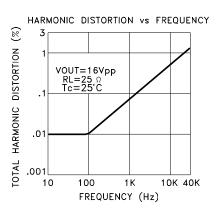


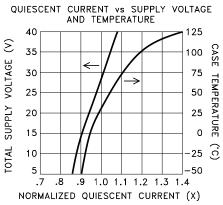


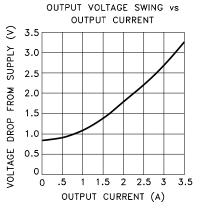


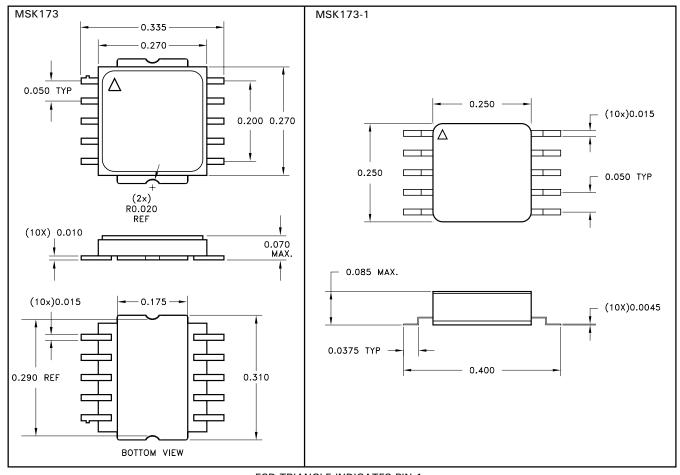












 ${\hbox{\footnotesize ESD TRIANGLE INDICATES PIN 1.}} \\ {\hbox{\footnotesize ALL DIMENSIONS ARE $\pm 0.010 INCHES UNLESS OTHERWISE LABELED.}} \\ {\hbox{\footnotesize CANNOT STATE PIN TO STATE PIN T$

ORDERING INFORMATION

| Part Number | Screening Level | Package Style | θJC (°C/W) |
|----------------|-------------------------|-------------------|------------|
| MSK173 | Industrial/Non-Hermetic | 10 Pin Power SOIC | 3.0 |
| MSK173B | Military-Mil-PRF-38534 | 10 Pin Power SOIC | 3.0 |
| MSK173-1 | Industrial/Non-Hermetic | 10 Pin Flatpack | 12 |
| MSK173B-1 | Military-Mil-PRF-38534 | 10 Pin Flatpack | 12 |

M.S. Kennedy Corp.
4707 Dey Road, Liverpool, New York 13088
Phone (315) 701-6751
FAX (315) 701-6752
www.mskennedy.com

The information contained herein is believed to be accurate at the time of printing. MSK reserves the right to make changes to its products or specifications without notice, however, and assumes no liability for the use of its products.