

SWITCHING **N-CHANNEL POWER MOS FET**

DESCRIPTION

NEC

The 2SK4080 is N-channel MOS FET device that features a low on-state resistance and excellent switching characteristics, and designed for low voltage high current applications such as DC/DC converter with synchronous rectifier.

FEATURES

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Low on-state resistance

 $R_{DS(on)1} = 9.0 \text{ m}\Omega \text{ MAX.}$ (Vgs = 10 V, ID = 24 A)

- Low QGD: QGD = 6.3 nC TYP.
- 4.5 V drive available

ORDERING INFORMATION

PART NUMBER	PACKAGE	
2SK4080(1)-S27-AY Note	TO-251 (MP-3-b)	
2SK4080-ZK-E1-AY Note	TO-252 (MP-3ZK)	
2SK4080-ZK-E2-AY Note	TO-252 (MP-3ZK)	

Note Pb-free (This product does not contain Pb in external electrode.)

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Drain to Source Voltage (VGS = 0 V)	VDSS	30	V
Gate to Source Voltage (VDs = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	D(DC)	±48	А
Drain Current (pulse) Note1	D(pulse)	±144	А
Total Power Dissipation (Tc = 25° C)	P _{T1}	29	W
Total Power Dissipation	P _{T2}	1.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C
Single Avalanche Current Note2	las	21	А
Single Avalanche Energy Note2	Eas	44.1	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Starting T_{ch} = 25°C, V_{DD} = 15 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V, L = 100 μ H

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The mark <R> shows major revised points.

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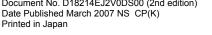
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

(TO-251)



(TO-252)



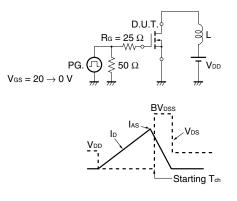


ELECTRICAL CHARACTERISTICS (TA = 25°C)

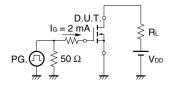
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	loss	V _{DS} = 30 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	v
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 12 A	7	14		S
Drain to Source On-state Resistance ^{Note}	RDS(on)1	V _{GS} = 10 V, I _D = 24 A		7.0	9.0	mΩ
	RDS(on)2	V _{GS} = 4.5 V, I _D = 24 A		10.2	15	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		1670		pF
Output Capacitance	Coss	V _{GS} = 0 V		290		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		150		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 15 V		10		ns
Rise Time	tr	I _D = 30 A		5.3		ns
Turn-off Delay Time	t _{d(off)}	V _{GS} = 12 V		42		ns
Fall Time	tr	R _G = 3 Ω		6.1		ns
Total Gate Charge	Q _{G1}	V_{DD} = 15 V, V_{GS} = 12 V, I_D = 30 A		32		nC
	Q _{G2}	V_{DD} = 15 V, V_{GS} = 4.5 V, I_D = 30 A		13		nC
Gate to Source Charge	Q _{GS}	V _{DD} = 15 V		4.6		nC
Gate to Drain Charge	Qgd	I⊳ = 30 A		6.3		nC
Gate Resistance	Rg			2.4		Ω
Body Diode Forward Voltage Note	VF(S-D)	IF = 30 A, VGS = 0 V		0.94	1.5	V
Reverse Recovery Time	trr	IF = 30 A, VGS = 0 V		29		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		23		nC

Note Pulsed

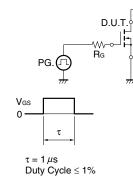
TEST CIRCUIT 1 AVALANCHE CAPABILITY

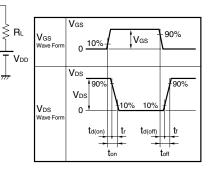


TEST CIRCUIT 3 GATE CHARGE

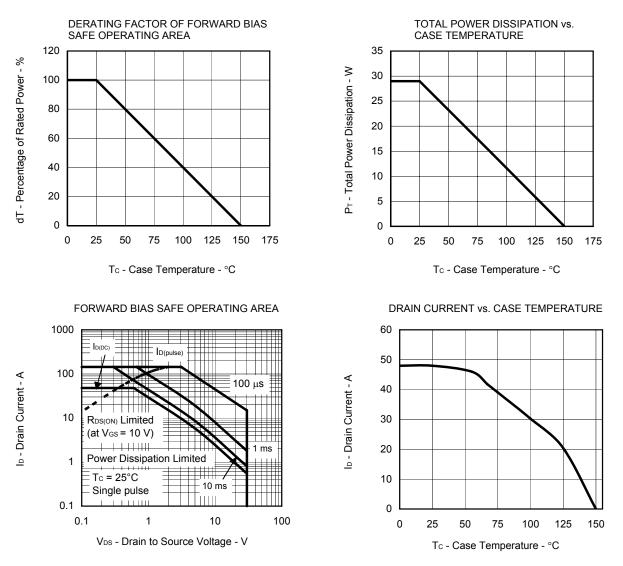


TEST CIRCUIT 2 SWITCHING TIME

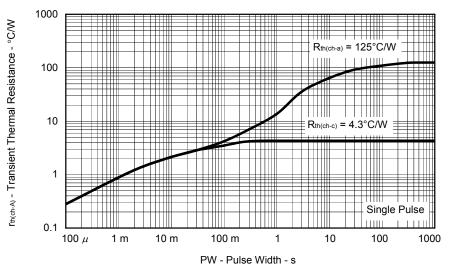




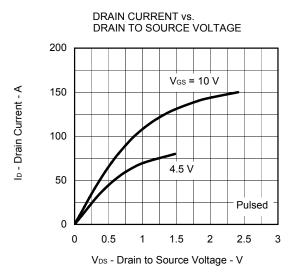
TYPICAL CHARACTERISTICS (T_A = 25°C)



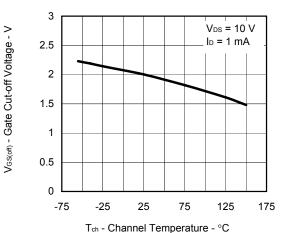


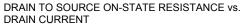


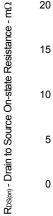
Data Sheet D18214EJ2V0DS

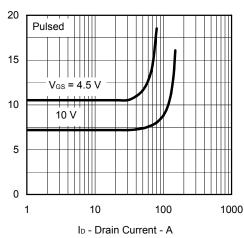




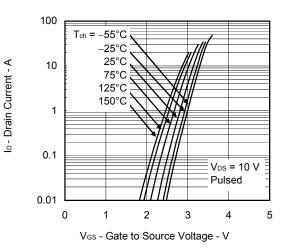




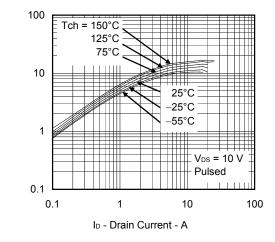




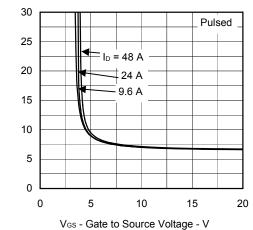
FORWARD TRANSFER CHARACTERISTICS



FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



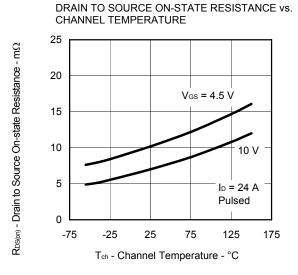
DRAIN TO SOURCE ON-STATERESISTANCE vs. GATE TO SOURCE VOLTAGE



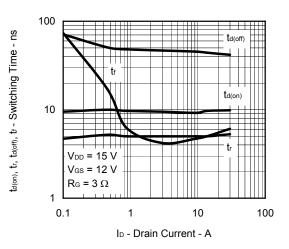
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| yfs | - Forward Transfer Admittance -

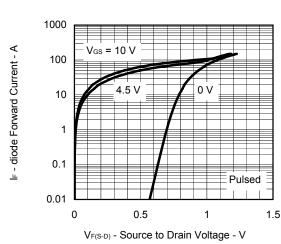
 $R_{DS(on)}$ - Drain to Source On-state Resistance - m Ω

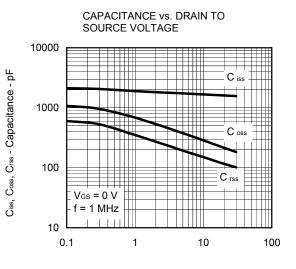


SWITCHING CHARACTERISTICS



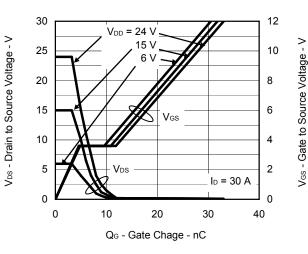
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



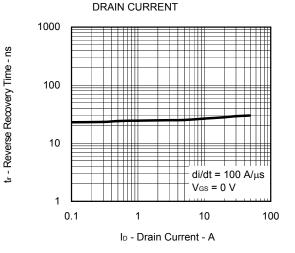


VDS - Drain to Source Voltage - V

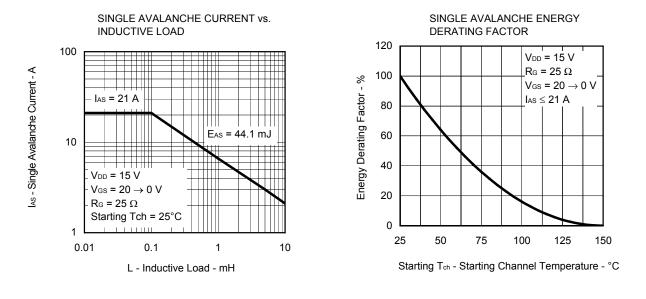
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



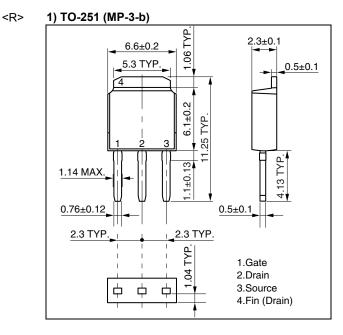


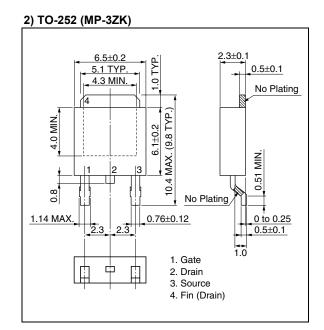


Data Sheet D18214EJ2V0DS

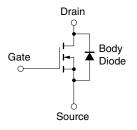


PACKAGE DRAWINGS (Unit: mm)





EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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