

2SK1330, 2SK1330A

Silicon N-Channel Power F-MOS FET

■ Features

- Low $R_{DS(on)} = 1.3\Omega$ (typ.)
- High speed switching $t_f = 120\text{ns}$ (typ.)
- Secondary breakdown free
- High breakdown voltage, high power

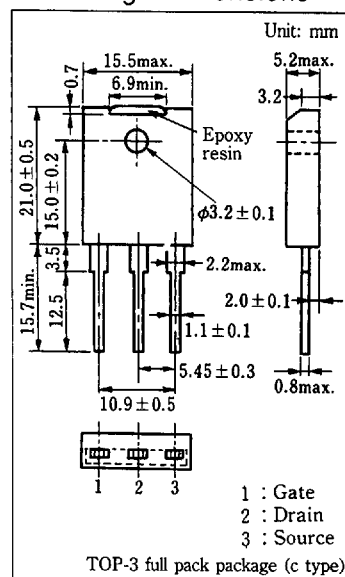
■ Use

- Non-contact relay.
- Motor control.
- Measuring Equipment.
- Switching regulator.
- Solenoid drive.

■ Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$)

Item	Symbol	Value	Unit
Drain-source voltage	V_{DS}	800	V
		900	
Gate-source voltage	V_{GS}	± 20	V
Drain current	DC	I_D	A
	Peak-to-peak value	I_{DP}	
Power dissipation	$T_c = 25^\circ\text{C}$	P_D	W
	$T_a = 25^\circ\text{C}$		
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	$-55 \sim +150$	$^\circ\text{C}$

■ Package Dimensions



■ Electrical Characteristics ($T_c = 25^\circ\text{C}$)

Item	Symbol	Condition	min.	typ.	max.	Unit
Drain current	I_{DS}	$V_{DS} = 640\text{V}$, $V_{GS} = 0$			0.1	mA
Gate-source current	I_{GS}	$V_{GS} = \pm 20\text{V}$, $V_{DS} = 0$			± 1	μA
Drain-source voltage	V_{DS}	$I_D = 1\text{mA}$, $V_{GS} = 0$	800			V
			900			
Gate threshold voltage	V_{th}	$V_{DS} = 25\text{V}$, $I_D = 1\text{mA}$	1		5	V
Drain-source ON resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}$, $I_D = 5\text{A}$	1.3		1.7	Ω
			1.4		1.8	
Forward transfer admittance	$ Y_{fs} $	$V_{DS} = 25\text{V}$, $I_D = 5\text{A}$	2.1	4.0		S
Input capacitance	C_{iss}	$V_{DS} = 20\text{V}$, $V_{GS} = 0$, $f = 1\text{MHz}$		2000		pF
Output capacitance	C_{oss}			280		pF
Reverse transfer capacitance	C_{rss}			120		pF
Turn-on time	t_{on}	$V_{GS} = 10\text{V}$, $I_D = 5\text{A}$ $V_{DD} \approx 200\text{V}$, $R_L = 40\Omega$		110		ns
Fall time	t_f			120		ns
Delay time	$t_d(\text{off})$			300		ns

