

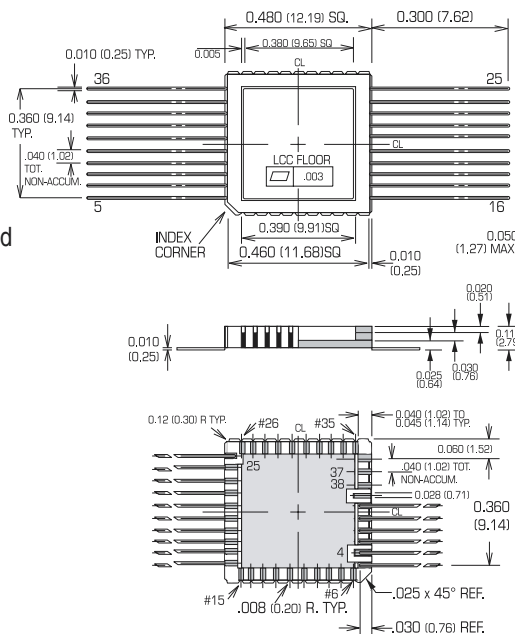
MODEL 068
375 kHz – 65 MHz Oscillator

Surface Mount 20 Lead Flatpack Package
Ceramic Chip Carrier Construction with Bottom Brazed Leads
Ruggedized 4 Point Crystal Mount

068	□	□	□	□	□	□ □ □ □
SERIES NUMBER:	TEMPERATURE STABILITY CODE	ACCURACY @ 25° C	OUTPUT WAVEFORM	OUTPUT ENABLE @ PIN 25	SCREENING	FREQUENCY (MHz)
	1. -55° C to +125° C ± 50 ppm 2. -55° C to +125° C ± 100 ppm 3. -55° C to +105° C ± 40 ppm 4. -55° C to +105° C ± 80 ppm 5. -20° C to +70° C ± 25 ppm 6. -20° C to +70° C ± 50 ppm 7. 0° C to +70° C ± 10 ppm 8. 0° C to +70° C ± 25 ppm 9. -40° C to +85° C ± 30 ppm 0. -40° C to +85° C ± 70 ppm	A: ±15 ppm B: ±25 ppm C: ±10 ppm X: Overall stability incl. in Temp. Stability code. Only avail. with codes 2,4,6,8 and 0.	T: TTL C: CMOS	E: Required X: Not Required	B: MIL-0-55310 Class B X: Corning Verification	

ADDITIONAL PARAMETERS

Input Voltage: + 5 Vdc ± 10%
 Input Current
 375.0 kHz to 4.0 MHz: 10 mA max.
 4.1 to 35.0 MHz: 20 mA max.
 35.1 to 65.0 MHz: 40 mA max.
 Output: CMOS or TTL
 Duty Cycle: CMOS: 40% to 60% @ 50% Vdd
 TTL: 40% to 60% @ 50% Vd
 Start-Up Time:
 375.0 kHz to 4.0 MHz: 10ns
 4.1 kHz to 35.0 MHz: 10ns
 35.1 kHz to 65.0 MHz: 5ns
 Rise & Fall Times for TTL (meas. 0.8 to 2.0 V)
 375.0 kHz to 35.0 MHz: 10ns max.
 35.1 to 65.0 MHz: 5ns max.
 Rise & Fall Times for CMOS (meas. 10% to 90%)
 375.0 kHz to 35.0 MHz: 10ns max.
 35.1 to 65.0 MHz: 5ns max.
 Load
 ACMOS: 15 pF/10k
 TTL: 10 TTL
 Aging: ± 3 ppm max. Year 1;
 ± 2 ppm max./year thereafter
 Storage Temperature: -55° C to +125° C



PIN CONNECTIONS

- 4 – Supply Voltage
- 25 – Enable/Disable or N/C
- 37 – GND
- 39 – Output
- All others are N/C