

CL1L5 / CL2L / CL1L5BT Series

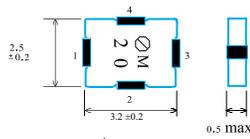
Specification subject to change - Please call factory.

1.0 Description

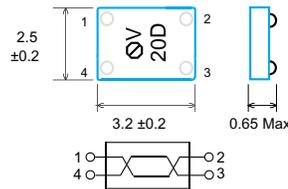
These high frequency, precision thin film chip delay lines are offered in a 1210 form factor, making them the ideal choice for MCM's and other applications with severe size restrictions. Useful for phase locked loop (PLL) circuits, they are tape and reel packaged for automated assembly. Typical applications for the differential chip with 100 ohm differential impedance are transceiver data/clock alignment and physical deskew. The chip-coil is designed in a Pb-free (95.5Sn/3.8Ag/0.7Cu), surface mount format. Ideal for applications with severe size restrictions.

2.0 Mechanical

-Please refer to page 37 for land pattern



CL1L5 - Single



CL2LA - Differential

dimension mm

3.0 Electrical

TYPE	CL1L5 (single)	CL2LA (differential)
Time Delay steps	20 to 200 ps in 20 ps steps	40 to 200 ps in 20 ps
Impedance	50 ±10%	100 ohm ±10%
DC resistance	3 ohm/ns max	1 ohm max
Rise time	< 100 ps	<100 ps
Distortion		±10%
Time delay tolerance		±10 ps
Temp coefficient of Td		±100 ppm/°C
Rated current		100 mA
Insertion loss		<0.5dB @ 1Ghz
Insulation resistance		>100 Mohm @ 50V
Operating temperature		-40 to +85°C

4.0 Part Number

- CL** _____ Product designator
- 1L** _____ Circuit/element qualifier (1 = CL1L), (2 = CL2L)
- 5** _____ Impedance (5 = 50 ohm), (A = 100 ohm)
- 2** _____ Height code (2 = CL1L), (A = CL2L), (B = 200 ps values)
- M** _____ Termination (M = CL1L), (T = CL2L), (T = 200 ps values)
- 010** _____ Time delay (010 = 100 ps)
- S** _____ Serial code (S = CL1L), (D = CL2L)



Manufacturer(s) of the products described on this page are indicated by their respective logos.