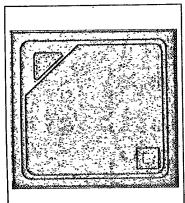
OPTEK OPTEK

T-41-61

NPN Silicon Phototransistor Chip Type OPC60X



ANTIREFLECTIVE COATING A - ACTIVE AREA (BASE) B - EMITER CONTACT (GOLD) OIMENSIONS ARE IN INCHES (MILLIMETERS)

Features

- · 2.7 times the active area of OPC600L
- · More sensitive at low light levels
- Active area is centered on chip

Description

The Optek Technology photosensor chips are fabricated using the latest silicon planar diffused technology and are silicon nitride passivated for long term stability. All photosensors have an antireflective coating over the active area to ensure maximum absorption of irradiated light. Chips can be specially probed to satisfy custom requirements.

Since Optek has no control over the techniques the customer may use to alloy and bond chips, Optek cannot be held responsible for damage to the chips resulting from such techniques.

Packaging Options

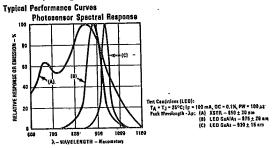
OPC60X Vials
OPC60XTP Sawn on Tape
OPC60XWP Waffle Pack
Special packaging and testing available
upon request.

Absolute Maximum Ratings (1)(TA = 25°C unless otherwise noted)

Storage and Operating Temperature65°C to +150°C	
Collector-Emitter Voltage 30V	
Emitter Collector Voltage 5.0V	
Power Dissipation	
Floatrical Characteristics /T _A = 25°C unless otherwise noted)	

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
V _(BR) CEO	Collector-Emitter Breakdown Voltage	30			V	I _C = 100μA
V _{(BR)ECO}	Emitter-Collector Breakdown Voltage	5.0			V	l _E = 100μA
ICEO	Collector Dark Current			100	nA	$V_{CE} = 10.0V, \Phi = 0\mu W$
Rλ	Responsivity		0.25		A/W	V _{CE} =5.0V,Φ=10μW ⁽³⁾
hFE	Current Gain	50				$V_{CE} = 5.0V_{s}l_{b} = 50\mu A$

Notes: (1) All maximum ratings are determined with the chip mounted on a dimpled TO-18 header using Optek techniques. (2) Maximum power dissipation is a function of the package in which the chip is housed and the environment in which the assembled package will be used. (3) Light source is a GaAs LED, λ_P = 935nm, typical.



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