

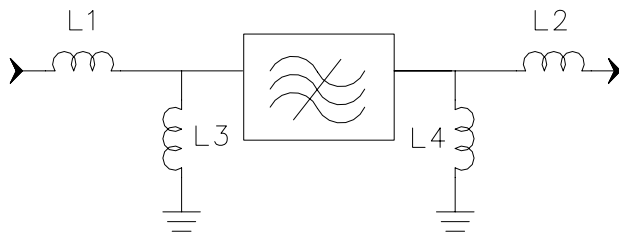
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	108.3	108.4	108.5
Insertion Loss	dB	-	21	23
3 dB Bandwidth	MHz	1.55	1.65	1.75
Selectivity	f0±1.2MHz	dBc	30	-
	f0±1.4MHz	dBc	40	-
	f0±1.8MHz	dBc	50	-
	f0±5.8MHz	dBc	55	-
Passband Variation	dB	-	0.5	1
Ultimate Rejection	dB	50	57	-
Absolute delay	usec	-	3.64	-
Substrate Material			quart	
Ambient Temperature	°C		25	
Package Size		DIP3512 (35.0x12.8x4.7mm ³)		

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

Matching Configuration



L1=100nH L2=56nH
L3=68nH L4=56nH

Source/Load Impedance=50 ohm

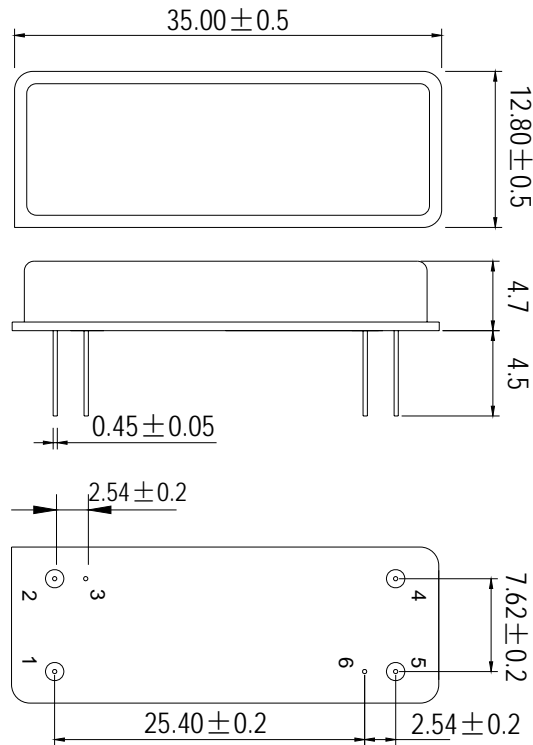
Notes - Component values may change depending on board layout.



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Package Dimension

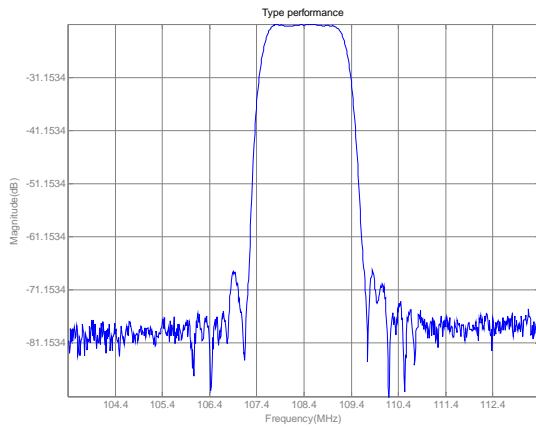


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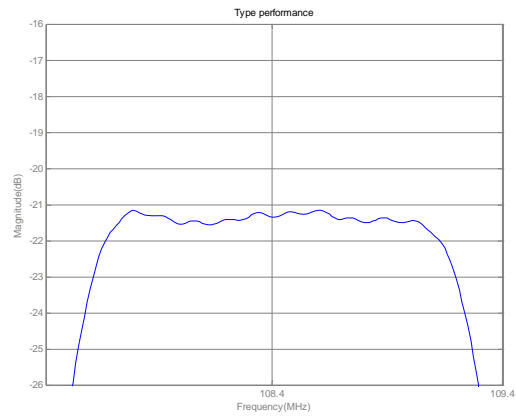
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Typical Performance

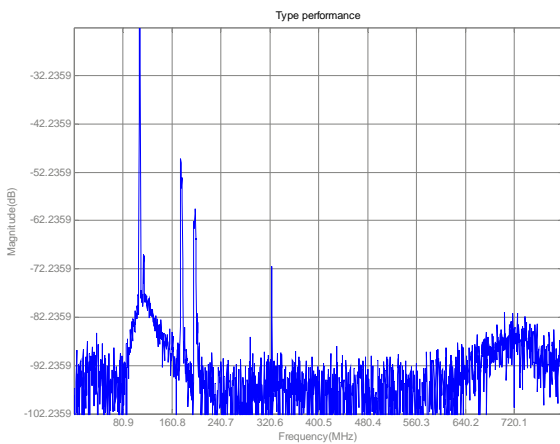
Frequency Response



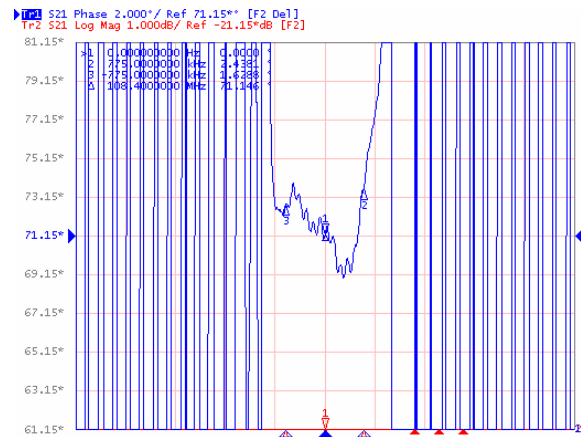
Passband Respond



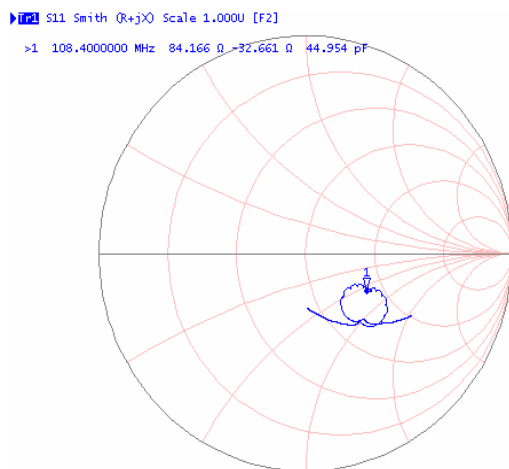
Wideband Response



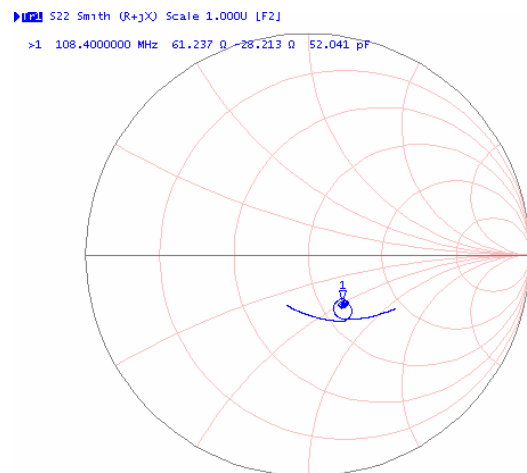
Phase Linearity ($f_0 \pm 775\text{KHz}$)



Smith Chart S11



Smith Chart S22



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Marking

LBS10802



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