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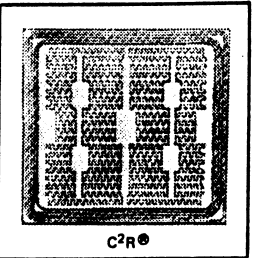
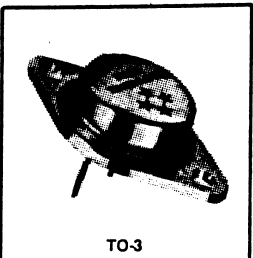
15 Amp NPN
 300, 350, 400V
 2N6653, 54, 55
 XGSR15030, 35, 40

C²R[®] HIGH SPEED/HIGH POWER SWITCHING TRANSISTORS

The XGSR series is an NPN double diffused epitaxial transistor designed for high speed switching systems. This unique series utilizes General Semiconductor Industries' C²R process (patent applied for) which describes a manufacturing technology that provides surface stabilization for high voltage operation and enhances long term reliability. Another design feature is the use of an interdigitated emitter providing a periphery greater than 7.0 inches (18 cm) which improves both the gain characteristics and current handling capability.

These transistors have been specifically designed and engineered for high speed/high voltage switching applications where the designer is concerned with optimizing power conversion efficiency.

In order to supply the user with a more complete definition of the C²R switching transistor capability, General Semiconductor Industries has attempted to furnish a data sheet with a thorough and meaningful technical dialogue.



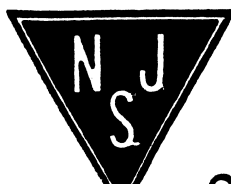
FEATURES:

- HIGH VOLTAGE
- HIGH GAIN
- HIGH CURRENT
- LOW SATURATION VOLTAGES
- FAST SWITCHING
- LOW LEAKAGE CURRENT

APPLICATIONS:

- HIGH SPEED SWITCHING
- POWER CONVERSION
- CONVERTERS
- INVERTERS
- CLASS D AMPLIFIERS
- CLASS C AMPLIFIERS

MAXIMUM RATINGS (T _J = 25°C unless otherwise noted)		2N6653 XGSR15030	2N6654 XGSR15035	2N6655 XGSR15040	UNIT
RATING	SYMBOL				
Collector-Base Voltage	V _{CB0}	350	400	450	Volts
Collector-Emitter Voltage	V _{CE0}	300	350	400	Volts
Emitter-Base Voltage	V _{EB0}	7.0	7.0	7.0	Volts
Collector Current - Continuous	I _C	20	20	20	Amps
Peak	I _{CM}	30	30	30	Amps
Base Current-Continuous	I _B	10	10	10	Amps
Emitter Current - Continuous	I _E	30	30	30	Amps
Peak	I _{EM}	40	40	30	Amps
Total Power Dissipation @T _C = 100°C	P _D	75	75	75	Watts
Total Power Dissipation @T _C = 25°C	P _D	150	150	150	Watts
Junction to Case Thermal Resistance	R _{θJC}	1.0	1.0	1.0	°C/W
Operating and Storage Junction Temperature Range	T _{J (oper)} T _{stg}	-65 to +175 -65 to +200	-65 to +175 -65 to +200	-65 to +175 -65 to +200	°C °C



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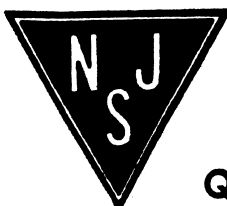
ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$ unless otherwise noted)									
SYMBOL	CONDITIONS	2N6652 XGSR15030		2N6654 KGR15038		2N6655 XGSR15040		UNIT	
		Min.	Max.	Min.	Max.	Min.	Max.		
V_{CB0}	$I_C = 1.0\text{mA}$	350	—	400	—	450	—	Volts	
V_{CE0}	$I_C = 50\text{mA}$	300	—	350	—	400	—	Volts	
V_{EBO}	$I_E = 1.0\text{mA}$	7.0	—	7.0	—	7.0	—	Volts	
V_{CEX} (SUST)	$I_C = 50\text{mA}, V_{BE} = -1.5\text{V}$	360	—	400	—	450	—	Volts	
V_{CER} (SUST)	$I_C = 50\text{mA}, R_F = 47\Omega$	325	—	375	—	425	—	Volts	
I_{CB0} (1)	$V_{CB} = 80\% V_{CB}$ Rated	—	500	—	500	—	500	μA	
(2)	$V_{CB} = 100\% V_{CB}$ Rated, $V_{BE} = -1.5\text{V}$	—	100	—	100	—	100	μA	
I_{EBO} (1)	$V_{EB} = 5.0\text{V}$	—	100	—	100	—	100	μA	
I_{EBO}	$V_{EB} = 7.0\text{V}$	—	50	—	50	—	50	μA	
I_{CE0}	$V_{CE} = 80\% V_{CE}$ Rated	—	1.0	—	1.0	—	1.0	mA	
I_{CEX}	$V_{CE} = V_{CE0}$ Rated, $V_{BE} = -1.5\text{V}, T_c = 150^\circ\text{C}$	—	3.0	—	3.0	—	3.0	mA	

h_{FE}^* (1)	$V_{CE} = 5.0\text{V}, I_C = 15\text{A}$	10	—	10	—	10	—	—
h_{FE}^* (2)	$V_{CE} = 2.0\text{V}, I_C = 15\text{A}$	10	—	10	—	10	—	—
V_{CE} (sat)*	$I_C = 15\text{A}, I_B = 3\text{A}$	—	0.8	—	0.8	—	0.8	Volts
V_{BE} (sat)*	$I_C = 15\text{A}, I_B = 3\text{A}$	—	1.3	—	1.3	—	1.3	Volts

f_T	$V_{CE} = 10\text{V}, I_C = 1.0\text{A}, 10\text{MHz}$	25	75	25	75	25	75	MHz
C_{ob0}	$V_{CB} = 10\text{V}, f = 1\text{MHz}$	100	300	100	300	100	300	pF

t_d	$V_{CC} = 200\text{V}, I_C = 15\text{A},$ $I_{B1} = I_{B2} = 3.0\text{A}, t_p = 10\mu\text{s},$ Duty Cycle < 2.0%, Resistive	0.05	—	0.05	—	0.05	—	μsec
t_r		0.2	—	0.2	—	0.2	—	μsec
t_s		1.5	—	1.5	—	1.5	—	μsec
t_f		0.35	—	0.35	—	0.35	—	μsec

*Pulse measurement conditions: Length = 300 μsec , Duty Cycle < 2% (measured using separate current carrying and voltage sensing leads).



Quality Semi-Conductors