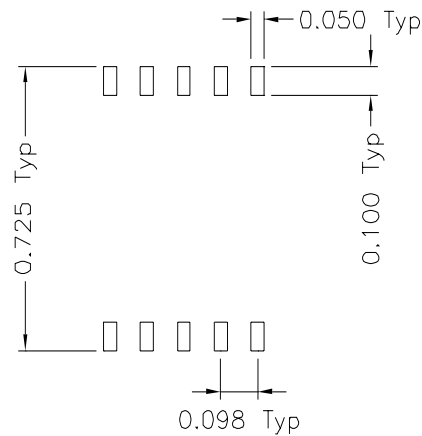
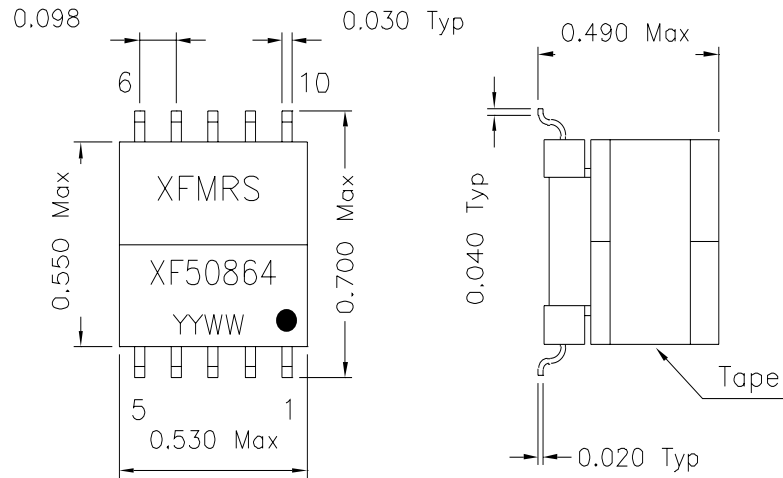
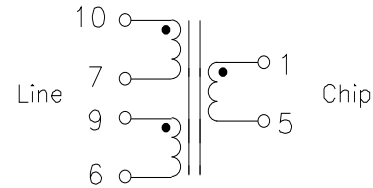


1. Dimensions:



Suggested PCB Layout

2.Schematic:



3.Electrical Specifications: @25°C

PIN10-6 OCL: 3.0mH ±6% @10KHz 0.1V
(CONNECT PIN9-7)

PIN10-6 LL: 10uH Max @100KHz 100mV
(CONNECT PIN9-7, PIN1-5)

CW/W: PIN1-10 100pF Max. CONNECT PIN9-7

THD: -70dB Max. @40KHz, 1.85V 10-6 - 28 Ohm load

INSERTION LOSS: ±0.5dB @20KHz-560KHz

LONGITUDINAL BALANCE: 55dB Min. @20KHz-250KHz

TEMPERATURE RANGE: -40°C - +85°C

ISOLATION VOLTAGE: 2500Vrms Chip - Line

DC Res.: Pins 9-6,10-7 1.15 Ohms ±15% (each)

DC Res.: Pins 1-5 0.600 Ohms ±15%

Turns Ratio: (10-6):(1-5) = 2.2:1±1% (tie 9-7)

Designed to meet UL1950 Supplementary Ins Specifications for a working voltage of 250Vrms.

DOC. REV A/1

XFMR5 Inc	Title: HDSL TRANSFORMER		
	UNLESS OTHERWISE SPECIFIED	P/N: XF50864	REV. A
TOLERANCES: .xxx ±0.010	DWN.	康玉坤	Oct-07-99
Dimensions in INCH	CHK.	李清儿	Oct-07-99
SHEET 1 OF 1	APP.	Joe Huff	Oct-07-99