TOSHIBA INSULATED GATE BIPOLAR TRANSISTOR SILICON N CHANNEL IGBT

GT50J102

HIGH POWER SWITCHING APPLICATIONS MOTOR CONTROL APPLICATIONS

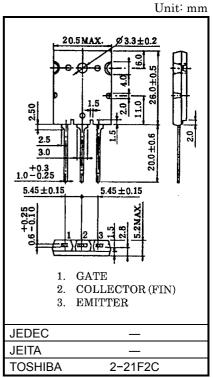
• The 3rd. Generation.

• Enhancement-Mode.

 $\begin{array}{ll} \bullet & \mbox{High Speed.} & : t_f = 0.30 \mu s \; (\mbox{Max.}) \\ \bullet & \mbox{Low Saturation Voltage.} & : \mbox{VCE}(\mbox{sat)} = 2.7 \mbox{V} \; (\mbox{Max.}) \\ \end{array}$

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Emitter Voltage		V _{CES}	600	V	
Gate-Emitter Voltage		V _{GES}	±20	V	
Collector Current	DC	Ic	50	А	
	1ms	I _{CP}	100		
Collector Power Dissipation		PC	200	W	
Junction Temperature		Tj	150	°C	
Storage Temperature Range		T _{stg}	-55~150	°C	
Screw Torque		_	0.8	N∙m	

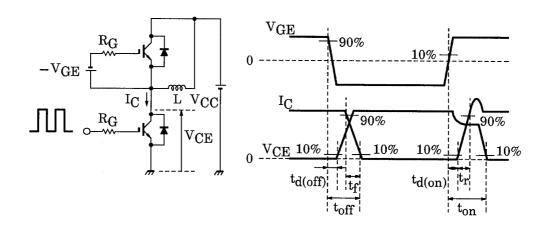


Weight: 9.75g

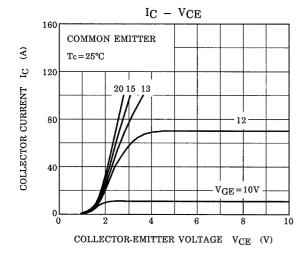
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

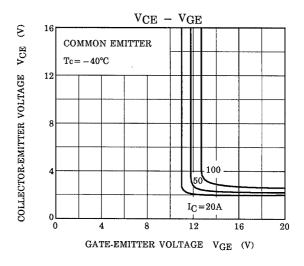
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Gate Leakage Curr	ent	I _{GES}	V _{GE} = ±20V, V _{CE} = 0	_	_	±500	nA
Collector Cut-Off C	Current	I _{CES}	V _{CE} = 600V, V _{GE} = 0	_	_	1.0	mA
Gate-Emitter Cut-off Voltage		V _{GE(OFF)}	I _C = 5mA, V _{CE} = 5V	5.0	7.0	8.0	V
Collector-Emitter Saturation Voltage		V _{CE(sat)}	I _C = 50A, V _{GE} = 15V	_	2.1	2.7	V
Input Capacitance		C _{ies}	V _{CE} = 10V, V _{GE} = 0 f = 1MHz	_	4500	_	pF
Switching Time	Turn-on delayTime	t _{d(on)}		_	0.08	_	
	Rise Time	t _r	Inductive Load	_	0.12	_	
	Turn-on Time	t _{on}	V _{GE} = ±15V	_	0.40	_	ше
	Turn-off delay Time	t _{d(off)}	$I_C = 50A$ $R_G = 24\Omega$	_	0.20	_	μs
	Fall Time	t _f	(Note 1)	_	0.15	0.30	
	Turn-off Time	t _{off}		_	0.50	_	
Thermal Resistance		R _{th(j-c)}		_	_	0.625	٧

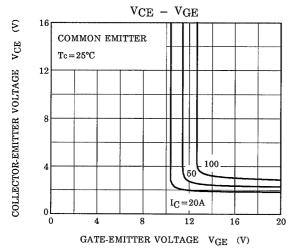
Note 1: Switching. time measurement circuit and input / output waveforms

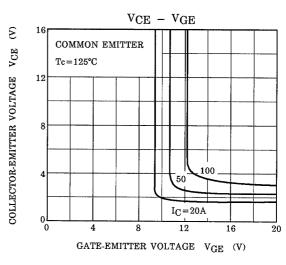


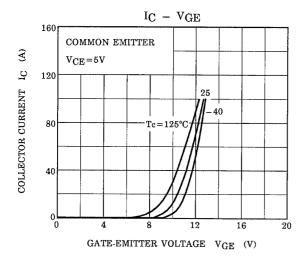
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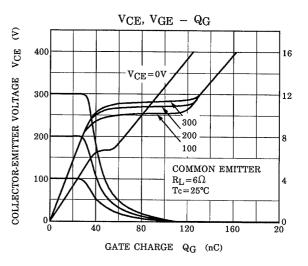






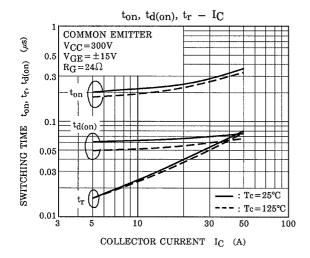


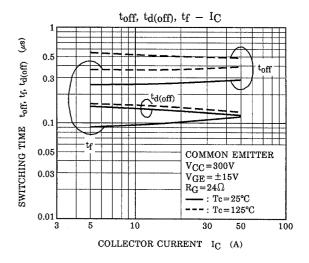


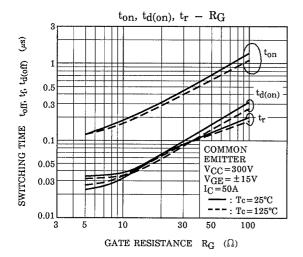


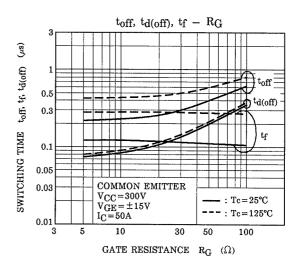
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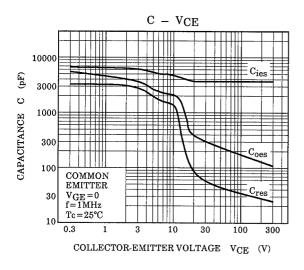
GATE-EMITTER VOLTAGE VGE

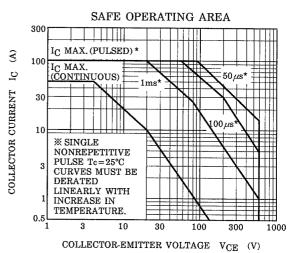


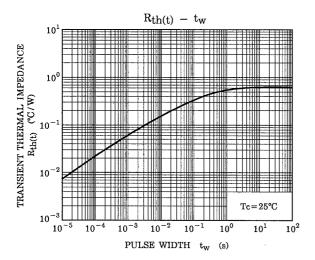


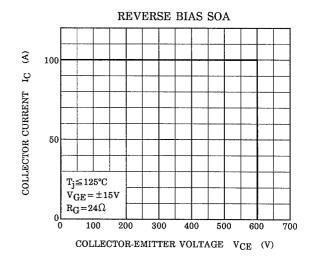












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