

MiniSKiiP® CIB IPM

Three-phase bridge rectifier + brake chopper + three-phase inverter intelligent power module

SKiiP 26NABI066V3

Data sheet status: Preliminary

Features

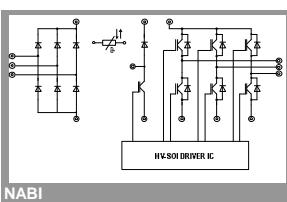
- One screw assembly of driver, module and heat sink
- Solder-free assembly of power, control and auxiliary contacts
- Trench-Field-Stop IGBT
- Robust and soft freewheeling diodes in CAL technology
- Latch-up free SOI driver IC
- Advanced level shifter technology
- Bootstrap power supply technology
- Matched propagation delay for all channels
- Overcurrent shut-down via current sensing
- Interlock logic for shoot-through prevention
- Common shut-down signal
- Undervoltage lockout for all channels with hysteresis band
- Integrated temperature sensor (NTC)
- RoHS compliant

Typical Applications

- Industrial- & consumer drives
- Power supplies (SMPS & UPS)
- Industrial air conditioner

Remarks

Absolute Maximum Ratings ($T_s=25^\circ\text{C}$, unless otherwise specified)						
Symbol	Parameter	Conditions	Values	Units		
IGBT - Inverter, Chopper						
V_{CES}			600	V		
I_c	$T_j = 175^\circ\text{C}$	$T_s = 25^\circ\text{C}$	59	A		
		$T_s = 70^\circ\text{C}$	47	A		
I_{Cnom}			50	A		
I_{CRM}	$I_{CRM} = 2 \times I_{Cnom}$		100	A		
t_{psc}	$V_{CC} = 360\text{ V}$ $V_{GE} \leq 15\text{ V}$ $V_{CES} \leq 600\text{ V}$	$V_{DC}=360\text{ V}, V_{GE}=15\text{ V}, T_j=150^\circ\text{C}$	≤6	μs		
$T_{J(max)}$			-40 ... +175	°C		
Diode - Inverter, Chopper						
I_F	$T_j = 175^\circ\text{C}$	$T_s = 25^\circ\text{C}$	54	A		
		$T_s = 70^\circ\text{C}$	42	A		
I_{Fnom}			50	A		
I_{FRM}	$I_{FRM} = 2 \times I_{Fnom}$		100	A		
I_{FSM}	$t_b = 10\text{ ms}, \sin 180^\circ, T_j = 25^\circ\text{C}$		345 (320)	A		
$T_{J(max)}$			-40 ... +175	°C		
Diode - Rectifier						
V_{RRM}		$T_s = 25^\circ\text{C}$	800	V		
I_F		$T_s = 70^\circ\text{C}, T_j = 150^\circ\text{C}$	37	A		
I_{FSM}		$T_s = 25^\circ\text{C}, 10\text{ms}, \text{half sine wave}$	370	A		
$T_{J(max)}$			-40 ... +150	°C		
Driver - Inverter, Chopper						
V_{CC}	Applied between VCC-VSS, VCCL-VSSL		17	V		
VBx	Applied between VB1-U, VB2-V, VB3-W		17	V		
VSx	Voltage to VSS, $t_p < 500\text{ns}$		-3 ... 600	V		
V_{in}	Applied between HIN1, LIN1, HIN2, LIN2, HIN3, LIN3, LIN4, /ERRIN - VSS		VSS-0.3 ... VCC+0.3	V		
V_{oErr}	Applied between /ERROUT-VSS		VSS-0.3 ... VCC+0.3	V		
$I_{max(EO)}$	Between /ERROUT-VSS		10	mA		
V_{ITRIP}	Applied between ITRIP-VSS		VSS-0.3 ... VCC+0.3	V		
f_{max}			20	kHz		
Temperature						
T_c			-40 ... +125	°C		
T_{stg}			-40 ... +125	°C		
System						
V_{CC}	Applied between P-NU, NV, NW		400	V		
$V_{CC(s)}$	Applied between P-NU, NV, NW		≥400	V		
V_{isol}	AC, rms, $f=60\text{Hz}$, $t=1\text{min}$, all pins to heat sink		2500	V		
I_{IRMS}	Per power terminal (20A / Spring)		20	A		
Electrical Characteristics ($T_s=25^\circ\text{C}$, unless otherwise specified)						
Symbol	Parameter	Conditions	min.	typ.	max.	Units
IGBT - Inverter, Chopper						
V_{CESat}	$I_C = 50\text{ A}$ $V_{GE} = 15\text{ V}$	$T_s = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	1.45	1.85		V
V_{CEO}		$T_s = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	0.9	1.1		V
r_{CE}	$V_{GE} = 15\text{ V}$	$T_s = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	11	15		mΩ
I_{CES}	$V_{GE} = 0\text{ V}$ $V_{CE} = 600\text{ V}$	$T_s = 25^\circ\text{C}$	17	21		
E_{on}	$V_{CC}=300\text{V}$	$T_s = 150^\circ\text{C}$	3.0			mJ
E_{off}	$I_C=50\text{A}$	$T_s = 150^\circ\text{C}$	2.0			mJ
$t_{d(on)}$	$R_{G\,on} / R_{G\,off} = 4.7\text{ Ω}$	$T_s = 150^\circ\text{C}$	756			ns
t_r	$di/dt_{on} = 648\text{ A}/\mu\text{s}$	$T_s = 150^\circ\text{C}$	78			ns
$t_{d(off)}$	$di/dt_{off} = 1271\text{ A}/\mu\text{s}$	$T_s = 150^\circ\text{C}$	1276			ns
t_f		$T_s = 150^\circ\text{C}$	128			ns
$R_{th(i-s)l}$	per IGBT		1.1			kW
Diode - Inverter, Chopper						
$V_F = V_{EC}$	$I_F = 50\text{ A}$ $V_{GE} = 0\text{ V}$ (Chilevel)	$T_s = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	1.5	1.7		V
V_{FO}		$T_s = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	1.0	1.1		V
r_F		$T_s = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	0.9	1.0		V
E_{rr}	$I_F=50\text{A}$	$T_s = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$	10	12		mΩ
Qrr	$di/dt = -1771\text{ A}/\mu\text{s}$	$T_s = 150^\circ\text{C}$	12	14		mΩ
$IRRM$	$V_{CC}=300\text{V}, V_{GE} = 0\text{ V}$	$T_s = 150^\circ\text{C}$	2320			μC
$R_{th(i-s)D}$	per diode		57			A
			1.6			kW



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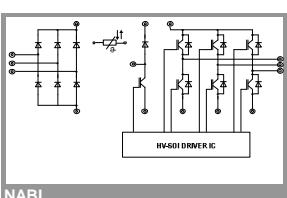
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Electrical Characteristics ($T_s=25^\circ\text{C}$, unless otherwise specified)		Conditions	Limits			
Symbol	Parameter		min.	typ.	max.	Units
Diode - Rectifier						
V_F	$I_F = 15 \text{ A}$ Chiplevel	$T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$	1.0	1.2		V
V_{FO}		$T_J=125^\circ\text{C}$	0.9	1.1		V
r_F		$T_J=125^\circ\text{C}$	0.8			
$R_{th(j-s)}$	per diode		13			$\text{m}\Omega$
Driver						
VCC	Applied between VCC-VSS, VCCL-VSSL		1.7	15		V
ICC	VCC=15V, all logic inputs=open, VCC-VSS		5,0			mA
VBx	Applied between VB1-U, VB2-V, VB3-W		15			V
IBx	$VBx=15\text{V}, V_{IH}=V_{IL}=0\text{V}$		300			μA
V_{IT+}	Applied between HIN1, HIN2, HIN3, LIN1, LIN2, LIN3, LIN4, /ERRIN - VSS		2,2	2,7		V
V_{IT-}	Applied between HIN1, HIN2, HIN3, LIN1, LIN2, LIN3, LIN4, /ERRIN - VSS		0,8	1,1		V
V_{oErr}	Error output voltage applied between /ERROUT-VSS		15			V
V_{UV}			10,3			V
V_{UVr}			12,1			V
$t_{d,ITRIP}$	ITRIP to output propagation delay		690			ns
t_{SIS}	Short pulse suppression for signal inputs		420			ns
t_{TD}	Interlock dead time		450			ns
f_{sw}			15	25		kHz
Temperature Sensor						
R_{100}	$T_{\text{Sensor}} = 100^\circ\text{C}$ ($R_{25} = 5\text{k}\Omega$)		339			Ω
$B_{100/125}$	$R_{(T)} = R_{100}\exp[B_{100/125}(1/T-1/373)]$; [T] = K		4096			K
Module						
m			60			g
M_S			2	2,5		Nm



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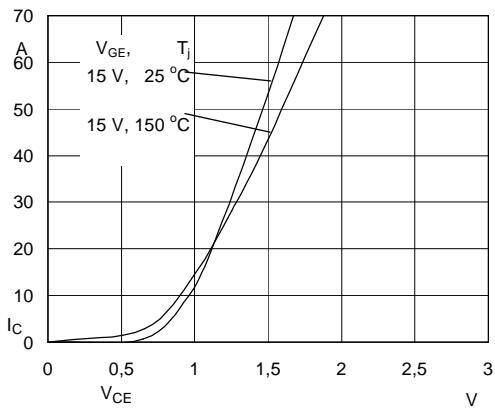


Fig.1: Typ. output characteristic

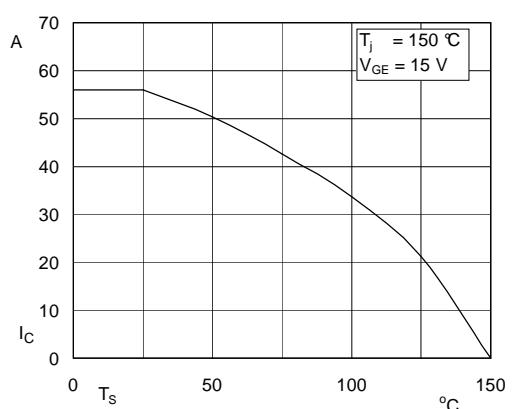


Fig. 2: Typ. rated current vs. Temperature

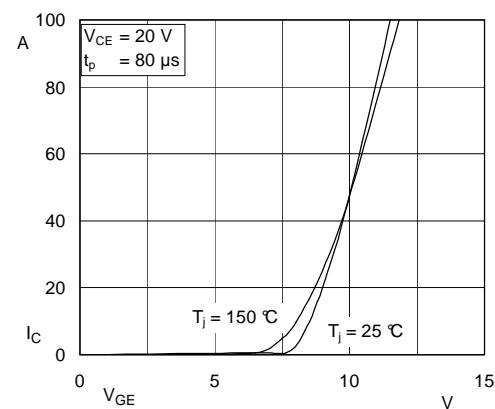


Fig. 3: Typ. transfer characteristic

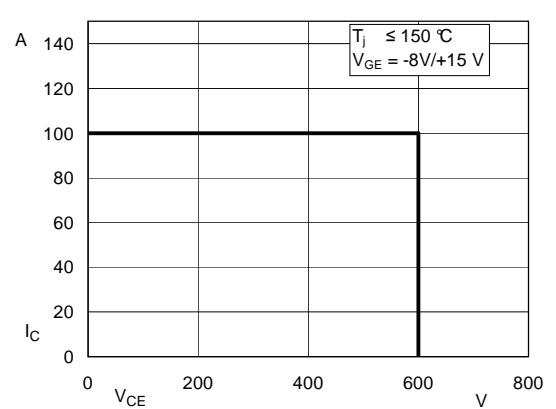


Fig. 4: Reverse bias safe operating area

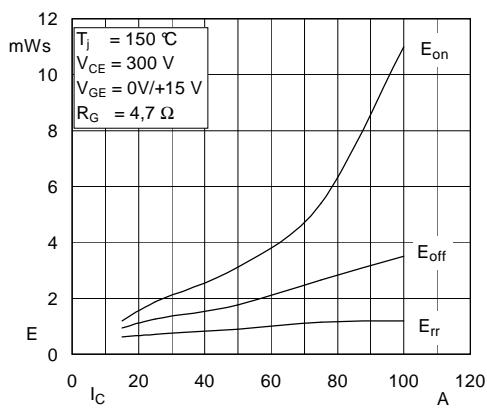


Fig. 5: Turn-on/-off energy = $f(I_C)$

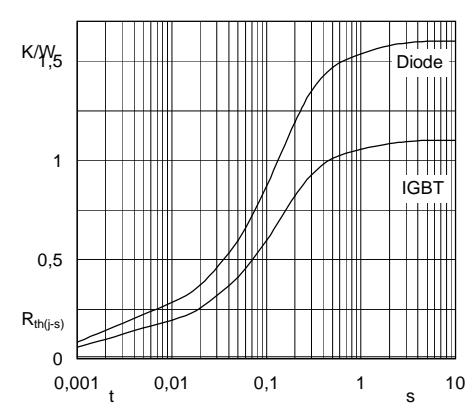


Fig. 6: Typ. thermal impedance

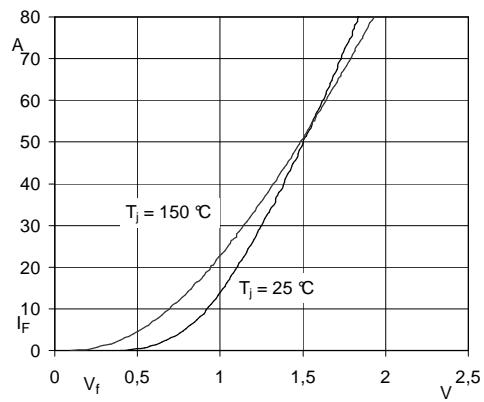


Fig. 7: Typ. freewheeling diode forward characteristic

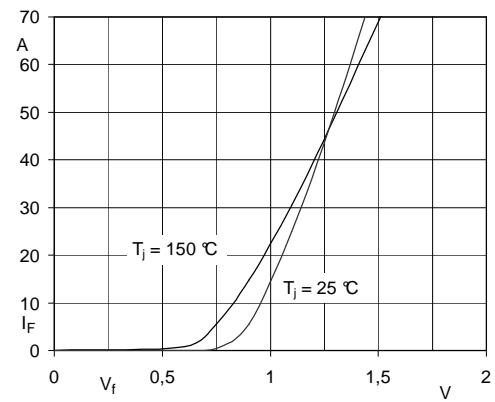


Fig. 8: Typ. input bridge forward characteristic

Pin Number	Pin Name	Pin Description
1	HIN1	PWM signal input for U phase high side switch
2	HIN2	PWM signal input for V phase high side switch
3	HIN3	PWM signal input for W phase high side switch
4	LIN1	PWM signal input for U phase low side switch
5	LIN2	PWM signal input for V phase low side switch
6	LIN3	PWM signal input for W phase low side switch
7	/ERROUT	Error logic output (inverted)
8	/ERRIN	External error / shut-down logic input (inverted)
9	ITRIP	Comparator input for current measurement
10	LIN4	PWM signal input for brake chopper switch
11	VSS	Driver IC supply voltage ground
12	VCC	Driver IC main supply voltage
13	VB2	Floating supply for V phase high side IGBT
14	VB1	Floating supply for U phase high side IGBT
15	VB3	Floating supply for W phase high side IGBT
16	VSSL	Low side supply voltage ground
17	VCCL	Low side supply voltage
L1	L1	Bridge rectifier input for phase 1
L2	L2	Bridge rectifier input for phase 2
L3	L3	Bridge rectifier input for phase 3
U/E1		U phase output
V/E3		V phase output
W/E5		W phase output
NU/E2		Negative DC-Link input for U phase
NV/E4		Negative DC-Link input for V phase
NW/E6		Negative DC-Link input for W phase
B		Brake chopper B terminal
+B		Brake chopper +B terminal
-B/E8		Brake chopper -B terminal
P		Positive DC-Link input
+RECT		Bridge rectifier output for positive DC-Link
-RECT		Bridge rectifier output for negative DC-Link
+T		Temperature sensor terminal (+)
-T		Temperature sensor terminal (-)

Fig. 4: PIN Description

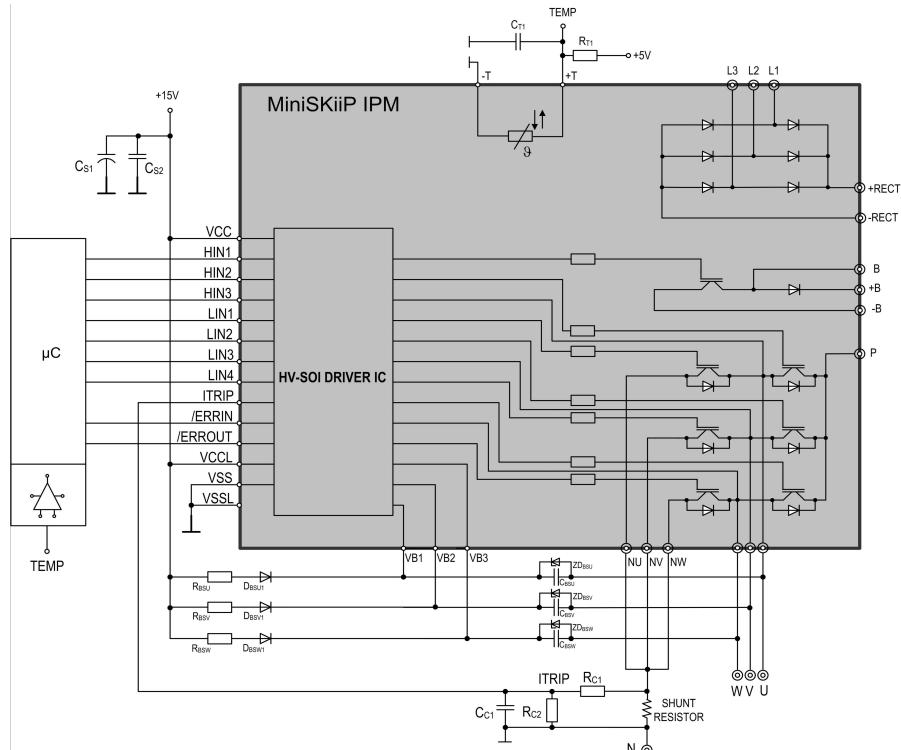


Fig. 5: Internal Circuit and Typ. Application

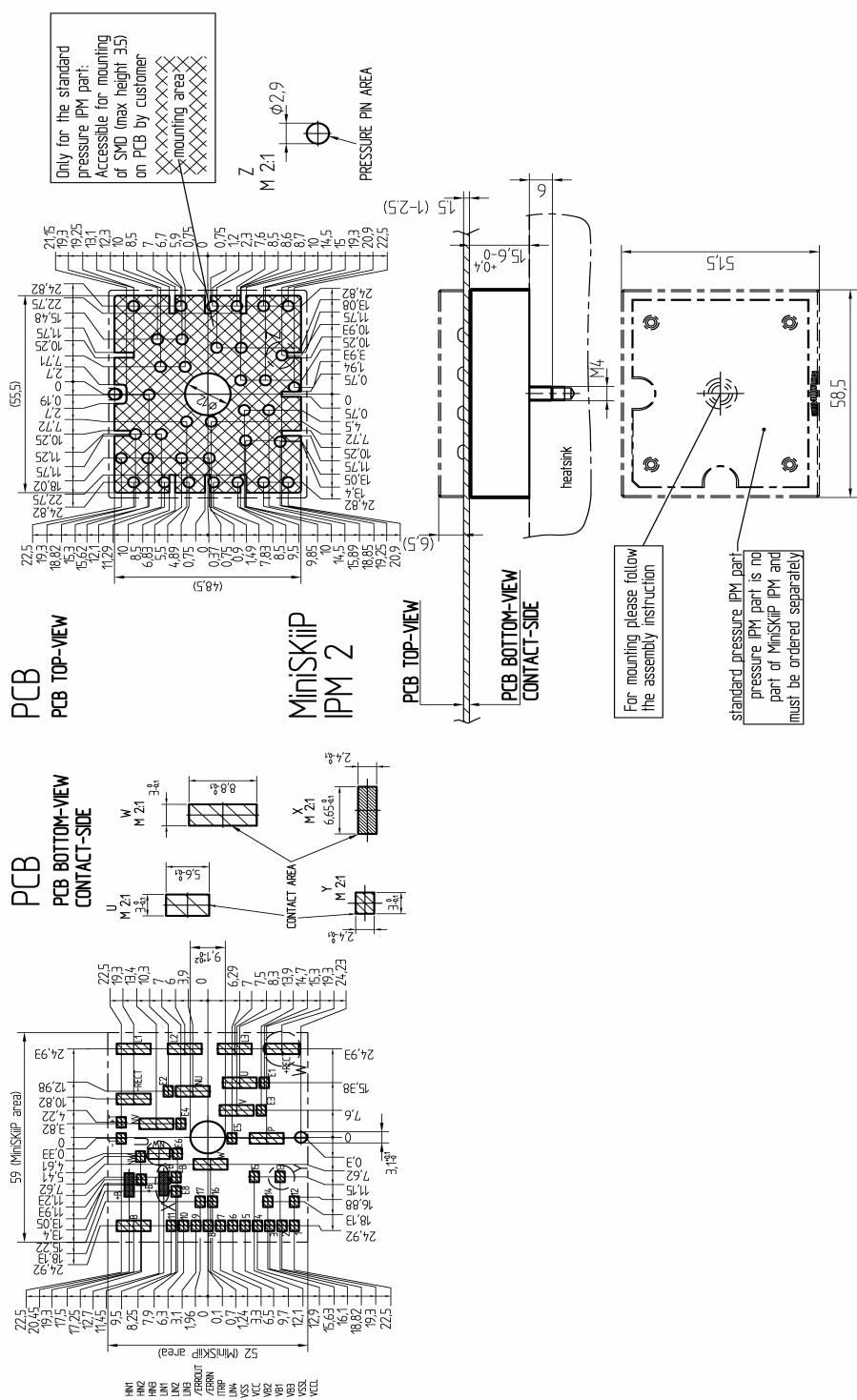


Fig. 6: Package Outline, Pinout

The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our personal.