2SC2925

Silicon NPN epitaxial planar type

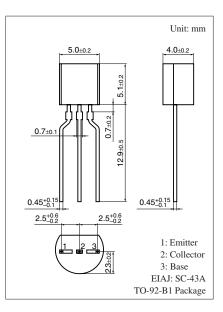
For low-frequency output amplification

Features

- High forward current transfer ratio h_{FE}
- \bullet Low collector-emitter saturation voltage $V_{CE(sat)}$

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	60	V	
Collector-emitter voltage (Base open)	V _{CEO}	50	V	
Emitter-base voltage (Collector open)	V _{EBO}	15	V	
Collector current	I _C	0.7	А	
Peak collector current	I _{CP}	1.5	А	
Collector power dissipation	P _C	750	mW	
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	





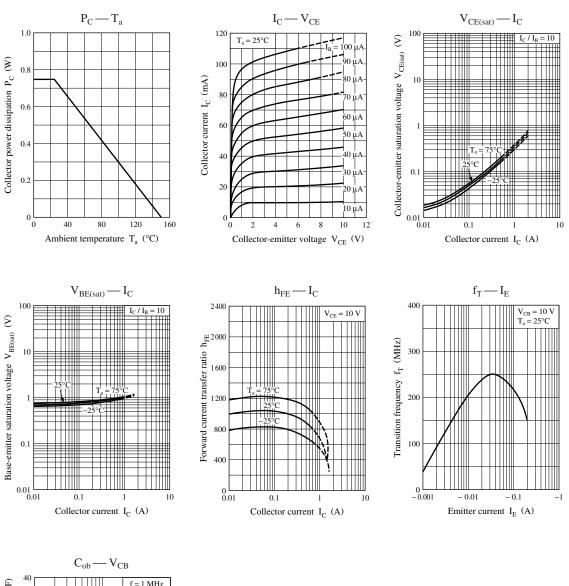
Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

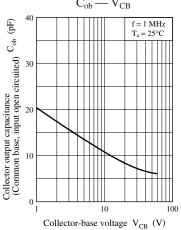
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu A, \ I_{\rm E} = 0$	60			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$	50			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_E = 10 \ \mu A, \ I_C = 0$	15			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 20 V, I_E = 0$			1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 20 \text{ V}, I_B = 0$			10	μΑ
Forward current transfer ratio *	h _{FE}	$V_{CE} = 10 \text{ V}, I_C = 150 \text{ mA}$	400	1 0 0 0	2000	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 500 \text{ mA}, I_{\rm B} = 50 \text{ mA}$		0.15	0.40	V
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		11	15	pF
(Common base, input open circuited)						

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors. 2. *: Rank classification

Rank	R	S	Т
h _{FE}	400 to 800	600 to 1 200	1 000 to 2 000

Panasonic





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