TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (Ultra High speed U-MOSIII)

## **TENTATIVE**

# **TPCP8001-H**

High Speed and High Efficiency DC-DC Converters Notebook PC Applications Portable Equipment Applications

- · Small footprint due to small and thin package
- · High speed switching
- Small gate charge: Qsw = 11 nC (typ.)
- Low drain-source ON resistance: RDS (ON) = 13 mO (typ.)
- High forward transfer admittance:  $|Y_{fS}| = 16 \text{ S (typ.)}$
- Low leakage current:  $IDSS = 10 \mu A \text{ (max) (VDS} = 30V)$
- Enhancement mode:  $V_{th} = 1.1$  to 2.3 V ( $V_{DS} = 10$  V,  $I_{D} = 1$  mA)

#### Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	30	V	
Drain-gate voltage (R	$k_{GS} = 20 \text{ k}\Omega$ )	$V_{DGR}$	30	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC (Note 1)	I <sub>D</sub>	7.2	Α	
Diain current	Pulsed (Note 1)	I <sub>DP</sub>	28.8	A	
Drain power dissipati	on $(t = 5 s)$ (Note 2a)	$P_{D}$	1.68	W	
Drain power dissipati	on $(t = 5 s)$ (Note 2b)	$P_{D}$	0.84	W	
Single pulse avalanc	he energy (Note 3)	E <sub>AS</sub>	33.6	mJ	
Avalanche current		I <sub>AR</sub>	7.2	Α	
Repetitive avalanche	energy c=25 ) (Note 4)	E <sub>AR</sub>	0.168	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature	range	T <sub>stg</sub>	–55 to 150	°C	

Unit: mm

033±005
1,2,3:SOURCE 5,6,7,8:DRAIN

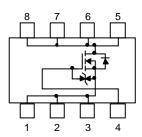
JEDEC ?

JEITA ?

TOSHIBA 2-5Q1A

Weight: 0.080 g (typ.)

#### **Circuit Configuration**



Note: For (Note 1), (Note 2), (Note 3), (Note 4), please refer to the next page.

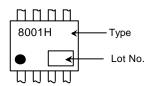
This transistor is an electrostatic sensitive device. Please handle with caution.



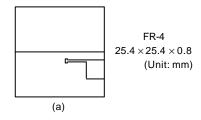
#### **Thermal Characteristics**

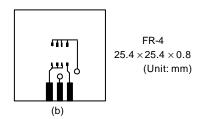
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient $(t=5\;s) \eqno(Note\;2a)$	R <sub>th (ch-a)</sub>	74.4	°C/W	
Thermal resistance, channel to ambient $(t=5\;s) \eqno(Note\;2b)$	R <sub>th (ch-a)</sub>	148.8	°C/W	

### Marking (Note 5)

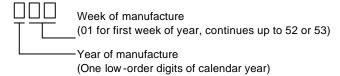


- Note 1: Please use devices on condition that the channel temperature is below 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (a)
- (b) Device mounted on a glass-epoxy board (b)





- Note 3: V  $_{DD}$  = 24 V,  $T_{ch}$  = 25 °C (initial), L = 0.5 mH,  $R_{G}$  = 1  $\Omega,\ I_{AR}$  = 7.2 A
- Note 4: Repetitive rating: pulse width limited by max channel temperature
- Note 5: \* Weekly code: (Three digits)





## Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-OFF cu	ırrent	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	10	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
Diam-source bre	ardown voltage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15 — —		_	<b>]</b>
Gate threshold v	oltage	$V_{th}$	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	1.1	_	2.3	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	$V_{GS} = 4.5 \text{ V}, I_D = 3.6 \text{ A}$	_	19	25	mΩ
Drain-source ON resistance		(ON)	$V_{GS} = 10 \text{ V}, I_D = 3.6 \text{ A}$	_	13	16	11122
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 3.6 \text{ A}$	10	19	_	S
Input capacitance	9	C <sub>iss</sub>		_	640	_	
Reverse transfer	Reverse transfer capacitance		$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	75	_	pF
Output capacitance		C <sub>oss</sub>		_	300	_	
Switching time	Rise time	t <sub>r</sub>	VGS 0 V	_	4	_	ns
	Turn-ON time	t <sub>on</sub>		_	8	_	
	Fall time	ŧſ		_	4	_	
	Turn-OFF time	t <sub>off</sub>	$V_{DD} \simeq 15 \text{ V}$ Duty $\leq 1\%$ , $t_W = 10 \mu\text{s}$	_	18	_	
Total gate charge		Qg	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.2 \text{A}$	_	11	_	
	(gate-source plus gate-drain)		$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 7.2 \text{A}$	_	6.3	_	
Gate-source charge 1		Q <sub>gs1</sub>		_	2.2	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.2 \text{A}$	_	2.6	_	
Gate switch char	Gate switch charge		]	_	3.6	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I <sub>DRP</sub>	_	_	_	28.8	Α
Forward voltage (diode)			$V_{DSF}$	$I_{DR} = 7.2 \text{ A}, V_{GS} = 0 \text{ V}$		_	-1.2	V

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