

**TrenchMOS™ transistor  
Standard level FET**

**BUK7515-100A**

**GENERAL DESCRIPTION**

N-channel enhancement mode standard level field-effect power transistor in a plastic envelope using 'trench' technology which features very low on-state resistance. It is intended for use in automotive and general purpose switching applications.

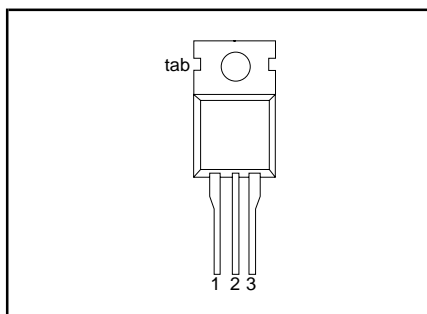
**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	UNIT
$V_{DS}$	Drain-source voltage	100	V
$I_D$	Drain current (DC)	75	A
$P_{tot}$	Total power dissipation	230	W
$T_j$	Junction temperature	175	°C
$R_{DS(ON)}$	Drain-source on-state resistance $V_{GS} = 10\text{ V}$	15	mΩ

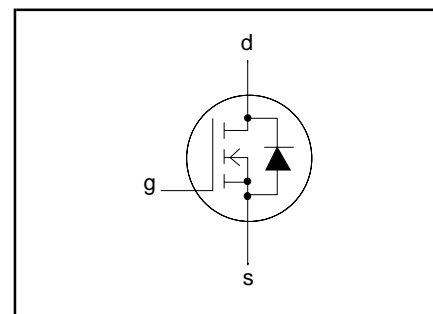
**PINNING - TO220AB**

PIN	DESCRIPTION
1	gate
2	drain
3	source
tab	drain

**PIN CONFIGURATION**



**SYMBOL**



**LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{DS}$	Drain-source voltage	-	-	100	V
$V_{DGR}$	Drain-gate voltage	$R_{GS} = 20\text{ k}\Omega$	-	100	V
$\pm V_{GS}$	Gate-source voltage	-	-	20	V
$I_D$	Drain current (DC)	$T_{mb} = 25\text{ }^\circ\text{C}$	-	75	A
$I_D$	Drain current (DC)	$T_{mb} = 100\text{ }^\circ\text{C}$	-	53	A
$I_{DM}$	Drain current (pulse peak value)	$T_{mb} = 25\text{ }^\circ\text{C}$	-	240	A
$P_{tot}$	Total power dissipation	$T_{mb} = 25\text{ }^\circ\text{C}$	-	230	W
$T_{stg}, T_j$	Storage & operating temperature	-	- 55	175	°C

**THERMAL RESISTANCES**

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
$R_{th\ j-mb}$	Thermal resistance junction to mounting base	-	-	0.65	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient	in free air	60	-	K/W

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### STATIC CHARACTERISTICS

T<sub>j</sub> = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>(BR)DSS</sub>	Drain-source breakdown voltage	V <sub>GS</sub> = 0 V; I <sub>D</sub> = 0.25 mA; T <sub>j</sub> = -55 °C	100 89	- -	- -	V V
V <sub>GS(TO)</sub>	Gate threshold voltage	V <sub>DS</sub> = V <sub>GS</sub> ; I <sub>D</sub> = 1 mA T <sub>j</sub> = 175 °C T <sub>j</sub> = -55 °C	2 1 -	3.0 -	4.0 -	V V V
I <sub>DSS</sub>	Zero gate voltage drain current	V <sub>DS</sub> = 100 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C	-	0.05	10	µA
I <sub>GSS</sub>	Gate source leakage current	V <sub>GS</sub> = ±20 V; V <sub>DS</sub> = 0 V	-	2	100	nA
R <sub>DS(ON)</sub>	Drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A T <sub>j</sub> = 175 °C	-	12.0	15.0 40.5	mΩ mΩ

### DYNAMIC CHARACTERISTICS

T<sub>mb</sub> = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
C <sub>iss</sub>	Input capacitance	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 25 V; f = 1 MHz	-	4500	6000	pF
C <sub>oss</sub>	Output capacitance		-	550	660	pF
C <sub>rss</sub>	Feedback capacitance		-	305	400	pF
t <sub>d on</sub>	Turn-on delay time	V <sub>DD</sub> = 30 V; R <sub>load</sub> = 1.2 Ω;	-	35	55	ns
t <sub>r</sub>	Turn-on rise time	V <sub>GS</sub> = 10 V; R <sub>G</sub> = 10 Ω	-	85	125	ns
t <sub>d off</sub>	Turn-off delay time		-	150	225	ns
t <sub>f</sub>	Turn-off fall time		-	70	100	ns
L <sub>d</sub>	Internal drain inductance	Measured from contact screw on tab to centre of die	-	3.5	-	nH
L <sub>d</sub>	Internal drain inductance	Measured from drain lead 6 mm from package to centre of die	-	4.5	-	nH
L <sub>s</sub>	Internal source inductance	Measured from source lead 6 mm from package to source bond pad	-	7.5	-	nH

### REVERSE DIODE LIMITING VALUES AND CHARACTERISTICS

T<sub>j</sub> = 25 °C unless otherwise specified

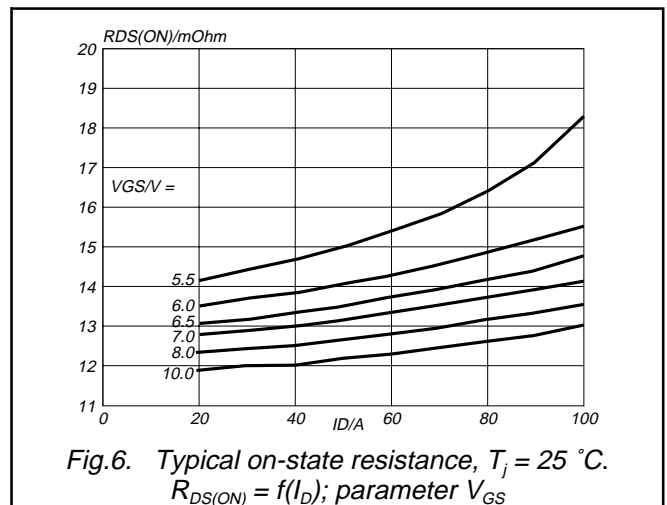
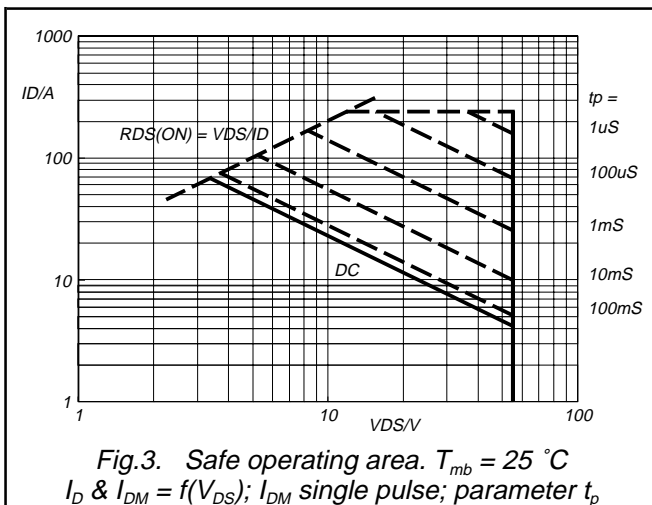
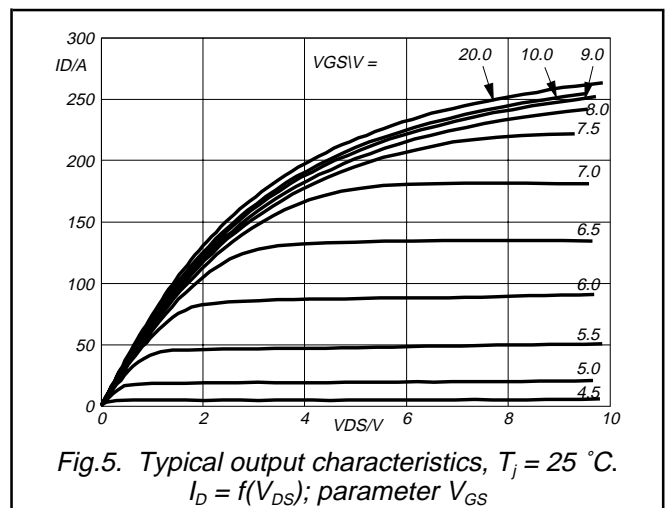
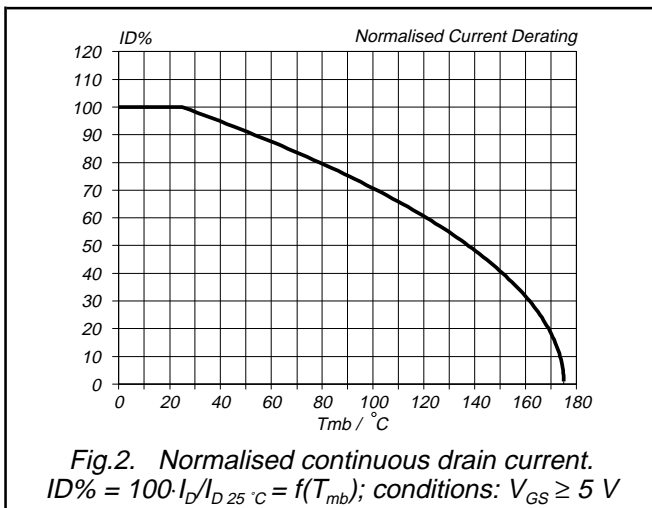
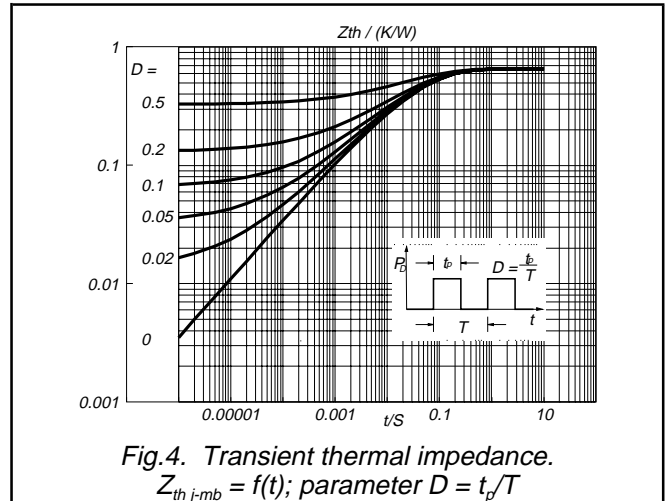
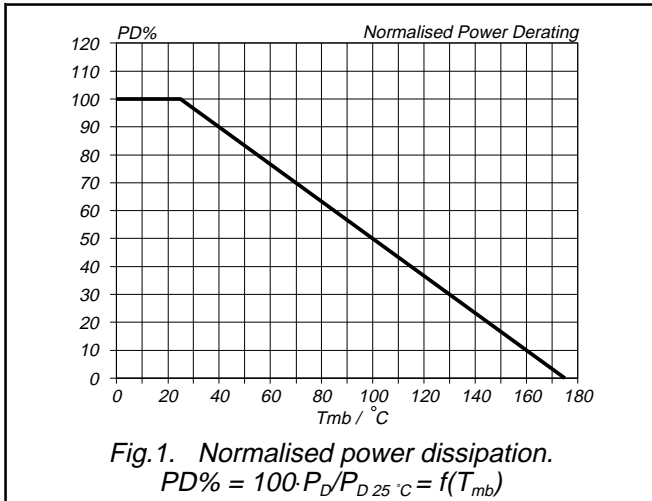
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>DR</sub>	Continuous reverse drain current		-	-	75	A
I <sub>DRM</sub>	Pulsed reverse drain current		-	-	240	A
V <sub>SD</sub>	Diode forward voltage	I <sub>F</sub> = 25 A; V <sub>GS</sub> = 0 V I <sub>F</sub> = 75 A; V <sub>GS</sub> = 0 V	- -	0.85 1.1	1.2 -	V V
t <sub>rr</sub>	Reverse recovery time	I <sub>F</sub> = 75 A; -di <sub>F</sub> /dt = 100 A/µs;	-	80	-	ns
Q <sub>rr</sub>	Reverse recovery charge	V <sub>GS</sub> = -10 V; V <sub>R</sub> = 30 V	-	0.35	-	µC

### AVALANCHE LIMITING VALUE

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
W <sub>DSS</sub>	Drain-source non-repetitive unclamped inductive turn-off energy	I <sub>D</sub> = 35 A; V <sub>DD</sub> ≤ 25 V; V <sub>GS</sub> = 10 V; R <sub>GS</sub> = 50 Ω; T <sub>mb</sub> = 25 °C	-	-	120	mJ

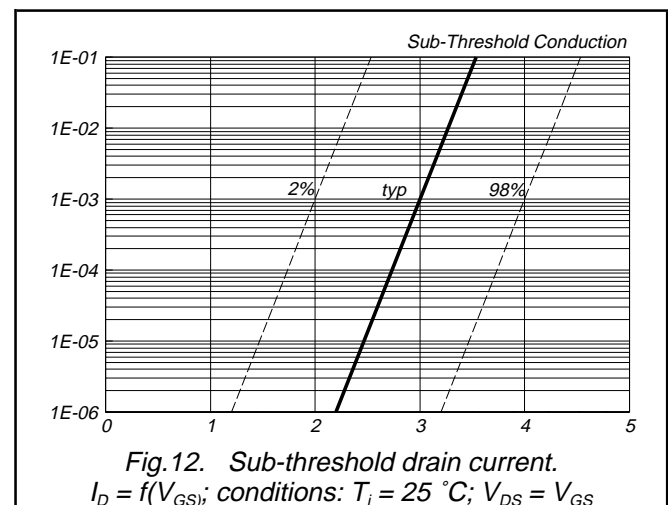
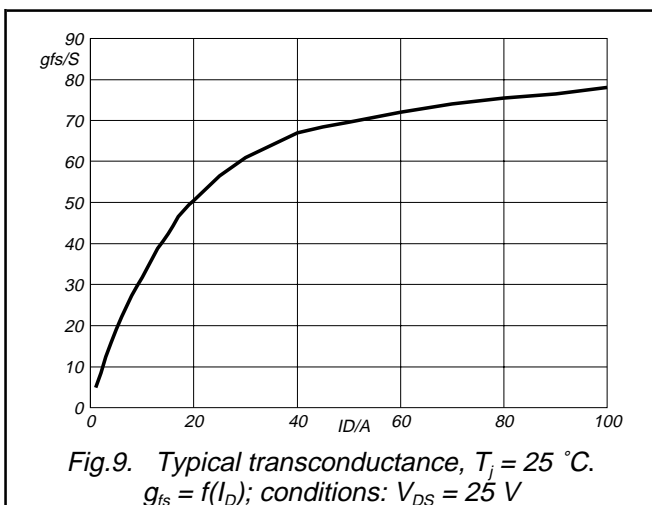
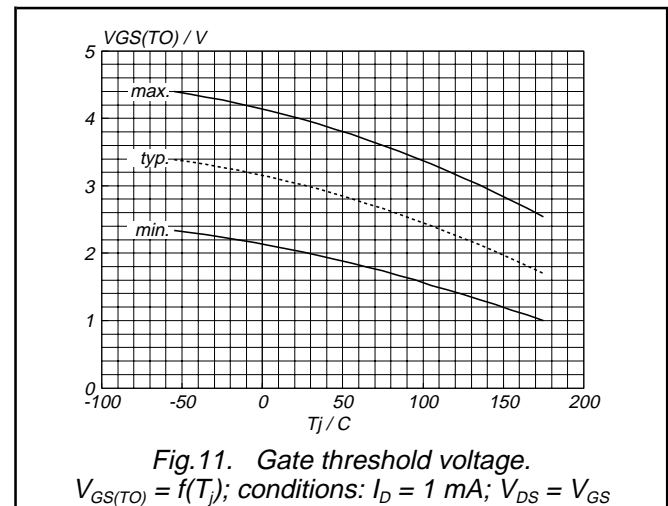
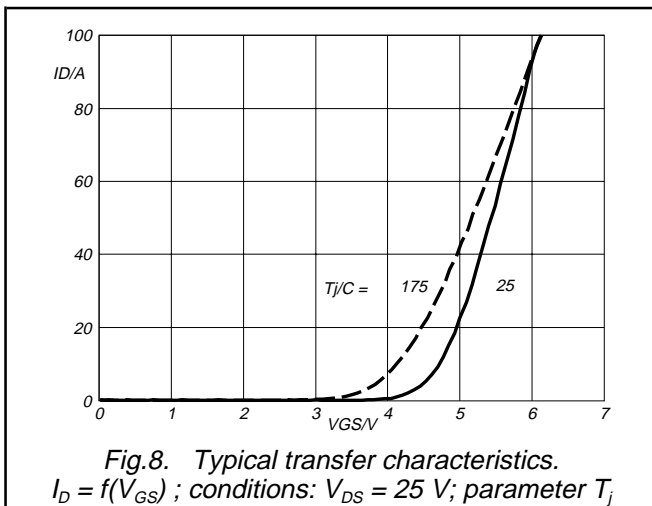
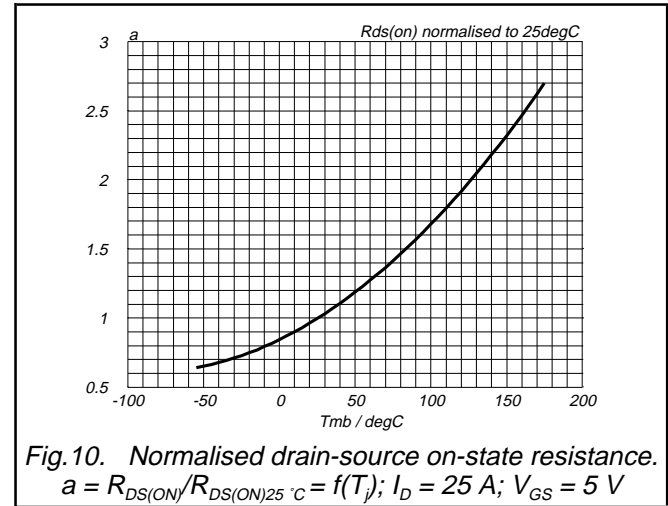
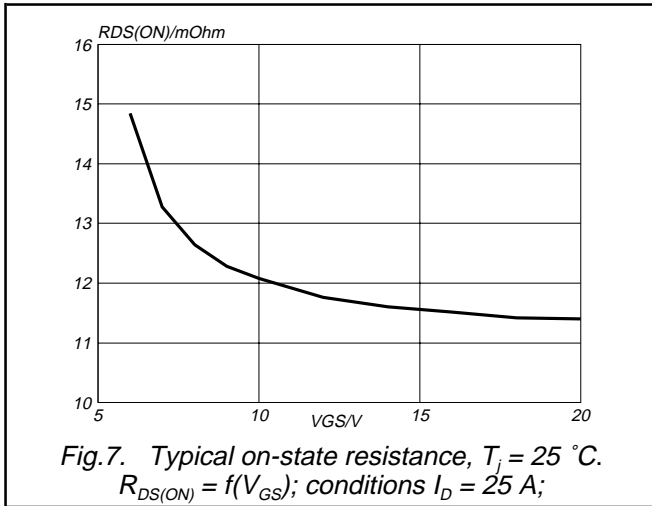
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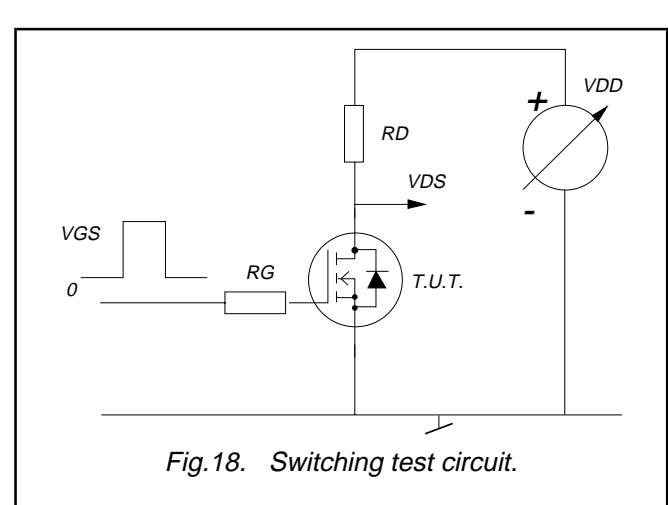
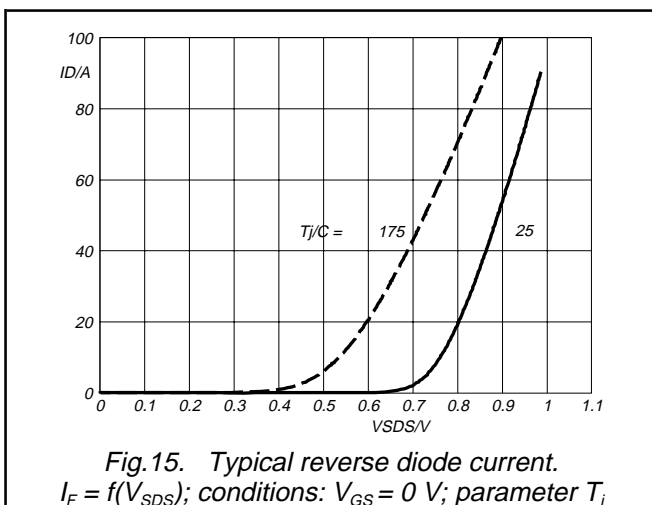
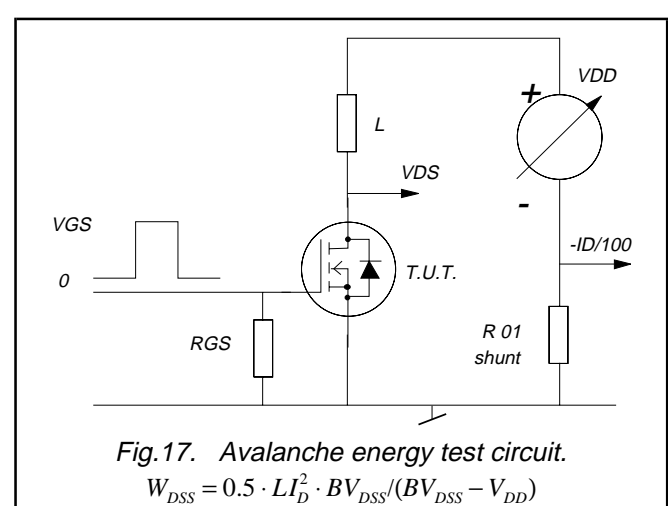
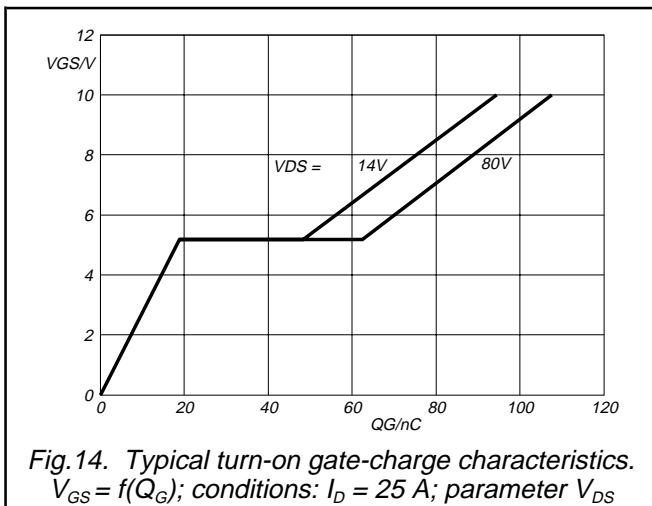
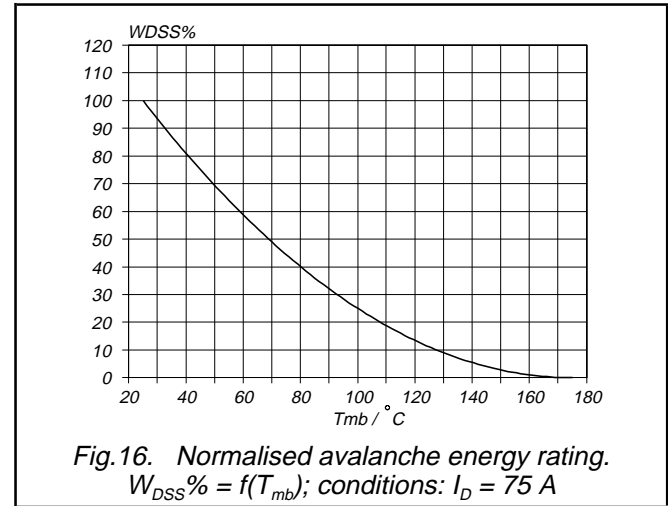
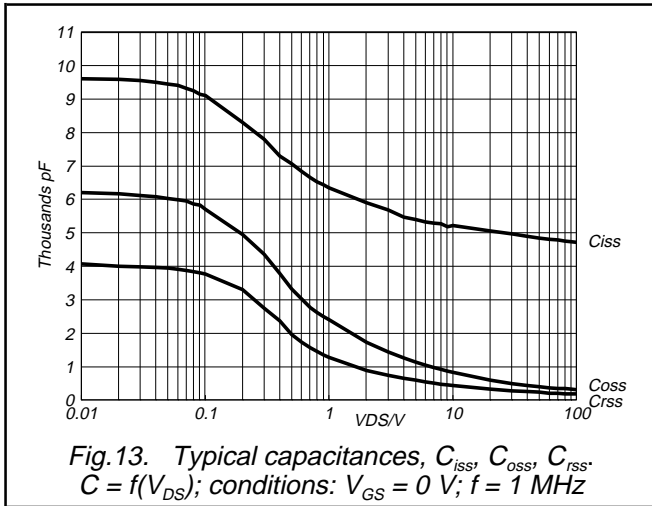
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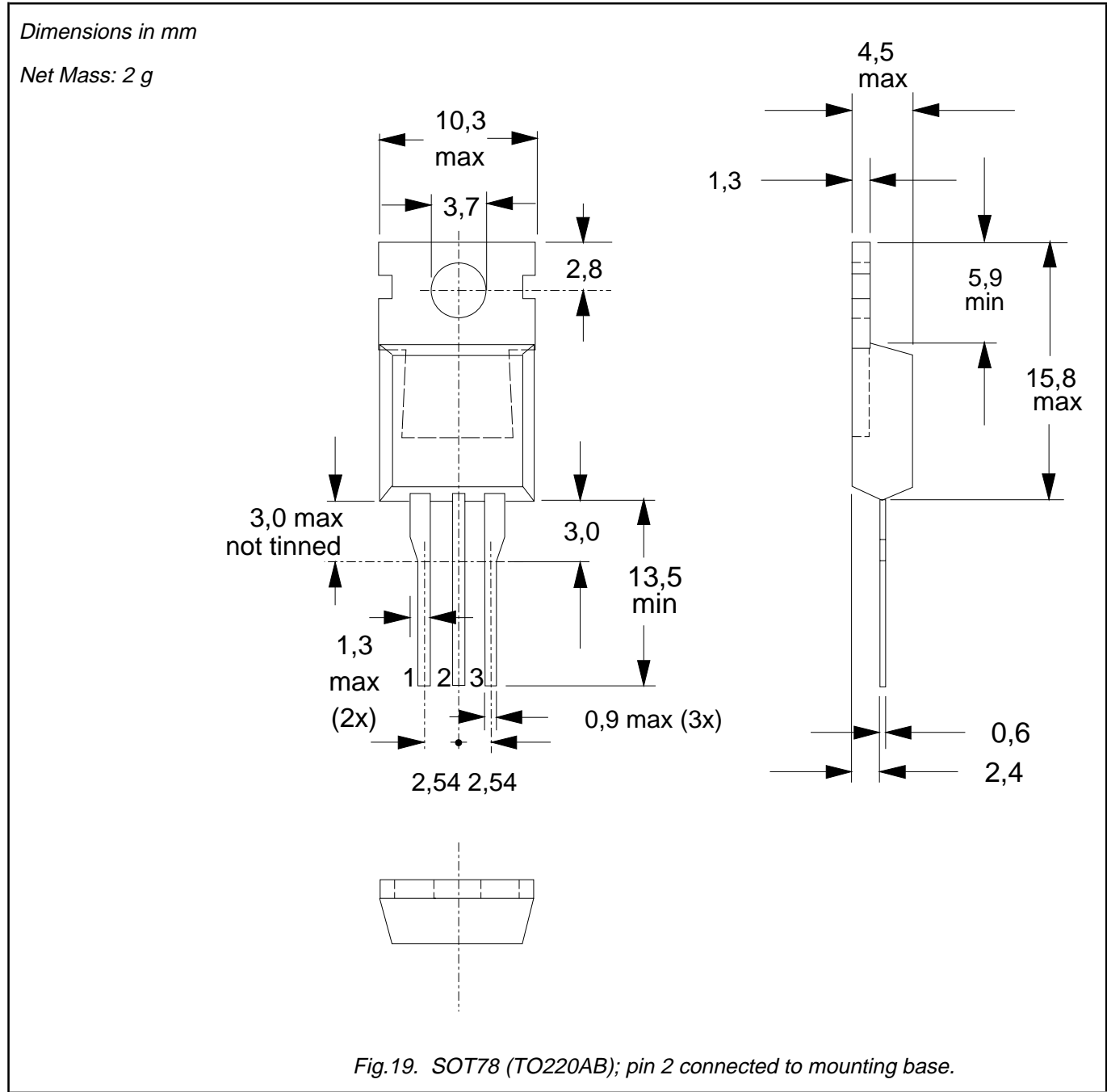
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**MECHANICAL DATA**



**Notes**

1. Observe the general handling precautions for electrostatic-discharge sensitive devices (ESDs) to prevent damage to MOS gate oxide.
2. Refer to mounting instructions for SOT78 (TO220) envelopes.
3. Epoxy meets UL94 V0 at 1/8".

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**DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	
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