

General Description

The UM3016 is the highest performance trench N-ch MOSFETs with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The UM3016 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

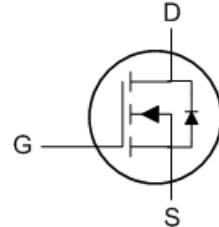
Product Summary

BVDSS	RD_{S(ON)}	ID
30V	4mΩ	15A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

SOP8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	15	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	12	A
I _{DM}	Pulsed Drain Current ²	75	A
EAS	Single Pulse Avalanche Energy ³	317	mJ
I _{AS}	Avalanche Current	53.8	A
P _D @T _A =25°C	Total Power Dissipation ⁴	1.5	W
T _{STG}	Storage Temperature Range	-55 to 175	°C
T _J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	85	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	24	°C/W

N-Ch 30V Fast Switching MOSFETs
Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.021	---	$\text{V}/^\circ\text{C}$
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{GS}=10\text{V}$, $I_D=15\text{A}$	---	3.4	4	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=10\text{A}$	---	5.2	6	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1.0	1.5	2.5	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	-5.73	---	$\text{mV}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5\text{V}$, $I_D=15\text{A}$	---	26.5	---	S
R_g	Gate Resistance	$V_{DS}=10\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	1.4	2.8	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=20\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=12\text{A}$	---	31.6	---	nC
Q_{gs}	Gate-Source Charge		---	6.1	---	
Q_{gd}	Gate-Drain Charge		---	13.8	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=15\text{V}$, $V_{GS}=10\text{V}$, $R_G=1.5\Omega$	---	11.2	---	ns
T_r	Rise Time		---	49	---	
$T_{d(off)}$	Turn-Off Delay Time		---	35	---	
T_f	Fall Time		---	7.8	---	
C_{iss}	Input Capacitance	$V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	3075	---	pF
C_{oss}	Output Capacitance		---	400	---	
C_{rss}	Reverse Transfer Capacitance		---	315	---	

Guaranteed Avalanche Characteristics

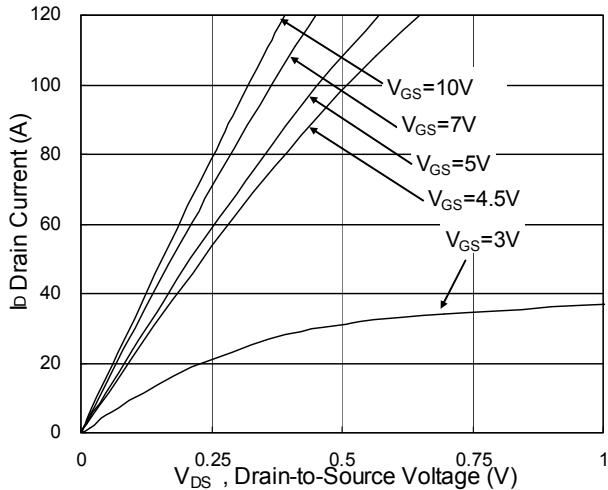
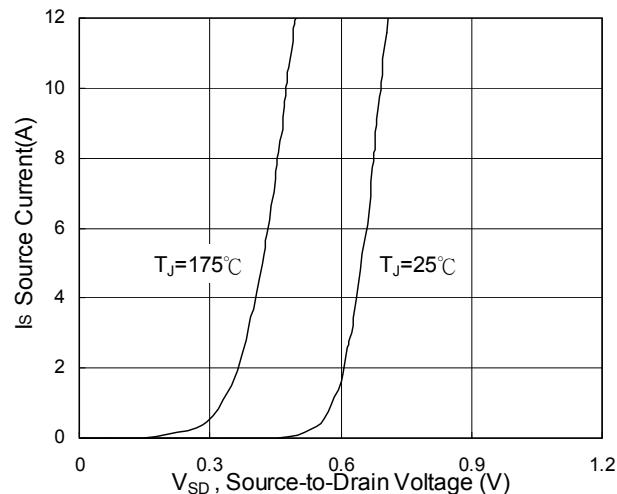
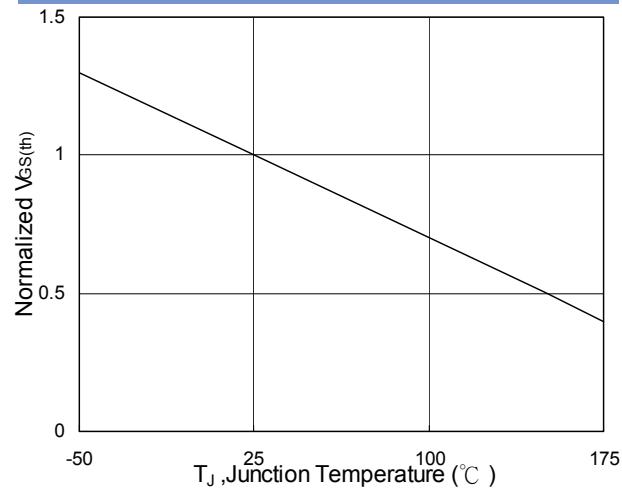
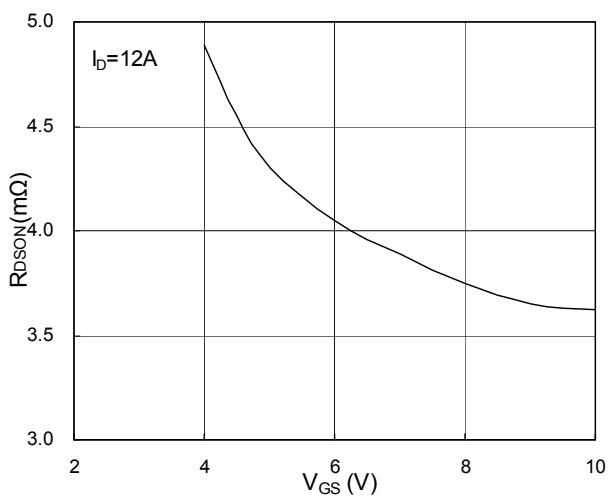
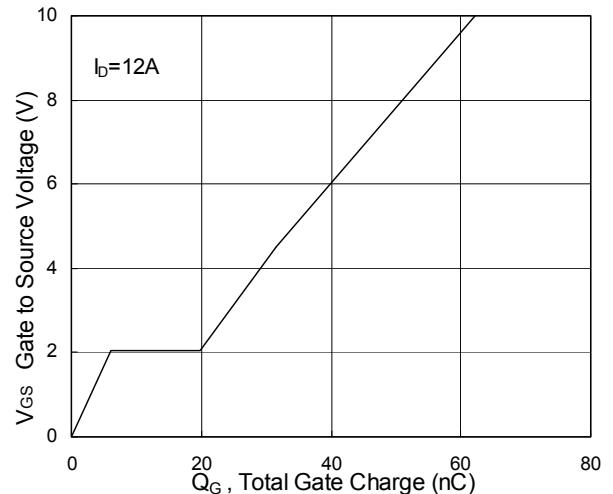
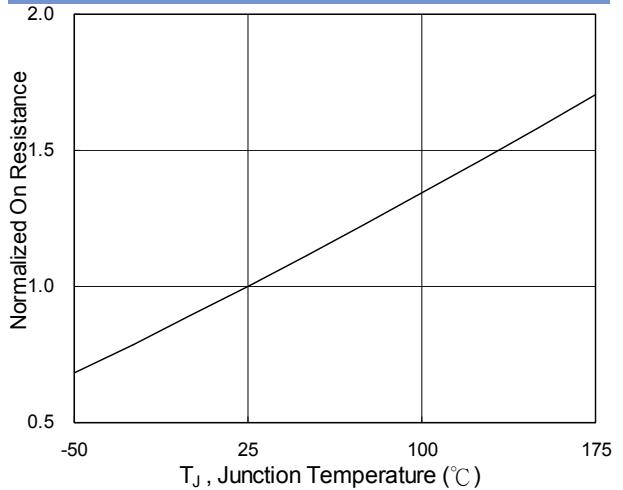
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	$V_{DD}=25\text{V}$, $L=0.1\text{mH}$, $I_{AS}=30\text{A}$	98	---	---	mJ

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,6}	$V_G=V_D=0\text{V}$, Force Current	---	---	15	A
I_{SM}	Pulsed Source Current ^{2,6}		---	---	75	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=53.8\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

Fig.1 Typical Output Characteristics

Fig.3 Forward Characteristics of Reverse

Fig.5 Normalized $V_{GS(th)}$ vs. T_J
N-Ch 30V Fast Switching MOSFETs

Fig.2 On-Resistance vs. G-S Voltage

Fig.4 Gate-charge Characteristics

Fig.6 Normalized $R_{DS(on)}$ vs. T_J

