



ELECTRONICS

Samsung Secret

Approval

Model No. :
Parts Code : V33A00031400

Customer : G/A

DATE : 22. Feb. 2010

SAMSUNG TFT-LCD
MODEL : LTA320AP12

The Information Described in this Specification is Preliminary and can be changed without prior notice

NOTE :

Customer's Approval	
SIGNATURE	DATE

APPROVAED BY <i>Kyungwon Ko</i>	DATE 22.Feb.2010
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LCD Business

Samsung Electronics Co . , LTD.

Revision History ----- (3)

General Description ----- (4)

General Information ----- (4)

1. Absolute Maximum Ratings ----- (5)

2. Optical Characteristics ----- (6)

3. Electrical Characteristics ----- (9)

 3.1 TFT LCD Module

 3.2 Back Light Unit

 3.3 Inverter Input & Specification

4. Input Terminal Pin Assignment ----- (12)

 4.1 Input Signal & Power

 4.2 Inverter Input Pin Configuration

 4.3 Inverter Input Power Sequence

 4.4 LVDS Interface

 4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

5. Interface Timing ----- (17)

 5.1 Timing Parameters (DE only mode)

 5.2 Timing Diagrams of interface Signal (DE only mode)

 5.3 Power ON/OFF Sequence

6. Outline Dimension ----- (20)

7. Reliability test ----- (22)

8. Packing ----- (23)

9. Marking & Others ----- (24)

10. General Precaution ----- (25)

 10.1 Handling

 10.2 Storage

 10.3 Operation

 10.4 Operation Condition Guide

 10.5 Others

*** Revision History**

Date	Rev. No	Page	Summary
Dec 10, 2009	000	all	First Issued
Jan 15, 2010	000	17	Correction of wrong information on Vtotal 1380 → 1200
Feb 09, 2010	000	17	Correction of wrong information on Vtotal 773 → 774
Feb 22, 2010	001	19	Power On/Off Sequence change '0<T2≤50msec' to '540msec<T2'

General Description

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Description

LTA320AP12 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 32.0" is 1366 x 768 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

Features

- RoHS compliance (Pb-free)
- High contrast & aperture ratio with wide color gamut
- APVA(Advanced Patterned Vertical Align) mode
- Wide viewing angle ($\pm 178^\circ$)
- High speed response
- HD resolution (16:9)
- Low Power consumption
- U-Type 4 CCFLs (Cold Cathode Fluorescent Lamp)
- DE(Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (1pixel/clock)

General Information

Items	Specification	Unit	Note
Module Size	760.0(H) x 450.0(V) x 50.5(D)[Max]	mm	± 1.0 mm
Weight	5,500	g	Max
Pixel Pitch	0.51075(H) x 0.51075(V)	mm	
Active Display Area	697.6845(H) x 392.256(V)	mm	
Surface Treatment	Haze 7 %, Hard-Coating 3H		
Display Colors	8 bit - 16.7M	colors	
Number of Pixels	1366 x 768	pixel	
Pixel Arrangement	RGB Horizontal stripe		
Display Mode	Normally Black		
Luminance of White	450 (Typ.)	cd/m ²	

MODEL

LTA320AP12

Doc. No

06- 001- S- 100222

Page

4/27

1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V _{DD}	GND-0.5	13.2	V	(1)
Dimming control	Max. Lum	-	5	V	(1)
Storage temperature	T _{STG}	-20	60	°C	(2)
Surface temperature	T _{SUR}	0	60	°C	(3)
Operation temperature	T _{OPR}	0	50	°C	(2)
Shock (non - operating)	S _{nop}	-	50	G	(4)
Vibration (non - operating)	V _{nop}	-	1.5	G	(5)

Note (1) Ta= 25 ± 2 °C

(2) Temperature and relative humidity range are shown in the figure below.

- a. 90 % RH Max. (Ta ≤ 39 °C)
- b. Relative Humidity is 90% or less. (Ta > 39 °C)
- c. No condensation

(3) Although abnormal visual problems can be occurred in Tsur range, the polarizer is not damaged in this range. —

(4) 11ms, sine wave, one time for ± X, ± Y, ± Z axis

(5) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

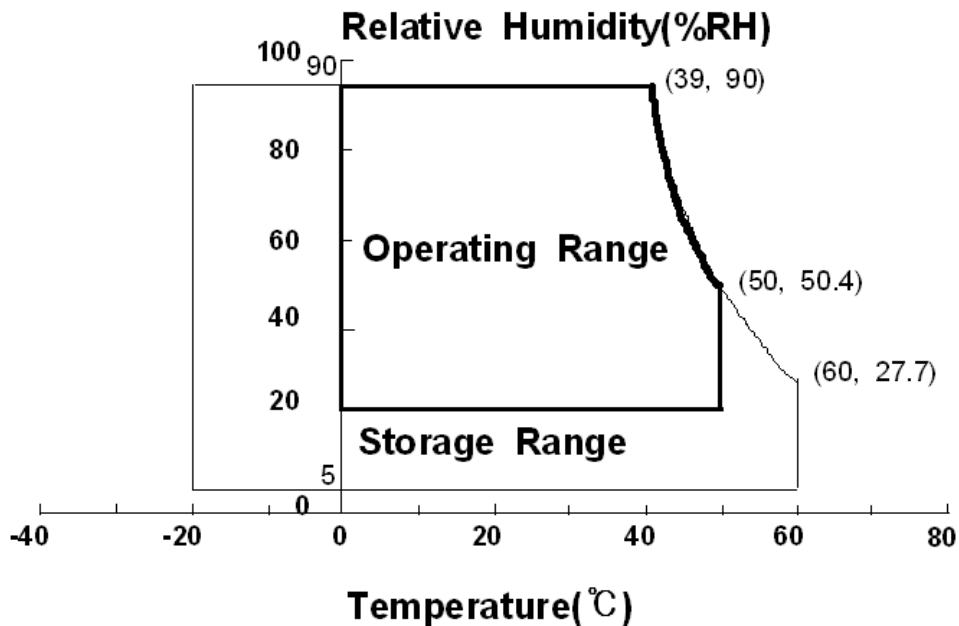


Fig. Temperature and Relative humidity range

2. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent.

Measuring equipment : TOPCON RD-80S, TOPCON SR-3, ELDIM EZCONTRAST

($T_a = 25 \pm 2^\circ\text{C}$, $V_{DD} = 12.0\text{V}$, $f_v = 60\text{Hz}$, $f_{DCLK} = 78\text{MHz}$, Dimming = 3.3V)

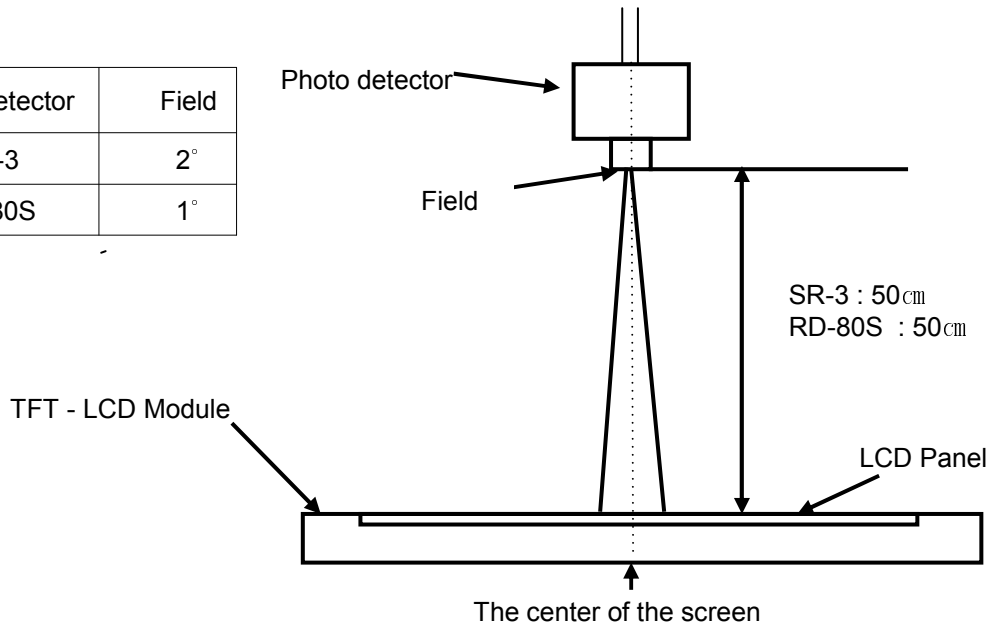
Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast Ratio (Center of screen)	C/R	Normal $\theta_{L,R}=0$ $\theta_{U,D}=0$ Viewing Angle	3000	4000	-		(1) SR-3	
Response Time	G-to-G		Tg	-	8	-	msec	(3) RD-80S
Luminance of White (Center of screen)	Y_L			360	450	-	cd/m ²	(4) SR-3
Color Chromaticity (CIE 1931)	Red		Rx	TYP. -0.03	0.633	TYP. +0.03		(5),(6) SR-3
			Ry		0.332			
	Green		Gx		0.287			
			Gy		0.606			
	Blue		Bx		0.147			
			By		0.060			
	White		Wx		0.280			
		Wy	0.290					
Color Gamut	-	-	72	-	%	(5) SR-3		
Color Temperature	-	-	10,000	-	K	(5) SR-3		
Viewing Angle	Hor.	θ_L	75	89	-	Degree	(6) EZ-Contrast	
		θ_R	75	89	-			
	Ver.	θ_U	75	89	-			
		θ_D	75	89	-			
Brightness Uniformity (9 Points)	B_{uni}		-	-	25	%	(2) SR-3	

- Test Equipment Setup

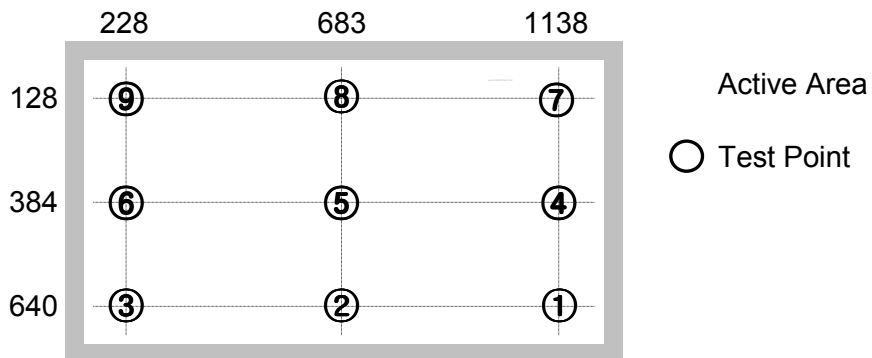
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

Environment condition : $T_a = 25 \pm 2^\circ\text{C}$

Photo detector	Field
SR-3	2°
RD-80S	1°



- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G \text{ max}}{G \text{ min}}$$

Gmax : Luminance with all pixels white

Gmin : Luminance with all pixels black

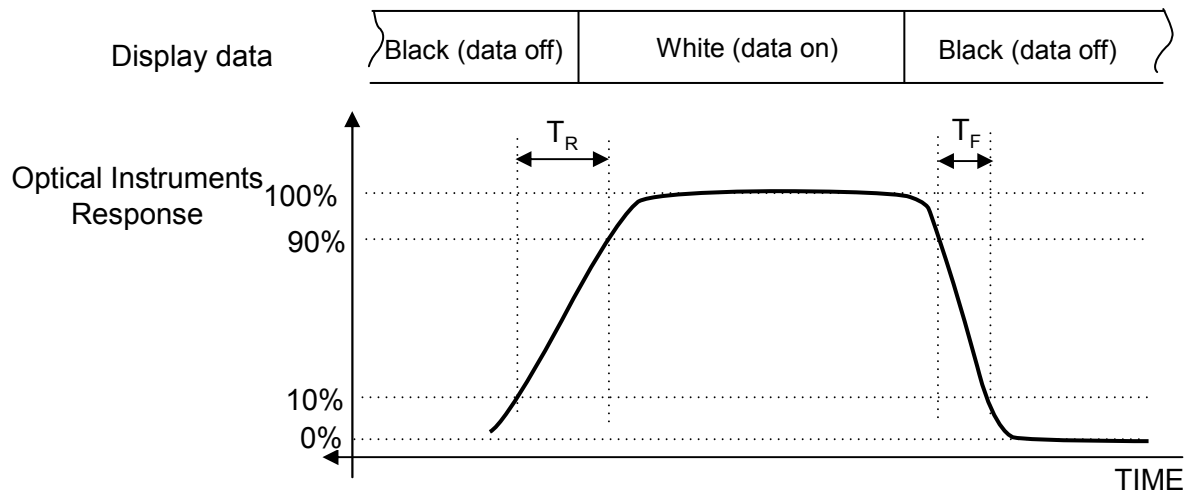
Note (2) Definition of 9 points brightness uniformity (Test Pattern : Full White)

$$B_{uni} = 100 * \frac{(B_{max} - B_{min})}{B_{max}}$$

Bmax : Maximum brightness

Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf



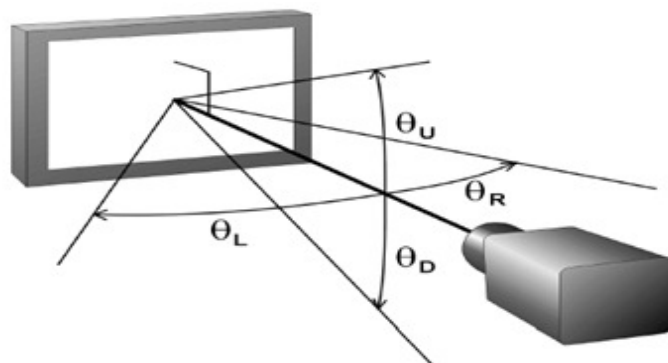
Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)

Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle

: Viewing angle range ($C/R \geq 10$)



3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

$T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$

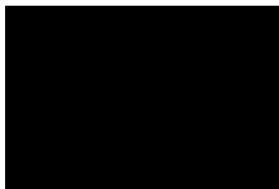
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply	V_{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	-	340	-	mA	(2),(3)
	(b) White	-	341	-	mA	
	(c) N-Pattern	-	550	665	mA	
Vsync Frequency	f_V	48	60	66	Hz	
Hsync Frequency	f_H	44	48	53	kHz	
Main Frequency	f_{DCLK}	72	78	85	MHz	
Rush Current	I_{RUSH}	-	-	4	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD} .

(2) $f_V=60\text{Hz}$, $f_{DCLK} = 78 \text{ MHz}$, $V_{DD} = 12.0\text{V}$, DC Current.

(3) Power dissipation check pattern (LCD Module only)

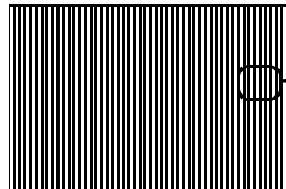
a) Black Pattern



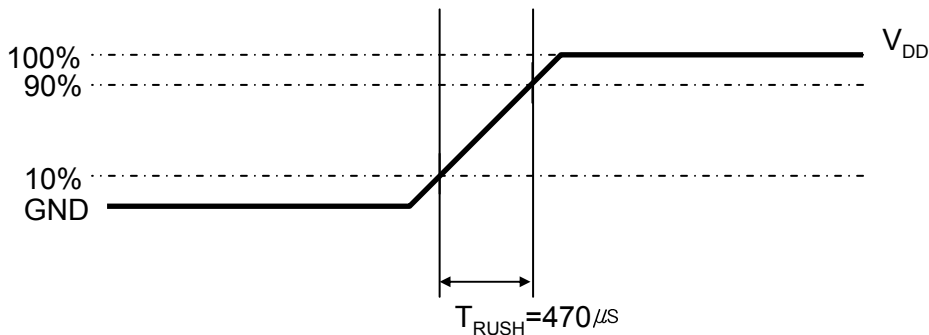
b) White Pattern



c) N-Pattern



(4) Measurement Conditions

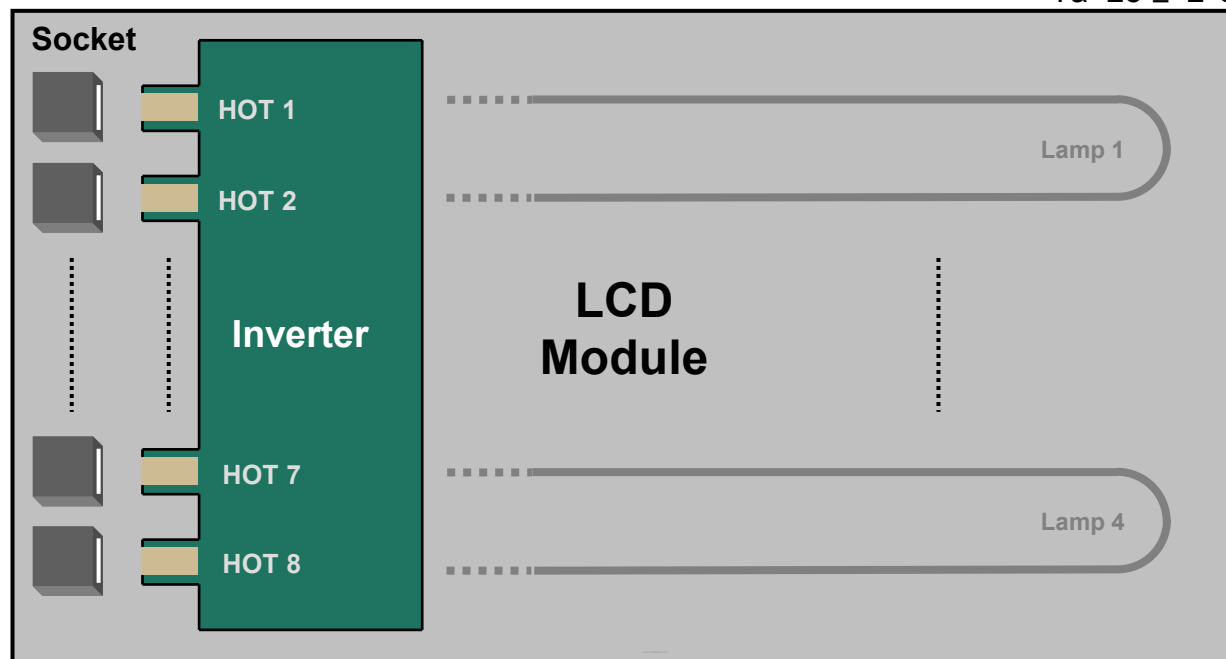


Rush Current I_{RUSH} can be measured when T_{RUSH} is $470\mu\text{s}$.

3.2 Back Light Unit

The back light unit contains 4 direct-lighting U-type CCFLs (Cold Cathode Fluorescent Lamp).

Ta=25 ± 2°C



Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	50,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : Ta = 25± 2°C , For single lamp only.]

3.3 Inverter Input Condition & Specification

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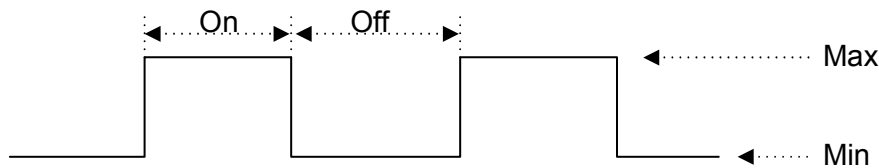
Items	Symbol	Conditions	Specifications			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	V _{in}	-	22	24	26	V	Ta=25± 2 °C (2)
Input Current	I _{OVER}	Vin=24.0V Vdim =3.3V	-	2.37	2.53	A	(1)
	I _{SAT}		-	2.21	2.35		
Lamp Current	I _{O,MAX}	Vdim =3.3 V	-	8.2	-	mArms	(2)
Frequency	F _{LAMP}	Vin=24.0 V	61	63	65	kHz	-
Backlight On/Off	ON	Vin=24.0 V	2.4	-	5.25	V	(3)
	OFF	Vin=24.0 V	0	-	0.8		
Dimming Control	V _{DIM}	Max Lum	3.3	-	-	V	(3)
		Min. Lum	-	-	0		
PWM Voltage	V _{PWM}	Max	3.0	-	5.0	V	(3)
		Min	0.0	-	0.4		
PWM Frequency	F _{PWM}	Vin=24.0 V	120	-	180	Hz	
PWM Duty	Duty	Vin=24.0 V	20	-	100	%	(4)

Note) Power Consumption is measured when 450 [cd/m] of luminance which is the typical luminance.

Lamp Current is measured at the point before Lamp.

- (1) Max Value of the Power Consumption is measured during initial turn on time* of the backlight
- (2) Max Value of the Power Consumption is measured after 60 min warm-up.
- (3) The ripple voltage should be controlled under 10% of Input Signal
- (4) Duty = On/(On+Off) * 100

* Initial turn-on time : From 0sec to 60min after turn-on



4. Input Terminal Pin Assignment

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4.1. Input Signal & Power

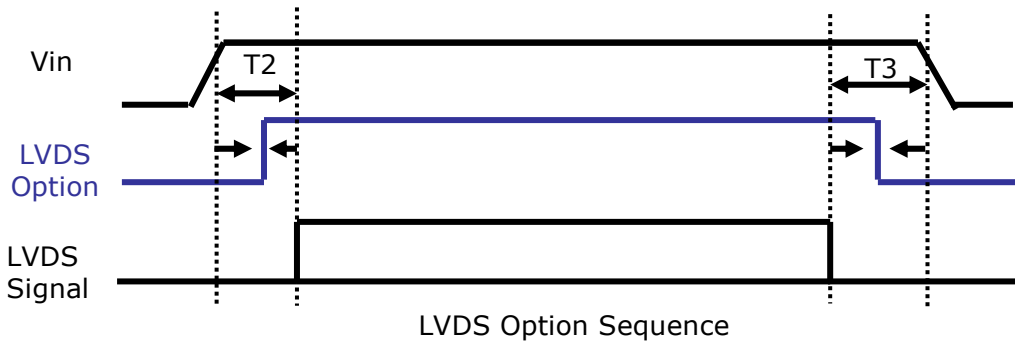
Connector : IS100-L300-G23 or Compatible (UJU)

PIN No.	Description	PIN No.	Description
1	No Connection	16	GND
2	No Connection	17	RxIN3-
3	No Connection	18	RxIN3+
4	GND	19	GND
5	RxIN0-	20	No Connection
6	RxIN0+	21	LVDS OPTION (Note 2)
7	GND	22	No Connection
8	RxIN1-	23	GND
9	RxIN1+	24	GND
10	GND	25	GND
11	RxIN2-	26	Vin
12	RxIN2+	27	Vin
13	GND	28	Vin
14	RxCLK-	29	Vin
15	RxCLK+	30	Vin

Note)(1) No Connection: This PINS are only used for SAMSUNG internal using.

(2) LVDS OPTION : If this PIN : HIGH (3.3 V) → Normal LVDS format
: LOW (GND) → JEIDA LVDS format

SEQUENCE : On = $V_{DD}(T1) \geq \text{LVDS Option} \geq \text{Interface Signal}(T2)$
OFF = $\text{Interface Signal}(T3) \geq \text{LVDS Option} \geq V_{DD}$



Note(3) Pin number starts from Right side

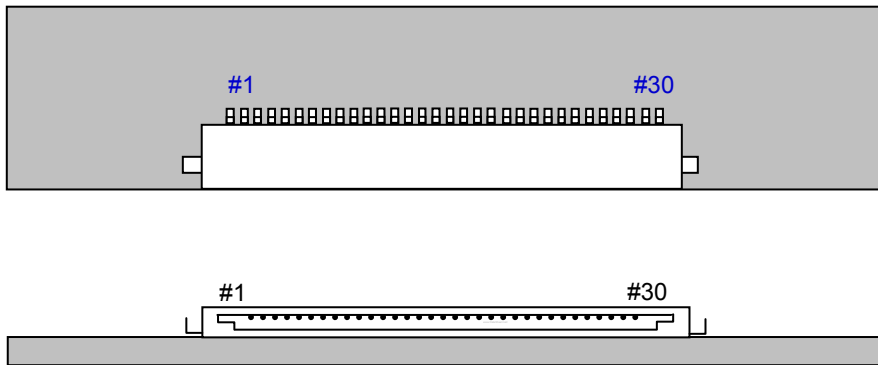
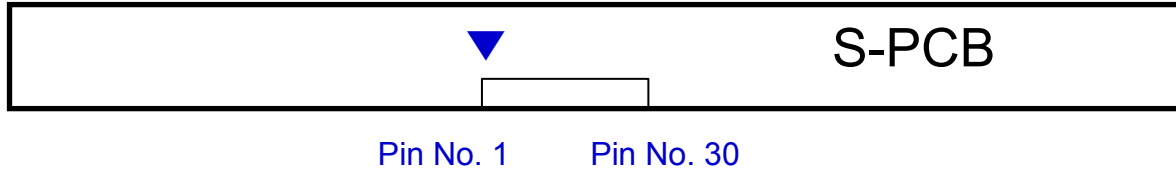


Fig. Connector diagram

- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pins should be separated from other signal or power.

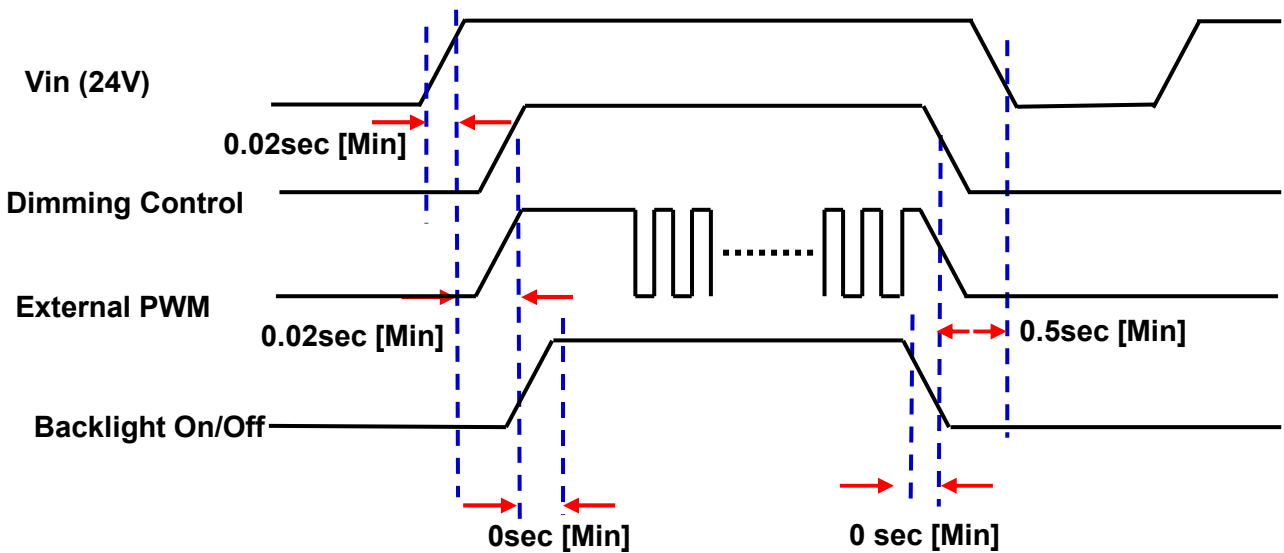
4.2. Inverter Input Pin Configuration

Connector : 20022WR-14B1 (Yeon-ho)

Pin No.	Pin Configuration(FUNCTION)
1	24 V
2	24 V
3	24 V
4	24 V
5	24 V
6	GND
7	GND
8	GND
9	GND
10	GND
11	Error Out [Normal : GND, Abnormal : Open Collector]
12	Backlight On /Off
13	Dimming Control [0V:Min, 3.3V:Max] *Note(1)
14	External PWM [20~100%] *Note(1)

Note(1) If use Dimming Control, Pin 14 Must be N.C
 If use External PWM, Pin 13 Must be N.C

4.3. Inverter Input Power Sequence



Note) SEQUENCE : On = Vin(24V) > Dimming Control ≥ Backlight On/off
 OFF = Backlight On/Off ≥ Dimming Control > Vin(24V)

4.4 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA & Normal)

	LVDS pin	JEIDA -DATA	Normal -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R2	R0
	TxIN/RxOUT1	R3	R1
	TxIN/RxOUT2	R4	R2
	TxIN/RxOUT3	R5	R3
	TxIN/RxOUT4	R6	R4
	TxIN/RxOUT6	R7	R5
	TxIN/RxOUT7	G2	G0
TxOUT/RxIN1	TxIN/RxOUT8	G3	G1
	TxIN/RxOUT9	G4	G2
	TxIN/RxOUT12	G5	G3
	TxIN/RxOUT13	G6	G4
	TxIN/RxOUT14	G7	G5
	TxIN/RxOUT15	B2	B0
	TxIN/RxOUT18	B3	B1
TxOUT/RxIN2	TxIN/RxOUT19	B4	B2
	TxIN/RxOUT20	B5	B3
	TxIN/RxOUT21	B6	B4
	TxIN/RxOUT22	B7	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R0	R6
	TxIN/RxOUT5	R1	R7
	TxIN/RxOUT10	G0	G6
	TxIN/RxOUT11	G1	G7
	TxIN/RxOUT16	B0	B6
	TxIN/RxOUT17	B1	B7
	TxIN/RxOUT23	RESERVED	RESERVED

4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL																								GRAY SCALE LEVEL
		RED							GREEN							BLUE										
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7	
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0	
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R252	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R252	
	LIGHT ↓	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R252	
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R252	
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R252	
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0	
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1	
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	G2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G252	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G252	
	LIGHT ↓	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	G252	
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	G252	
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	G252	
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B1	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	B2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B252	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B252	
	LIGHT ↓	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	B252	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	B252	
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B252	

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

5. Interface Timing

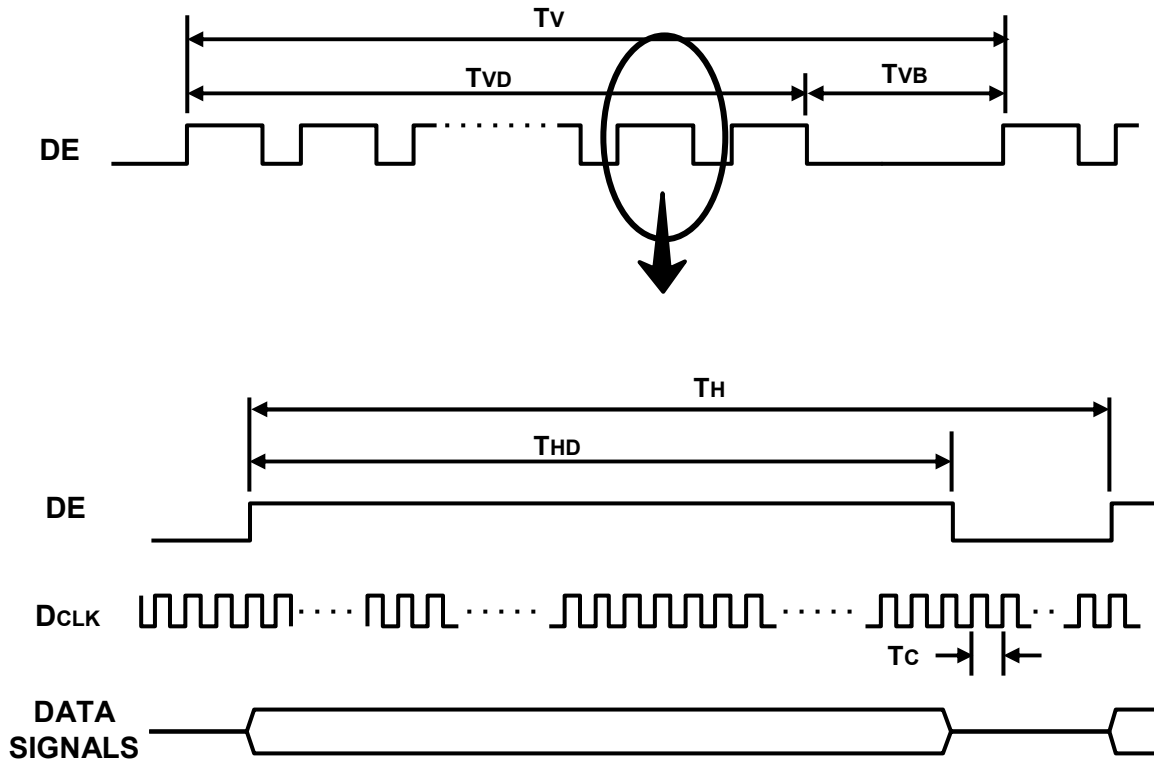
5.1 Timing Parameters (DE only mode)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	72	78	85	MHz	-
Hsync		F_H	44	48	53	KHz	-
Vsync		F_V	48	60	66	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	768	-	Lines	-
	Vertical Total	T_{VB}	774	802	1200	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1366	-	Clocks	-
	Horizontal Total	T_H	1460	1624	2000	clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

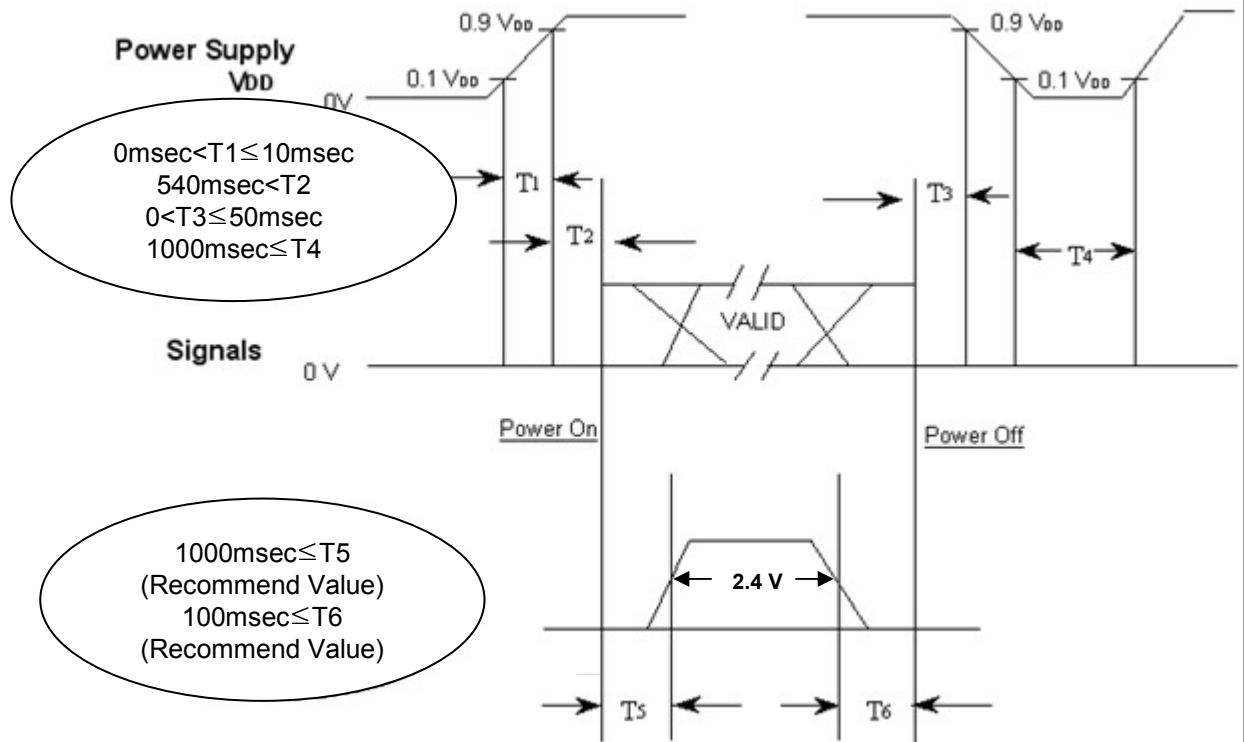
- (1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system
- (2) Internal $V_{DD} = 3.3V$

5.2 Timing diagrams of interface signal (DE only mode)



5.3 Power ON/OFF Sequence

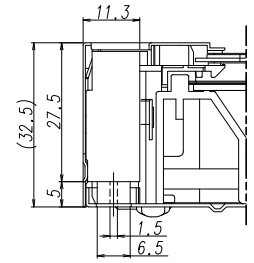
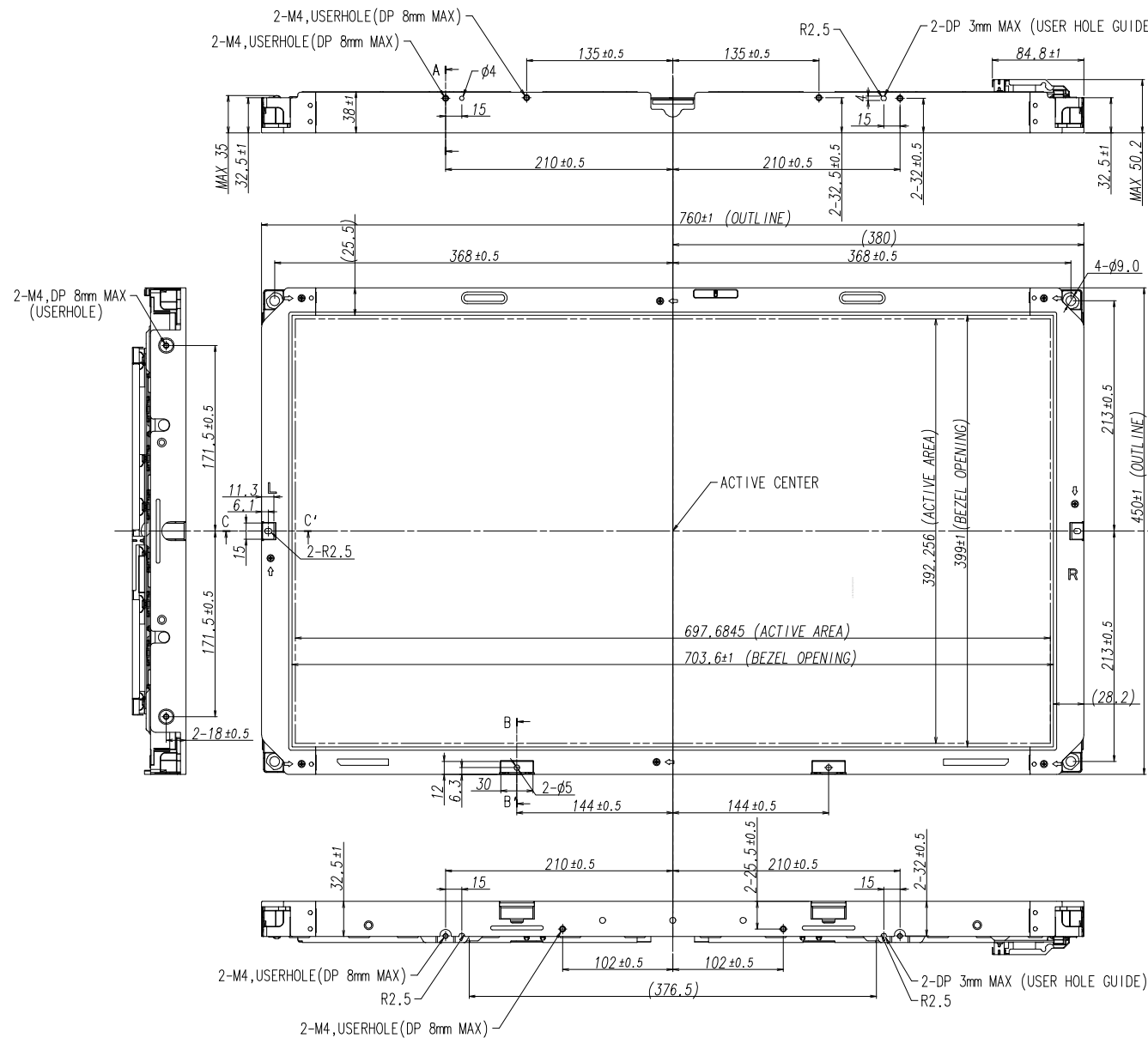
To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



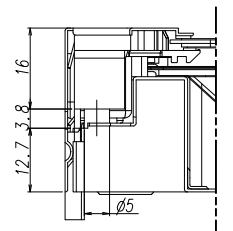
- T1 : V_{DD} rising time from 10% to 90%
- T2 : The time from V_{DD} to valid data at power ON.
- T3 : The time from valid data off to V_{DD} off at power Off.
- T4 : V_{DD} off time for Windows restart
- T5 : The time from valid data to B/L enable at power ON.
- T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD} .
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.
- In case T5 is less than 1000msec and T6 is less than 100msec, Garbage Display can be seen.(It is not related to electrical function issue, Just for recommendation to prevent Garbage Display)

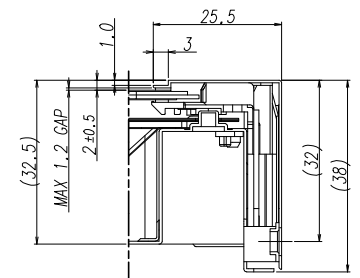
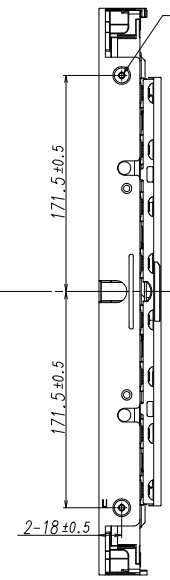
NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	SPEC NO	REMARK
1	OUTLINE DIMENSION	-	LTA320AP12	1		



SECTION C-C'

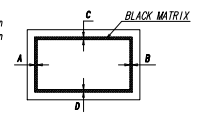


SECTION B-B'



SECTION A-A'

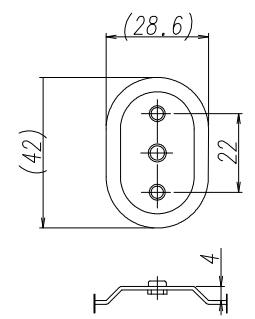
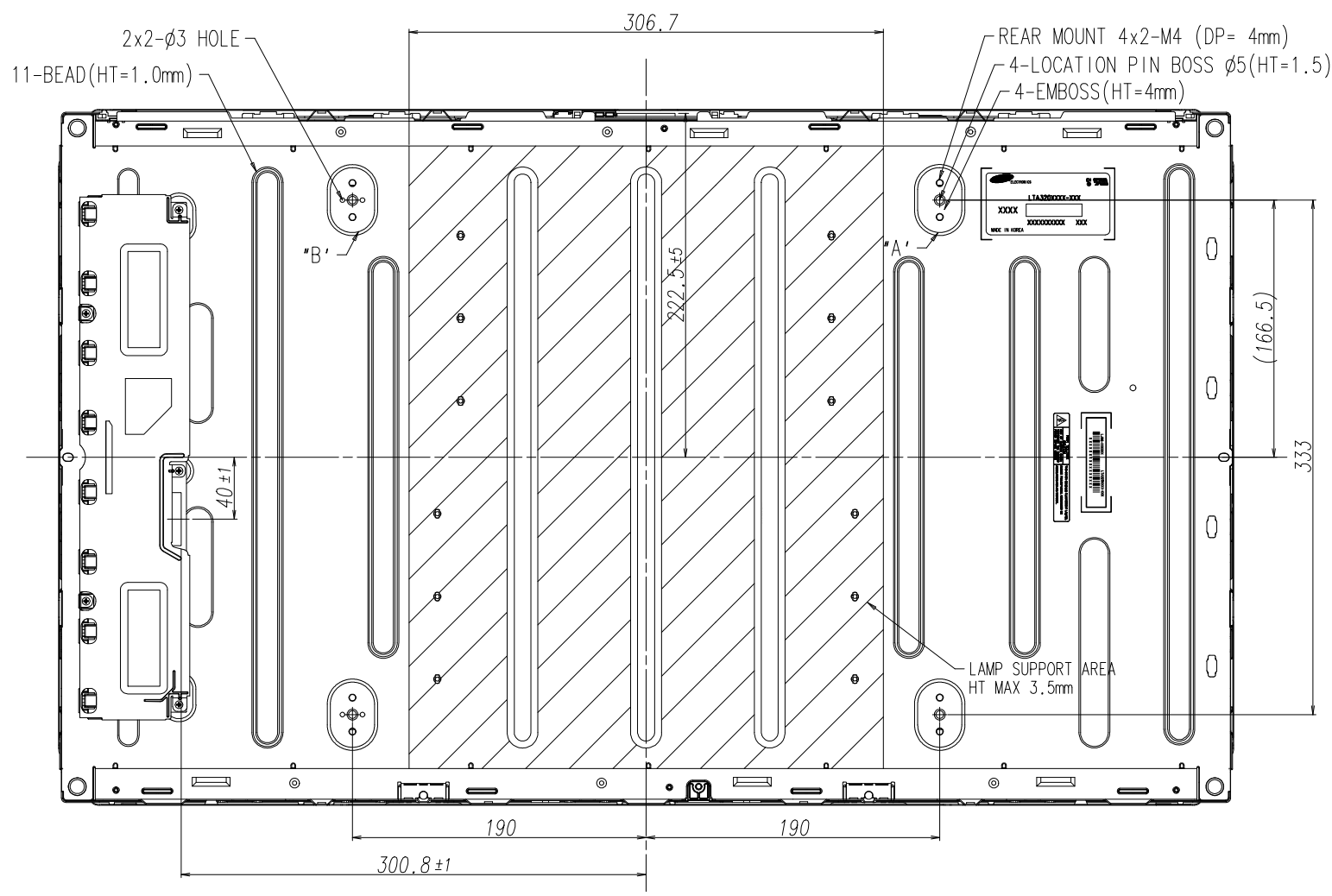
- * NOTES**
- BACKLIGHT : 4U COLD CATHODE FLUORESCENT LAMPS.
 - 1/φ CONNECTOR SPECIFICATION.
 - MAKER : JAE
 - PART NO. : F1-E30S
 - INVERTER CONNECTOR SPECIFICATION
 - MAKER : JST
 - PART NO. : S14B-PHA-SM
 - UNSPECIFIED TOLERANCE TO BE ±0.5.
 - ALLOWED DEPTH OF USERHOLE SCREW INSERTION IS 8.0 MAX
 - CALIFERS MEASURING FORCE : 750 ±250 gf
 - GAP BETWEEN TOP CHASSIS AND GLASS IS 1.2 mm MAX
 - WEIGHT : 5.1 Kg (Max 5.5Kg)
 - BLACK MATRIX SPEC
 - |A - B| ≤ 2.0 mm
 - |C - D| ≤ 2.0 mm



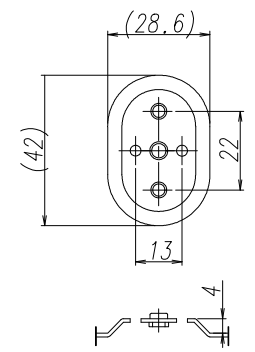
APPROVAL

GENERAL TOLERANCE				REV DATE	DESCRIPTION OF REVISION				REASON	CHK'D BY
STEP	LEVEL 1	LEVEL 2	LEVEL 3	UNIT	mm	DRAWN BY	DES'D BY	CHK'D BY	APP'D BY	MODEL NAME
0 < X < 4	±0.05	±0.1	±0.2	SCALE	1/2	K,R,NO		J,H,OHN		LTA320AP12
4 < X < 16	±0.08	±0.15	±0.3	TOLERANCE				09.12.08	09.12.09	OUTLINE DIM. (FRONT)
16 < X < 64	±0.12	±0.25	±0.5	LEVEL	3					PART/SHEET NAME
64 < X < 256	±0.25	±0.4	±0.8	SPEC. NO		SAMSUNG ELECTRONICS				CODE NO.

NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	SPEC NO	REMARK
	OUTLINE DIMENSION	-	LTA320AP12	1		



DETAIL "A" (2PARTS)



DETAIL "B" (2PARTS)

REVISIONS

APPROVAL

GENERAL TOLERANCE				REV	DATE	DESCRIPTION OF REVISION				REASON		CHG'D BY	
STEP	LEVEL 1	LEVEL 2	LEVEL 3	UNIT	mm	DRA'N BY	DES'D BY	CHK'D BY	APP'D BY	MODEL NAME	LTA320AP12		
0 < X ≤ 4	±0.05	±0.1	±0.2	SCALE	1/2				J.H.CHUN	PART/SHEET NAME	OUTLINE DIM. (REAR)	SHEET 2/2	
4 < X ≤ 16	±0.08	±0.15	±0.3	TOLERANCE			K.R.KO		09.12.09	SPEC. NO			
16 < X ≤ 64	±0.12	±0.25	±0.5	SAMSUNG ELECTRONICS									VER. 001
64 < X ≤ 256	±0.25	±0.4	±0.8										

7. Reliability Test

Samsung Secret

Item	Test condition	Quantity
Temperature Step Stress	-20 ~ 60℃, 40hr, 5 Cycle determination	4EA
HTOL	50℃, 500hr determination	4EA
LTOL	0℃, 500hr determination	4EA
HTS	70℃, 500hr determination	4EA
LTS	-30℃, 500hr determination	4EA
THB	40℃ / 95%RH, 500hr determination	4EA
WHTS	60℃ / 75%RH, 250hr determination	4EA
Thermal Shock	-20℃ ~ 60℃, 200cycle determination	4EA
ESD(operation)	contact : ± 8 kV, 150pF/330Ω, 200Point, 1 time/Point non-contact : ± 15 kV, 150pF/330Ω, 200Point, 1 time/Point	3EA
Inverter Input Con. ESD	contact : ± 2kV, 150pF/330, Input Con.Pin, 3 times/Pin	3EA
Vibration	10~300Hz/1.5G/10minSR, XYZ, 30min/axis	3EA
Shock	Half Sine, 50G, 11msec, ± X,Y,Z 1time/axis	3EA
PALLET Vibration	1.05Grms 5~200Hz 1hr	1PALLET(24EA)
PALLET Drop	4 edge 1face(bottom) 20 cm	1PALLET(24EA)

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these should be no change which may affect practical display functions.

- * HTOL/ LTOL : High/Low Temperature Operating Life
- ** THB : Temperature Humidity Bias
- *** HTS/LTS : High/Low Temperature Storage
- **** WHTS : Wet High Temperature Storage

MODEL

LTA320AP12

Doc. No

06- 001- S- 100222

Page

22/27

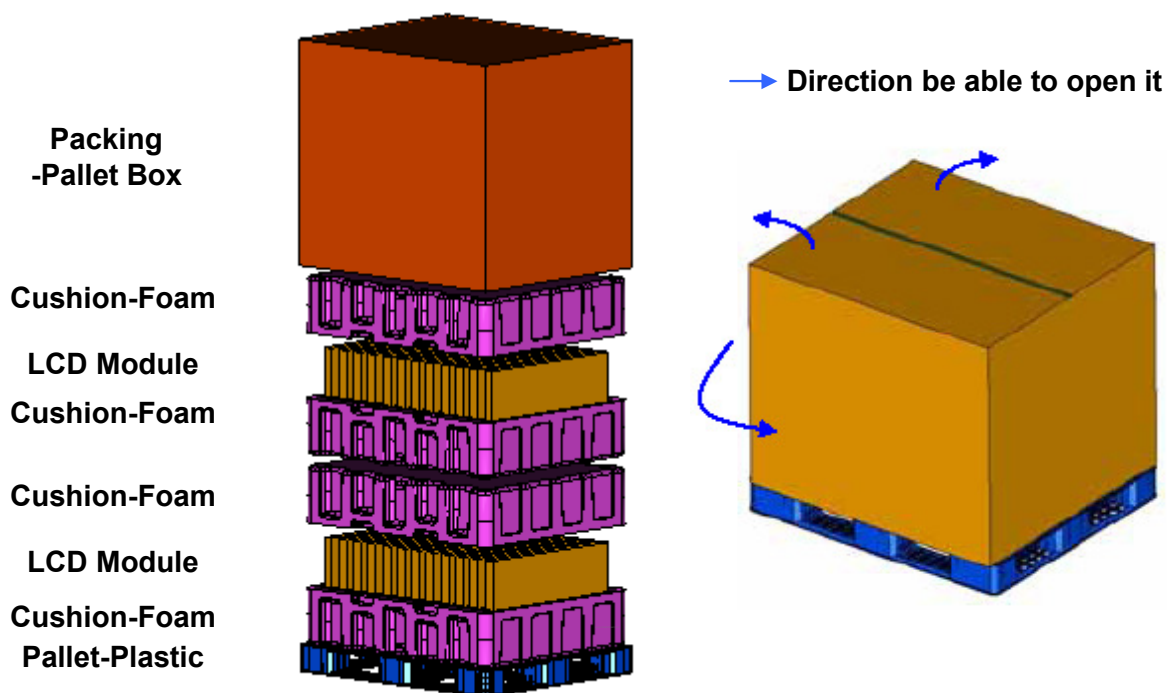
8. PACKING

8.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method



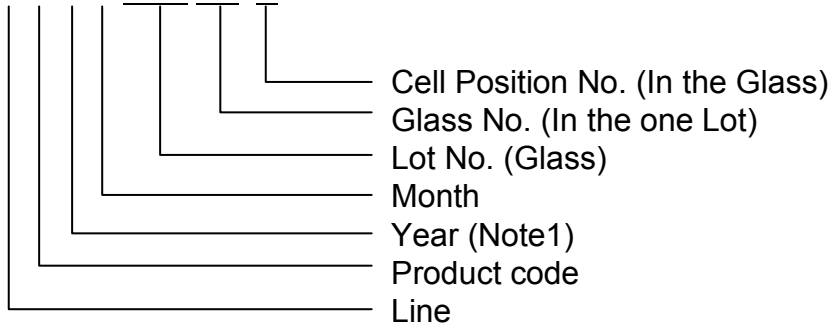
8.2 Packing Specification

Item	Specification	Remark
LCD Packing	24ea / (Packing-Pallet Box)	1. 132 Kg / LCD (24ea) 2. 3.5 Kg / Cushion-pallet (4ea) 3. 8.8 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 8kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1150mm(H) x 985mm(V) x 1611mm(height)
Total Pallet Weight	162.8 kg	Pallet(8kg) + Module(5.5*24=132) + Cushion (up*2+botton*2=14kg) + Pallet-BOX (8.8kg)

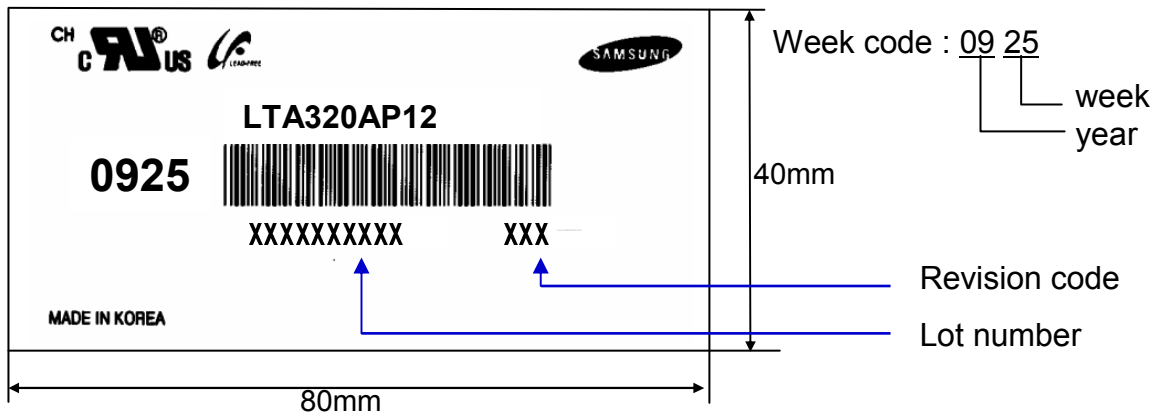
9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

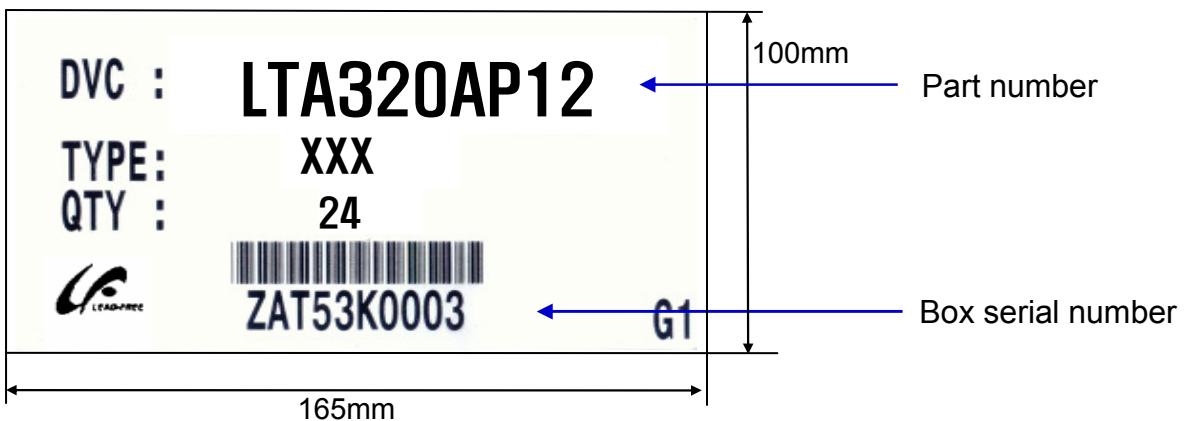
- (1) Part number : LTA320AP12
- (2) Revision: Three letters
- (3) Lot number : X X X X XXX XX X



(4) Nameplate Indication



(5) Packing box attach



(6) Others

- 1. After service part

Lamps cannot be replaced because of the narrow bezel structure.

MODEL	LTA320AP12	Doc. No	06- 001- S- 100222	Page	24/27
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10. General Precautions

10.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module.
In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily.
Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane.
Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (l) Do not pull or fold the lamp wire.
- (m) Do not adjust the variable resistor located on the Module.
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (o) Pins of I/F connector should not be touched directly with bare hands.

10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35 °C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

- (a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
 - Temperature : 20± 15 °C
 - Humidity : 55± 20%
 - Display pattern : continually changing pattern (Not stationary)
- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

10.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.