

**ACE432N****Precision adjustable shunt voltage reference**

## Description

The ACE432N is low-voltage three-terminal adjustable voltage references, with specified thermal stability over applicable industrial and commercial temperature ranges. Output voltage can be set to any value between VREF (1.24V) and 20V with two external resistors. These devices have a typical output impedance of  $0.25\Omega$ . Active output circuitry provides a very sharp turn-on characteristic, making the ACE432N excellent replacements for low-voltage Zener diodes in many applications, including onboard regulation and adjustable power supplies.

## Features

- Low-Voltage Operation --- Down to 1.24V
- Adjustable Output Voltage,  $V_o = V_{ref}$  to 20V
- Low Operational Cathode Current --- 80uA (Typ)
- $0.25\Omega$  Typical Output Impedance

## Application

- Battery Power Equipment
- Linear Regulators
- Switch Power Supply
- Cellular Phone
- Digital Cameras
- Computer Disk Drivers
- Instrumentation

## Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Cathode Voltage	$V_Z$	20	V
Continuous Cathode Current	$I_Z$	100	mA
Reference Current	$I_{REF}$	3	mA
Thermal resistance junction to ambient SOT-23-5L SOT-23-3	$\theta_{JA}$	206 206	°C/W
Operating junction temperature	$T_J$	-40 to 150	°C
Storage temperature range	$T_{STG}$	-65 to 150	°C

The IC has a protection circuit against static electricity. Do not apply high static electricity or high voltage that exceeds the performance of the protection circuit to the IC.

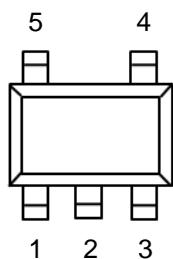


**ACE432N**

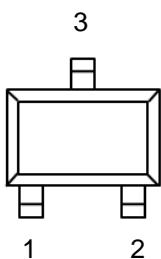
Precision adjustable shunt voltage reference

### Packaging Type

SOT-23-5L



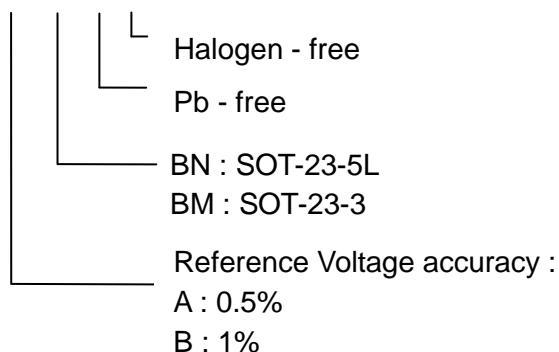
SOT-23-3



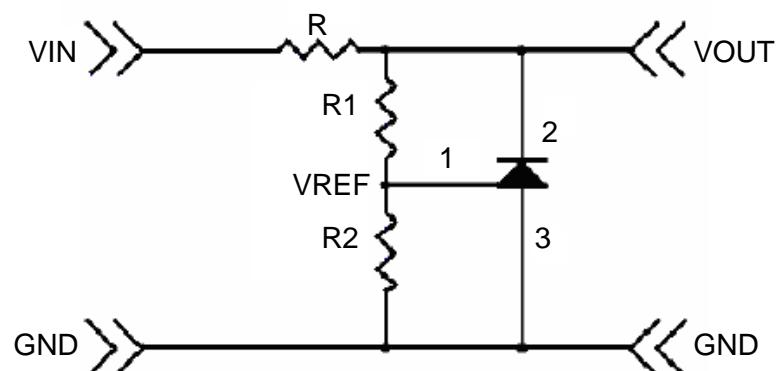
SOT-23-5L	SOT-23-3	Description
3	2	Cathode
5	3	Anode
4	1	Ref
1.2		NC

### Ordering information

ACE432N X XX + H



### Typical Application Circuit

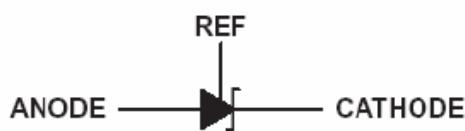
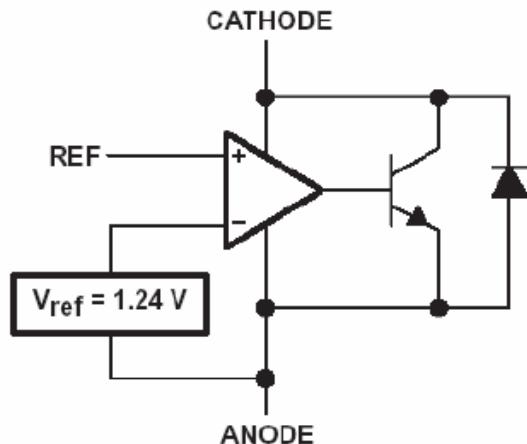




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## Block Diagram



## Electrical Characteristics

Parameter	Symbol	Test Conditions		Min.	Typ.	Max.	Unit
Reference Voltage	$V_{REF}$	$V_Z=V_{REF}$ , $I_Z=10mA$	$T_A=25^\circ C$	1.234	1.24	1.246	V
			$T_A=-40^\circ C \sim 80^\circ C$	1.222		1.258	
			$T_A=25^\circ C$	1.228	1.24	1.252	
			$T_A=-40^\circ C \sim 80^\circ C$	1.215		1.265	
$V_{REF}$ Temp Deviation	$V_{DEV}$	$V_Z=V_{REF}$ , $I_Z=10mA$ $T_A=-40^\circ C \sim 80^\circ C$			10	25	mV
Ratio of change in $V_{REF}$ to change in cathode voltage	$\Delta V_{REF}/\Delta V_Z$	$I_Z=10mA$ $\Delta V_Z=16V$ to $V_{REF}$			-1.0	-2.7	mV/V
Reference Input current	$I_{REF}$	$I_Z=10mA$ , $R1=10K\Omega$ , $R2=\infty$			0.15	0.5	$\mu A$
IREF Temp Deviation	$I_{REF(DEV)}$	$I_Z=10mA$ , $T_A=-40^\circ C \sim 80^\circ C$ $R1=10K\Omega$ , $R2=\infty$			0.1	0.4	$\mu A$
Off-state cathode current	$I_{Z(OFF)}$	$V_{REF}=0$	$V_Z=6V$		0.5	1.0	$\mu A$
Dynamic output impedance	$R_Z$	$I_Z=1mA \sim 100mA$ $V_Z=V_{REF}$ , $f \leq 1KHz$			0.25	0.4	$\Omega$
Minimum Operation Current	$I_{Z(MIN)}$	$V_Z=V_{REF}$			30	80	$\mu A$



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### Application Circuit

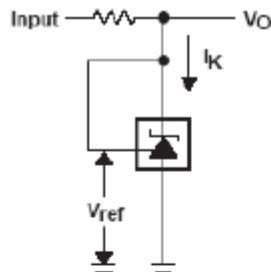


Figure 1. Test Circuit for  $V_{KA}=V_{REF}$

$$V_O=V_{KA}=V_{REF}$$

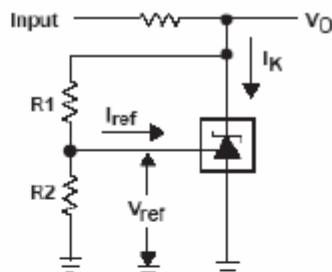


Figure 2. Test Circuit for  $V_{KA}>V_{REF}$ ,

$$V_O=V_{KA}=V_{REF}\cdot 1(1+R1/R2)+I_{REF}\cdot R1$$

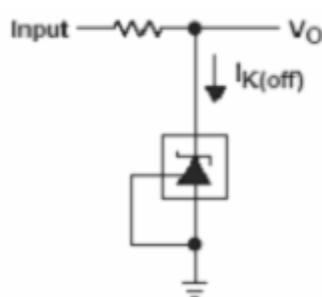


Figure 3. Test Circuit for  $I_{K(OFF)}$

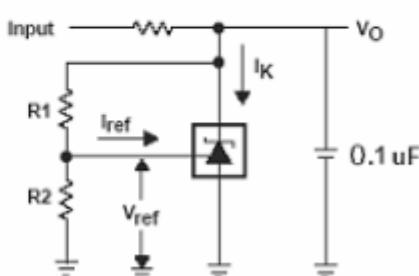


Figure 4. Test Circuit for  $V_{KA}>V_{REF}$ ,

$$V_O=V_{KA}=V_{REF}\cdot 1(1+R1/R2)+I_{REF}\cdot R1$$

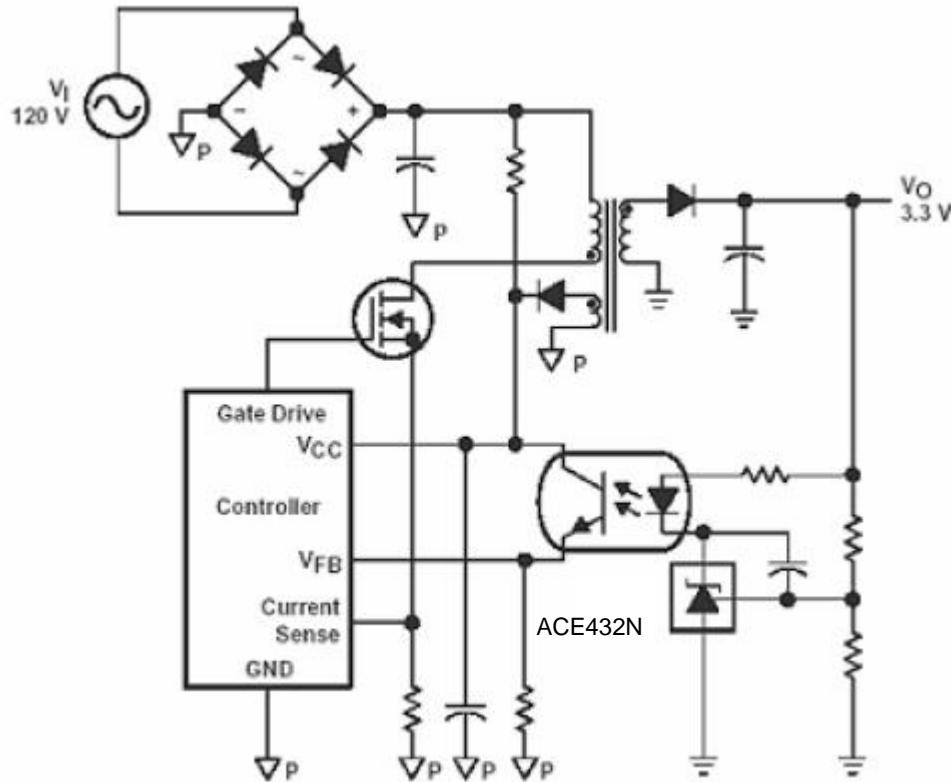


Figure 5. Flyback with isolation using ACE432N as voltage reference and error amplifier

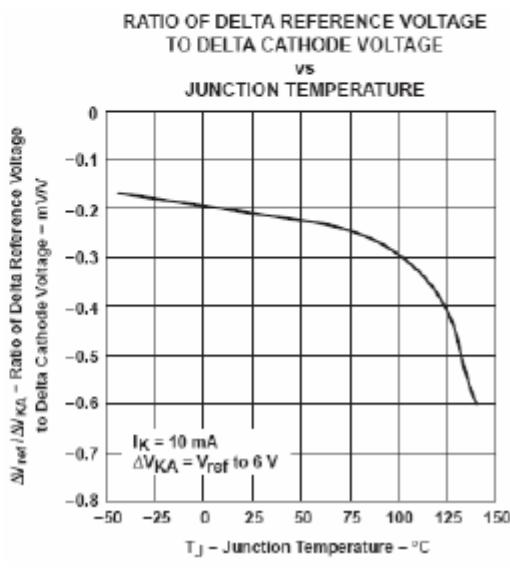
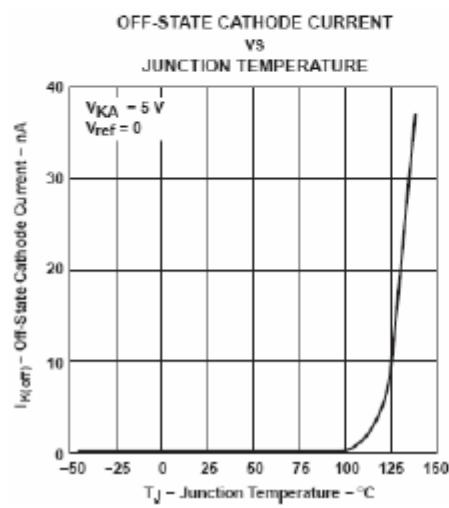
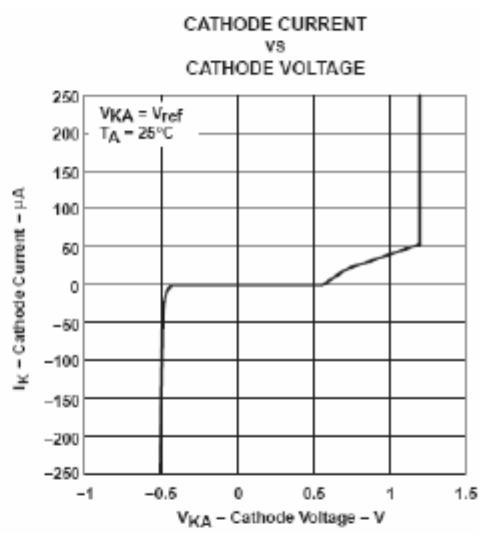
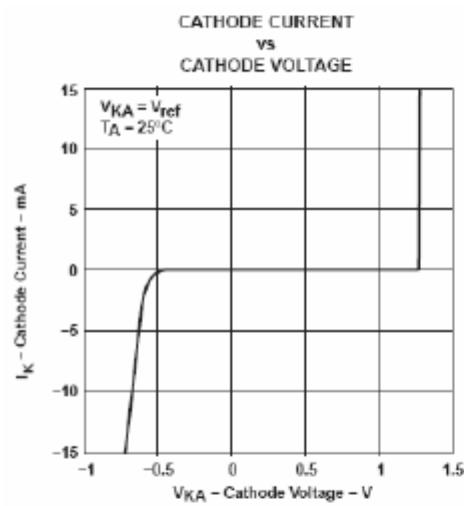
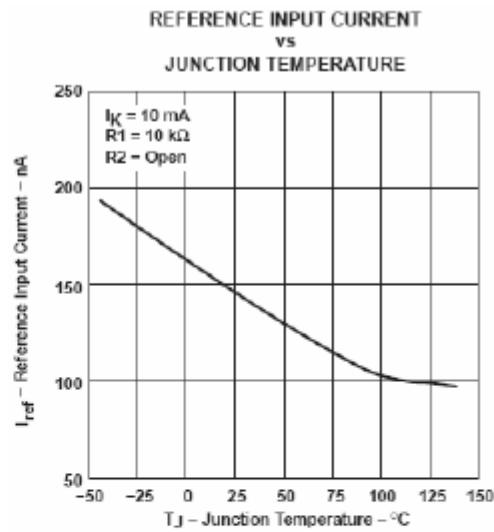
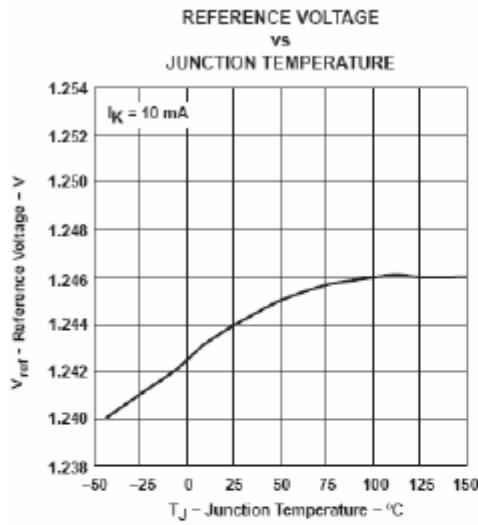
\* To improve the stability of output voltage, Figure 4, a 0.1uF capacitor is recommended between cathode to anode.



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### Performance characteristics

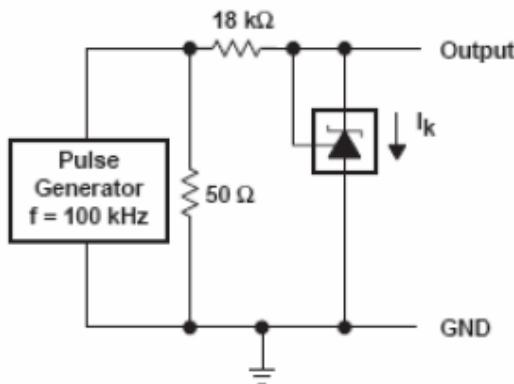
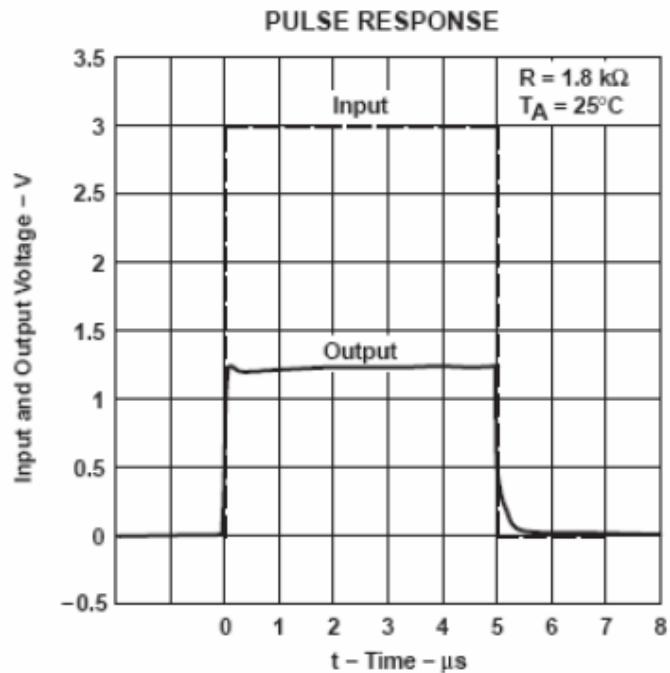
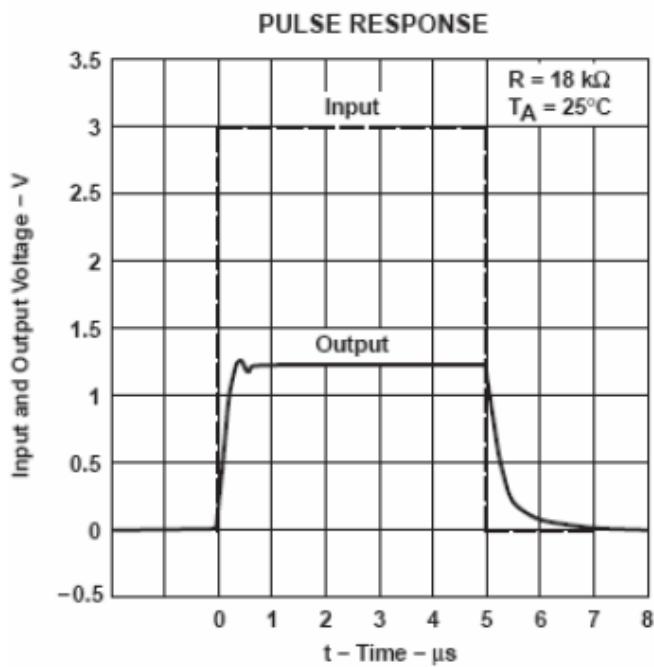




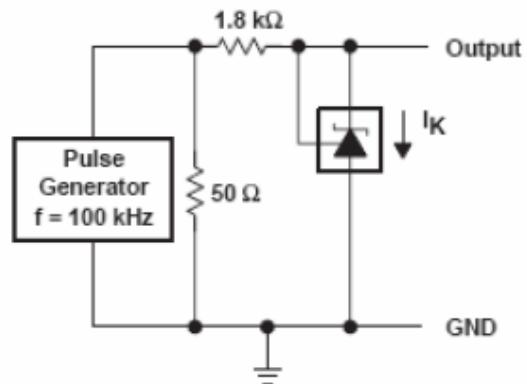
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Precision adjustable shunt voltage reference

## Performance Characteristics



TEST CIRCUIT FOR PULSE RESPONSE



TEST CIRCUIT FOR PULSE RESPONSE

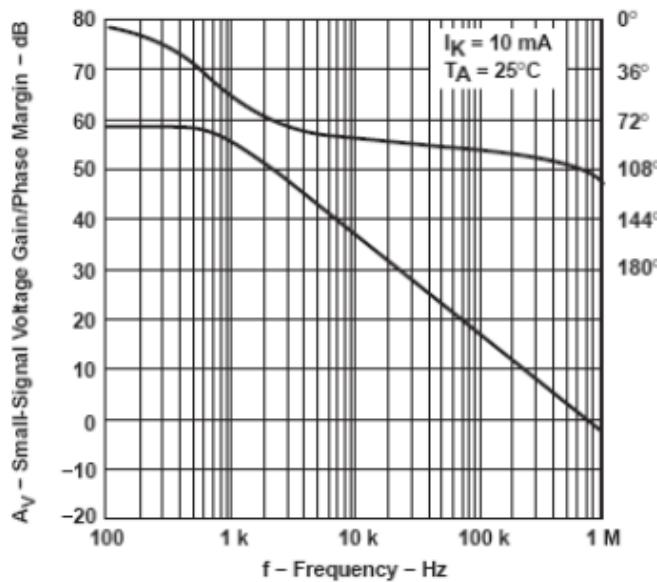


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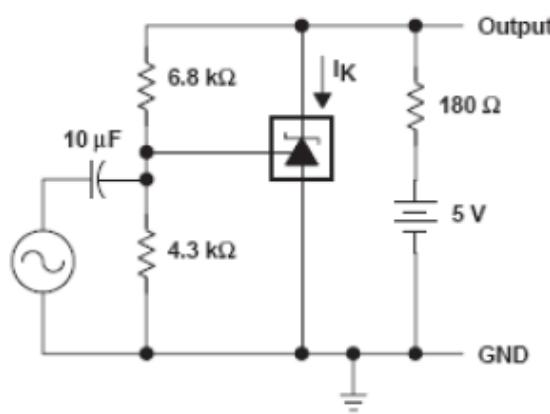
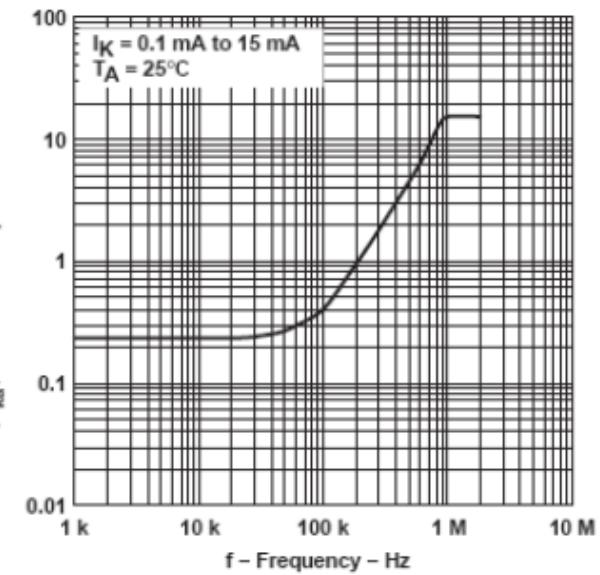
Precision adjustable shunt voltage reference

## Performance Characteristics

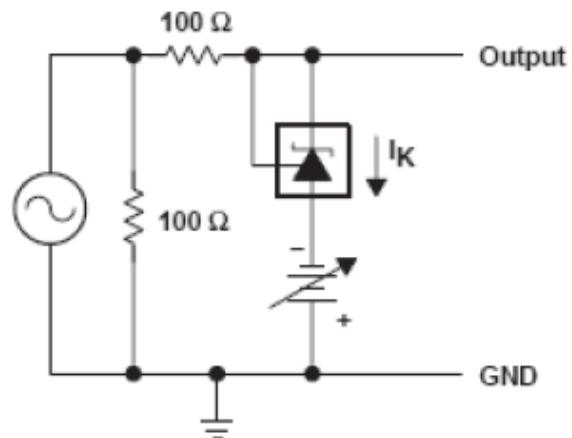
SMALL-SIGNAL VOLTAGE GAIN/PHASE MARGIN  
VS  
FREQUENCY



REFERENCE IMPEDANCE  
VS  
FREQUENCY



TEST CIRCUIT FOR VOLTAGE GAIN  
AND PHASE MARGIN



TEST CIRCUIT FOR REFERENCE IMPEDANCE

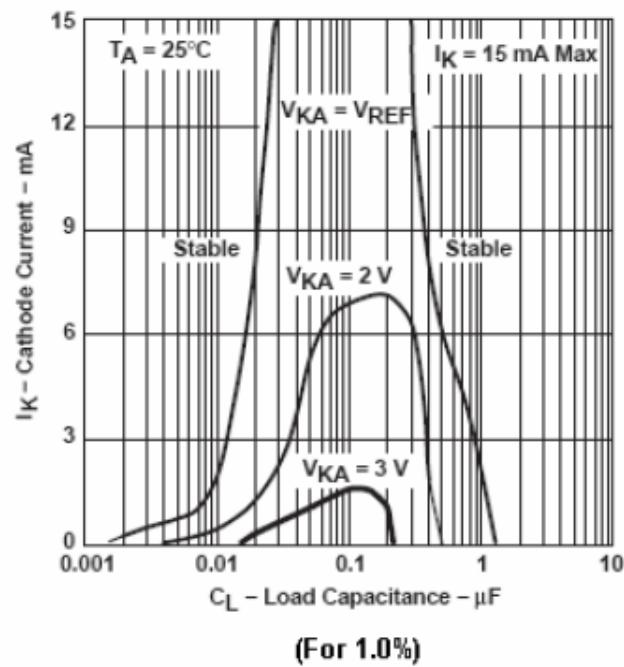


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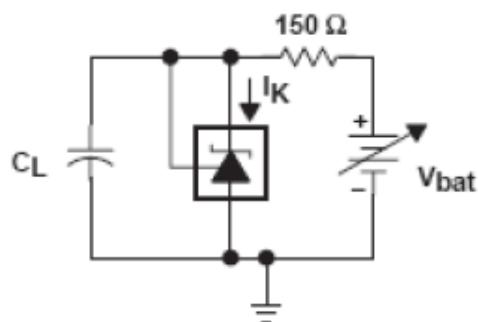
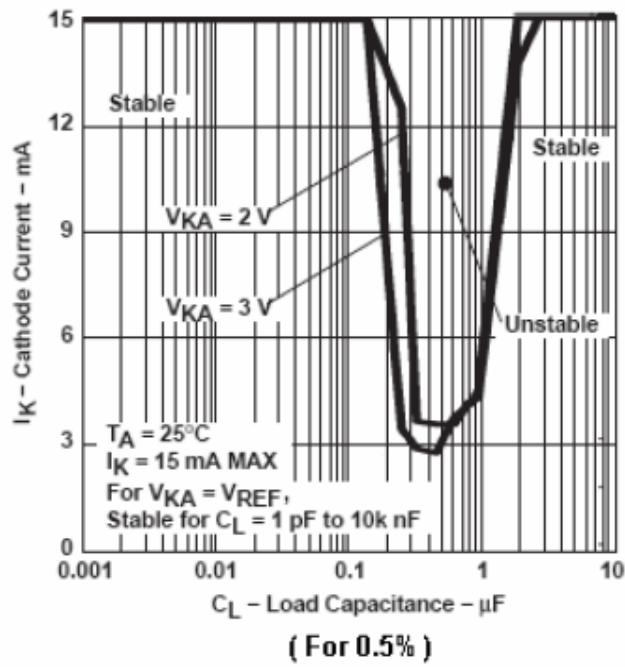
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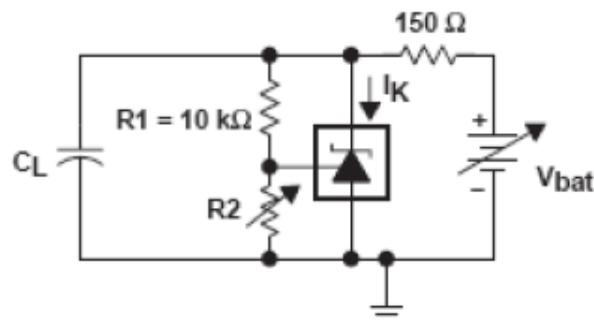
STABILITY BOUNDARY CONDITION



STABILITY BOUNDARY CONDITION‡



TEST CIRCUIT FOR  $V_{KA} = V_{REF}$



TEST CIRCUIT FOR  $V_{KA} = 2 \text{ V}, 3 \text{ V}$

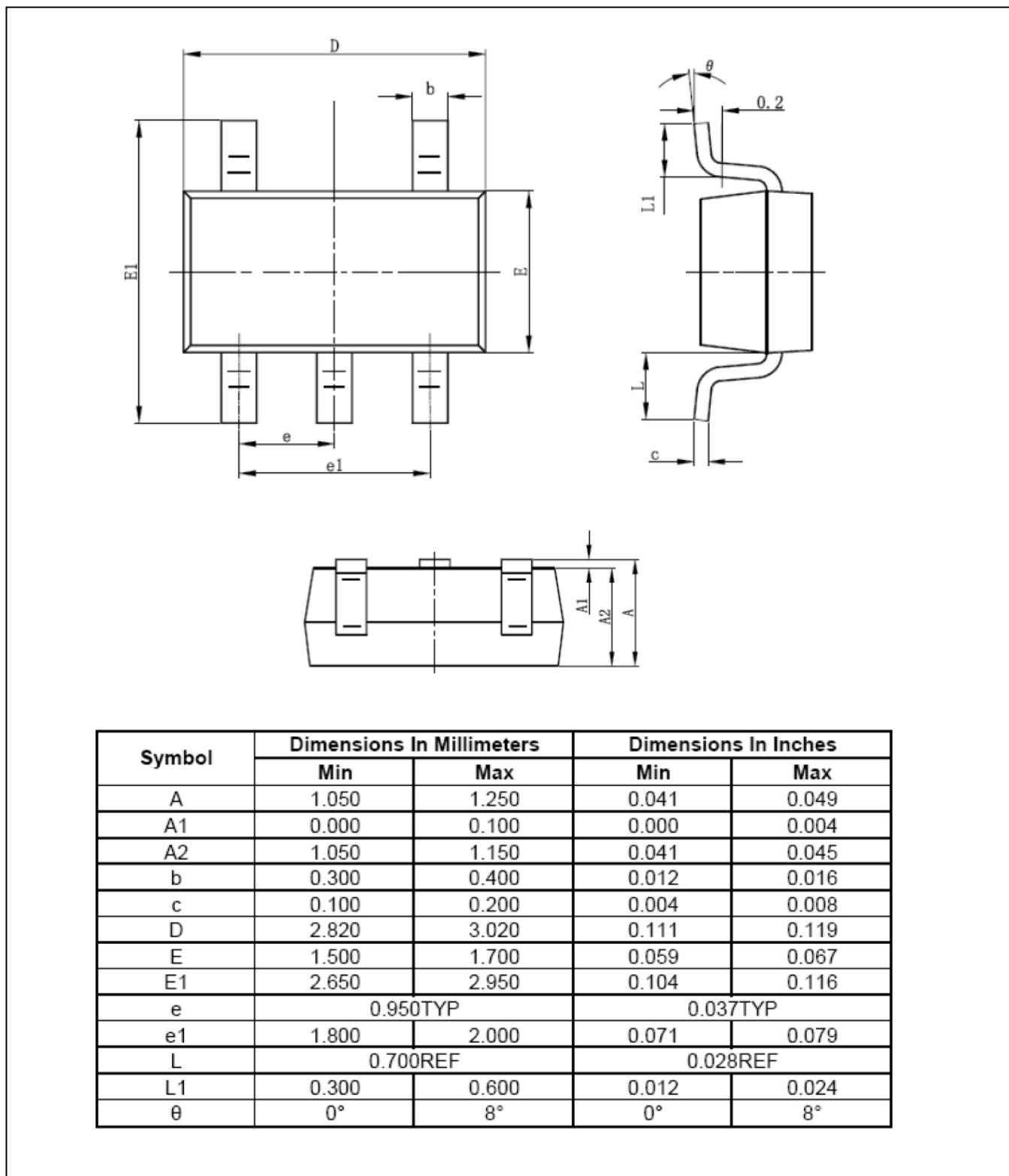


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## Packing Information

SOT-23-5L



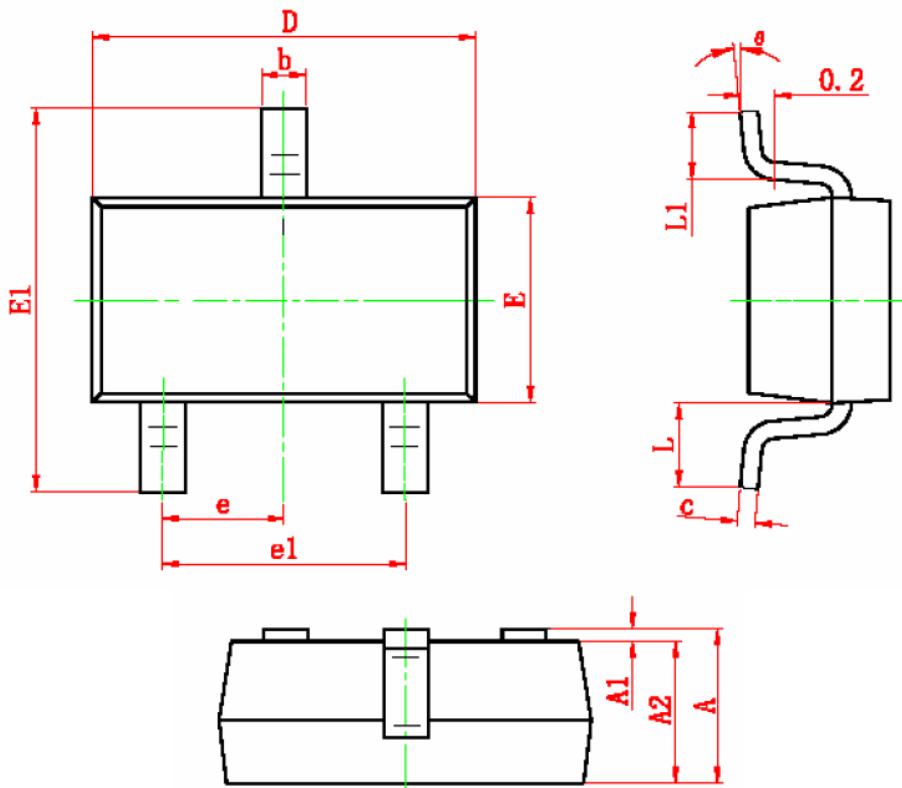


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## Packing Information

SOT-23-3



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.200	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.100	0.035	0.039
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	6°



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### Notes

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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