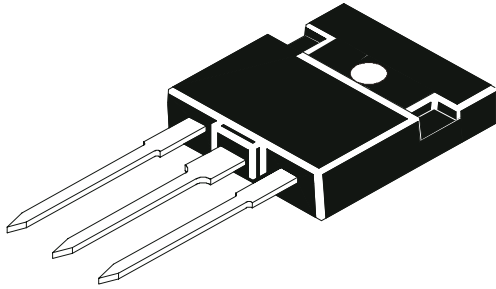


NPN SILICON PLASTIC POWER DARLINGTON TRANSISTOR

**CN1016
(9AW)
TO-3P**



**MARKING:- CN
1016**

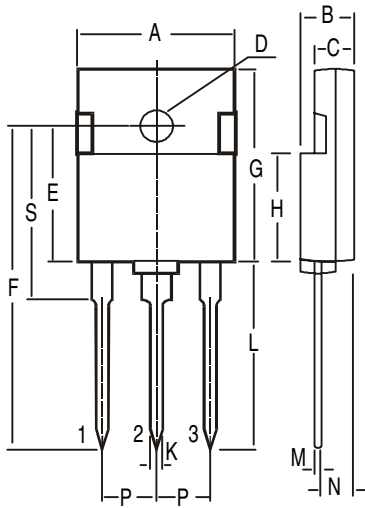
ABSOLUTE MAXIMUM RATINGS(Ta=25deg C)

| DESCRIPTION | SYMBOL | VALUE | UNIT |
|---|--------|-------------|-------|
| Collector -Base Voltage | VCBO | 160 | V |
| Collector -Emitter Voltage | VCEO | 150 | V |
| Emitter Base Voltage | VEBO | 5.0 | V |
| Collector Current | IC | 10 | A |
| Peak t=30ms | ICP | 15 | A |
| Base Current | IB | 1.0 | A |
| Collector Power Dissipation @ Tc=25 deg C | PC | 60 | W |
| Junction Temperature | Tj | 150 | deg C |
| Storage Temperature Range | Tstg | -55 to +150 | deg C |

ELECTRICAL CHARACTERISTICS (Ta=25 deg C Unless Specified)

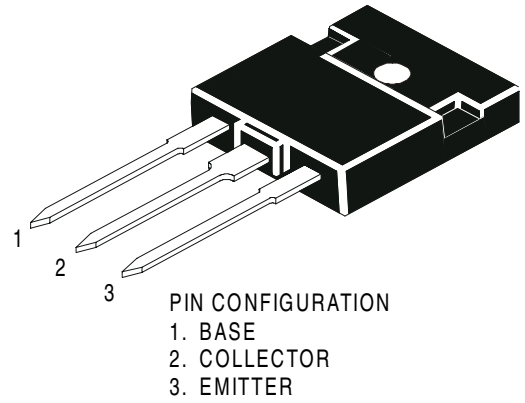
| DESCRIPTION | SYMBOL | TEST CONDITION | MIN | TYP | MAX | UNIT |
|--------------------------------------|----------|-------------------------|-----|-----|-----|------|
| Collector Cut off Current | ICBO | VCB=160V, IE=0 | - | - | 100 | uA |
| Emitter Cut off Current | IEBO | VEB=5V, IC=0 | - | - | 3.0 | mA |
| Collector Emitter Voltage | VCEO | IC=30mA, IB=0 | 150 | - | - | V |
| DC Current Gain | hFE | IC=6A, VCE=4V | 2.0 | - | 20 | K |
| | | IC=10A, VCE=4V | 1.0 | - | - | K |
| Collector Emitter Saturation Voltage | VCE(Sat) | IC=6A, IB=6mA | - | - | 2.5 | V |
| Base Emitter Saturation Voltage | VBE(Sat) | IC=6A, IB=6mA | - | - | 3.0 | V |
| Dynamic Characteristics | | | | | | |
| Transition Frequency | ft | VCE=12V, IC=1A, | - | 80 | - | MHz |
| Collector Output Capacitance | Cob | VCB=10V, IE=0 f=1MHz | - | 85 | - | pF |

TO-3P (TO-218) Plastic Package



All dimensions in mm.

| DIM | MIN. | MAX. |
|-----|-------|-------|
| A | 15.80 | 16.40 |
| B | 5.20 | 5.70 |
| C | 3.80 | 4.20 |
| D | Ø3.30 | Ø3.60 |
| E | 14.50 | 15.10 |
| F | 33.25 | 36.75 |
| G | 20.75 | 21.25 |
| H | 11.50 | 12.25 |
| K | 1.00 | 1.30 |
| L | 18.75 | 21.65 |
| M | 0.40 | 0.60 |
| N | 3.15 | 3.45 |
| P | 5.21 | 5.72 |
| S | 18.75 | 19.25 |



TO BE MOUNTED WITH SILICONE GREASE ON THE BACK SIDE,
 ENSURING BACK SIDE HOLES FILLED.

Packing Detail

| PACKAGE | STANDARD PACK | | INNER CARTON BOX | | OUTER CARTON BOX | | |
|---------|-----------------|----------------|------------------|------|-------------------|------|--------|
| | Details | Net Weight/Qty | Size | Qty | Size | Qty | Gr Wt |
| TO-3P | 100 pcs/polybag | 628 gm/100 pcs | 3" x 7.5" x 7.5" | 0.3K | 17" x 15" x 13.5" | 4.8K | 42 kgs |

Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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