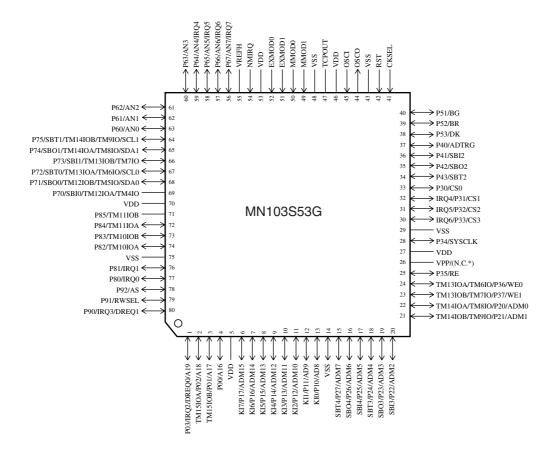
■ MN103S53G

Туре	MN103S53G (under development)		
Command ROM (×64-bit)	128 K-byte		
Data RAM (×32-bit)	12 K-byte		
Package	LQFP80-P-1414A *Pb-free		
Minimum Instruction Execution Time	25.0 ns (at 3.0 V to 3.6 V, 40 MHz)		
Interrupts	• RESET • IRQ \times 8 • NMI • Timer \times 28 • SIF \times 10 • I ² C \times 2 • DMA \times 6 • WDT • A/D • System error		
Timer Counter	8-bit timer × 10 Reload-down count Cascade connection possible (usable as a 16-bit to 32-bit timer)		
	16-bit timer × 6 Up-down count Input capture function PWM generating function Compare/capture register 2-ch.		
	Watchdog timer × 1		
DMA Controller	Number of channels: 2 Unit of transfer: 8/16/32 bits Max. Transfer cycles: 65535 Staring factor: external interrupt, timer factor, serial transmission/reception factor, I ² C transmission/reception factor, external transmission request factor, A/D conversion finish, software factor Transfer method: 2-bus cycle transfer Adressing modes: fixed, increment, decrement Transfer modes: word transfer, burst transfer, intermittent transfer		
Serial Interface	UART/synchronous/multi-master I ² C interface selective: 2 UART/synchronous interface selective: 3		
I/O Pins I/O	58 • Common use		
A/D Inputs	10-bit × 8-ch.		
Electrical Characteristics	T.B.D.		

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Pin Assignment



LQFP80-P-1414A*Pb-free

SupportTool

In-circuit Emulator	PX-ICE103S53	
On-board Development Tools	PX-ODB103S-O	
Flash Memory Built-in Type	Туре	MN103SF53G (under development)
	Command ROM (× 64-bit)	128 K-byte
	Data RAM (× 32-bit)	12 K-byte
	Minimum instruction execution time	25.0 ns (at 3.0 V to 3.6 V, 40 MHz)
	Package	LQFP80-P-1414A *Pb-free

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