

Memory Module Specification

KVR400X72RC3A/512 512MB 64M x 72-Bit DDR400 Registered CL3 ECC 184-Pin DIMM

DESCRIPTION:

This document describes ValueRAM's 64M x 72-bit (512MB) DDR400 CL3 SDRAM (Synchronous DRAM) Registered ECC memory module. The components on this module include eighteen 64M x 4-bit (16M x 4-Bit x 4 bank) DDR400 SDRAM in FBGA packages and three 16-bit drivers in TSSOP packages. This 184-pin DIMM uses gold contact fingers and requires +2.6V. The electrical and mechanical specifications are as follows:

FEATURES:

- \checkmark Clock Cycle Time (tCK)
- $\blacksquare Row Cycle Time (tRC)$
- \checkmark Refresh Row Cycle Time (tRFC)
- \checkmark Row Active Time (tRAS)
- ☑ Single Power Supply of
- Power (IDD0)
- **UL** Rating

5ns (min.) / 10ns (max.) 55ns (min.) 70ns (min.) 40ns (min.) / 70,000ns (max.) +2.6V (+/- .1V) 6.162 W operating 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.200" (30.48mm).



