

REVISIONS													
LTR	DESCRIPTION										DATE (YR-MO-DA)	APPROVED	
REV													
SHEET													
REV													
SHEET													
REV STATUS OF SHEETS	REV												
	SHEET	1	2	3	4	5	6	7	8	9	10		
PMIC N/A	PREPARED BY <i>Charles E. Besore</i>					DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444							
STANDARDIZED MILITARY DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE AMSC N/A	CHECKED BY <i>Charles E. Besore</i>					MICROCIRCUIT, LINEAR, OPERATIONAL AMPLIFIER, LOW NOISE, MONOLITHIC SILICON							
	APPROVED BY <i>Thomas L. Bellis</i>					SIZE	CAGE CODE						
	DRAWING APPROVAL DATE 18 APRIL 1990					A	67268	5962-89635					
REVISION LEVEL					SHEET 1								

DESC FORM 193
SEP 87

U.S. GOVERNMENT PRINTING OFFICE: 1987 - 748-129/60911

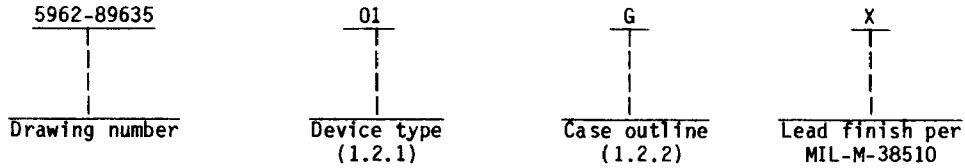
5962-E1380

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	HA-5101	Low-noise operational amplifier

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
G	A-1 (8-lead, .370" x .185"), can package
P	D-4 (8-lead, .405" X .310" X .200"), dual-in-line package
2	C-2 (20-terminal, .358" X .358" X .100"), square chip carrier package

1.3 Absolute maximum ratings.

Voltage between +V and -V terminals - - - - -	40 V dc
Differential input voltage - - - - -	7.0 V dc
Voltage at either input terminal - - - - -	+V to -V
Input current - - - - -	25 mA
Output short circuit current duration - - - - -	Indefinite
Maximum power dissipation (P _D): 1/	
Case G - - - - -	830 mW
Case P - - - - -	1.22 W
Case 2 - - - - -	1.35 W
Lead temperature (soldering, 10 seconds) - - - - -	+275°C
Thermal resistance, junction-to-case (θ _{JC}) - - - - -	See MIL-M-38510, appendix C
Thermal resistance, junction-to-ambient (θ _{JA}):	
Case G - - - - -	121°C/W
Case P - - - - -	82°C/W
Case 2 - - - - -	74°C/W
Junction temperature (T _J) - - - - -	+175°C

1/ Derate linearly above T_A = +75°C as follows:

- Case G = 8.3 mW/°C
- Case P = 12.2 mW/°C
- Case 2 = 13.5 mW/°C

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89635
	REVISION LEVEL	SHEET 2

1.4 Recommended operating conditions.

Positive supply voltage range (+V) - - - - - +5.0 V dc to +15 V dc
 Negative supply voltage range (-V) - - - - - -5.0 V dc to -15 V dc
 Common mode input voltage (V_{CM}) - - - - - $< (+V - -V)/2$
 Load resistance (R_L) - - - - - $> 500\Omega$
 Ambient operating temperature range (T_A) - - - - - -55°C to $+125^\circ\text{C}$

2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full ambient operating temperature range.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89635
	REVISION LEVEL	SHEET 3

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_A \leq +125^{\circ}\text{C}$ 1/	Group A subgroups	Limits		Unit
				Min	Max	
Input offset voltage	V_{IO}	$V_{CM} = 0.0 \text{ V}$	1		± 3.0	mV
			2,3		± 4.0	
Input bias current	$+I_B$	$V_{CM} = 0.0 \text{ V}, +R_S = 100 \text{ k}\Omega,$ $-R_S = 100 \text{ }\Omega$	1		± 200	nA
			2,3		± 325	
	$-I_B$	$V_{CM} = 0.0 \text{ V}, +R_S = 100 \text{ }\Omega,$ $-R_S = 100 \text{ k}\Omega$	1		± 200	nA
			2,3		± 325	
Input offset current	I_{IO}	$V_{CM} = 0.0 \text{ V}, +R_S = 100 \text{ k}\Omega,$ $-R_S = 10 \text{ k}\Omega$	1		± 75	nA
			2,3		± 125	
Common mode voltage range	$+V_{CM}$	$+V = 3.0 \text{ V}, -V = -27 \text{ V}$	1,2,3	12		V
	$-V_{CM}$	$+V = 27 \text{ V}, -V = -3.0 \text{ V}$	1,2,3	-12		V
Large signal voltage gain	$+A_{VOL}$	$V_{OUT} = 0.0 \text{ V}$ and $10 \text{ V},$ $R_L = 2.0 \text{ k}\Omega$	4,5,6	100		kV/V
	$-A_{VOL}$	$V_{OUT} = 0.0 \text{ V}$ and $-10 \text{ V},$ $R_L = 2.0 \text{ k}\Omega$	4,5,6	100		
Common mode rejection ratio	$+CMRR$	$\Delta V_{CM} = 10 \text{ V}, +V = 5.0 \text{ V},$ $-V = -25 \text{ V}, V_{OUT} = -10 \text{ V}$	1,2,3	80		dB
	$-CMRR$	$\Delta V_{CM} = -10 \text{ V}, +V = 25 \text{ V},$ $-V = -5.0 \text{ V}, V_{OUT} = 10 \text{ V}$	1,2,3	80		
Output current	$+I_{OUT}$	$V_{OUT} = -15 \text{ V}, +V = 18 \text{ V},$ $-V = -18 \text{ V}$	1,2,3	25		mA
	$-I_{OUT}$	$V_{OUT} = 15 \text{ V}, +V = 18 \text{ V},$ $-V = -18 \text{ V}$	1,2,3	-25		

See footnotes at end of table.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89635
		REVISION LEVEL	SHEET 4

DESC FORM 193A
SEP 87

U. S. GOVERNMENT PRINTING OFFICE: 1988-549-904

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C 1/	Group A subgroups	Limits		Unit
				Min	Max	
Output voltage swing	+V _{OUT1}	R _L = 2.0 kΩ	1,2,3	12		V
	-V _{OUT1}	R _L = 2.0 kΩ	1,2,3	-12		V
	+V _{OUT2}	+V = 18 V, -V = -18 V, R _L = 600 Ω	1,2,3	15		V
	-V _{OUT2}	+V = 18 V, -V = -18 V, R _L = 600 Ω	1,2,3	-15		V
Quiescent power supply	+I _{CC}	V _{OUT} = 0.0 V, I _{OUT} = 0 mA	1,2,3		6.0	mA
	-I _{CC}	V _{OUT} = 0.0 V, I _{OUT} = 0 mA	1,2,3		-6.0	mA
Power supply rejection ratio	+PSRR	+V = +10 V and +20 V, -V = -15 V	1,2,3	80		dB
	-PSRR	-V = -10 V and -20 V, +V = +15 V	1,2,3	80		dB
Offset voltage adjustment 2/	+V _{I0} adj	R _L = 2.0 kΩ, C _L = 50 pF, A _V = 1.0 V/V	1,2,3	V _{I0} -1.0		mV
	-V _{I0} adj		1,2,3	V _{I0} +1.0		
Differential input resistance 3/	R _{IN}	V _{CM} = 0.0 V, T _A = +25°C	4	250		kΩ
Input noise voltage 3/	E _n	R _S = 20 Ω, f ₀ = 1.0 kHz, T _A = +25°C	4		7.0	4/
Input noise current 3/	I _n	R _S = 2.0 MΩ, f ₀ = 1.0 kHz, T _A = +25°C	4		3.0	5/
Low frequency peak-to-peak noise 3/	E _{np-p}	0.1 Hz to 10 Hz, T _A = +25°C	4		4.5	μVp-p

See footnotes at end of table.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89635
	REVISION LEVEL	SHEET 5

DESC FORM 193A
SEP 87

* U. S. GOVERNMENT PRINTING OFFICE: 1988-549-904

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C 1/	Group A subgroups	Limits		Unit
				Min	Max	
Slew rate	+SR	V _{OUT} = -3.0 V to 3.0 V, T _A = +25°C	4	6.0		V/μs
	-SR	V _{OUT} = 3.0 V to -3.0 V, T _A = +25°C	4	6.0		V/μs
Unity gain bandwidth	UGBW	V _{OUT} = 100 mV, T _A = +25°C	4	10		MHz
Full power bandwidth 3/ 6/	FPBW	V _{pk} = 10 V, T _A = +25°C	4	95		kHz
Closed loop stable gain 3/	CLSG	R _L = 2.0 kΩ, C _L = 50 pF	4,5,6	1.0		V/V
Rise time 7/	t _r	V _{OUT} = 0.0 V to +200 mV	9		200	ns
			10,11		400	
Fall time 7/	t _f	V _{OUT} = 0.0 V to -200 mV	9		200	ns
			10,11		400	
Overshoot	+OS	V _{OUT} = 0.0 V to +200 mV	9,10,11		35	%
	-OS	V _{OUT} = 0.0 V to -200 mV	9,10,11		35	
Output resistance 3/	R _{OUT}	Open loop, T _A = +25°C	4		150	Ω
Quiescent power consumption 8/	P _C	V _{OUT} = 0.0 V, I _{OUT} = 0 mA	1,2,3		180	mW

- 1/ +V = +15 V, -V = -15 V, R_S = 100 Ω, R_L = 500 kΩ, C_L = 50 pF, V_{OUT} = 0.0 V, and A_V = 1.0 V/V unless otherwise specified.
 2/ Offset adjustment range is V_{IO} (measured) ±1.0 mV minimum referred to output. This test is for functionality only to assure adjustment through 0.0 V
 3/ If not tested, shall be guaranteed to the limits specified in table I.
 4/ nV per the square root of frequency expressed in Hz.
 5/ pA per the square root of frequency expressed in Hz.
 6/ Full power bandwidth = $\frac{SR}{2\pi V_{pk}}$.
 7/ Rise and fall times measured between 10% and 90% point.
 8/ Quiescent power consumption based on quiescent supply current test maximum (no load on outputs).

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89635
		REVISION LEVEL	SHEET 6

Device type	01	01
Case outline	G and P	2
Terminal number	Terminal symbol	
1	BAL	NC
2	-IN	BAL
3	+IN	NC
4	-V	NC
5	BAL	-IN
6	OUT	NC
7	+V	+IN
8	NC	NC
9	---	NC
10	---	-V
11	---	NC
12	---	BAL
13	---	NC
14	---	NC
15	---	OUT
16	---	NC
17	---	+V
18	---	NC
19	---	NC
20	---	NC

FIGURE 1. Terminal connections.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89635
		REVISION LEVEL	SHEET 7

DESC FORM 193A
SEP 87

★ U. S. GOVERNMENT PRINTING OFFICE: 1986-549-904

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 7 and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89635
		REVISION LEVEL	SHEET 8

DESC FORM 193A
SEP 87

U. S. GOVERNMENT PRINTING OFFICE: 1988-549-904

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883:
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).
 - (2) $T_A = +125^{\circ}\text{C}$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*,2,3,4,5,6,9
Group A test requirements (method 5005)	1,2,3,4,5,6, 9,10,11
Groups C and D end-point electrical parameters (method 5005)	1

* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All propose changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECS, telephone (513) 296-6022.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A	5962-89635
	REVISION LEVEL	SHEET 9

DESC FORM 193A
SEP 87

* U. S. GOVERNMENT PRINTING OFFICE: 1968-549-904

6.5 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

6.6 Approved source of supply. An approved source of supply is listed in MIL-BUL-103. Additional sources will be added to MIL-BUL-103 as they become available. The vendor listed in MIL-BUL-103 has agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECS. The approved source of supply listed below is for information purposes only and is current only to the date of the last action of this document.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>
5962-8963501GX	34371	HA2-5101/883
5962-8963501PX	34371	HA7-5101/883
5962-89635012X	34371	HA4-5101/883

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

34371

Vendor name and address

Harris Semiconductor
P.O. Box 883
Melbourne, FL 32901

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-89635
		REVISION LEVEL	SHEET 10

DESC FORM 193A
SEP 87

★ U. S. GOVERNMENT PRINTING OFFICE: 1968-549-904

019133

AUG 20 1990