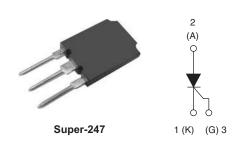


Vishay High Power Products

Phase Control SCR, 70 A



PRODUCT SUMMARY					
V _T at 100 A	< 1.4 V				
I _{TSM}	1400 A				
V _{RRM}	1200/1600 V				

DESCRIPTION/FEATURES

The 70TPS.. High Voltage Series of silicon controlled rectifiers are specifically designed for high and medium power switching and phase control applications.

Typical applications are in input rectification (soft start) or AC-switches or high current crow-bar as well as others phase-control circuits.

These products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level.

MAJOR RATINGS AND CHARACTERISTICS							
PARAMETER	TEST CONDITIONS	VALUES	UNITS				
I _{T(AV)}	Sinusoidal waveform	70	۸				
I _{RMS}	Lead current limitation	75	А				
V _{RRM} /V _{DRM}	Range	1200/1600	V				
I _{TSM}		1400	А				
V _T	100 A, T _J = 25 °C	1.4	V				
dV/dt		500	V/µs				
dl/dt		150	A/μs				
T _J		- 40 to 125	°C				

VOLTAGE RATINGS				
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA	
70TPS12	1200	1300	15	
70TPS16	1600	1700	15	

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ABSOLUTE MAXIMUM RATING		1			1	i
PARAMETER	SYMBOL	T	TEST CONDITIONS		VALUES	UNITS
Maximum average on-state current	$I_{T(AV)}$	$T_C = 82 ^{\circ}C, 180^{\circ} c$	onduction half sine w	ave	70	
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}	Lead current limita	Lead current limitation			Α
Maximum peak, one-cycle	1	10 ms sine pulse, i	rated V _{RRM} applied		1200	
non-repetitive surge current	I _{TSM}	10 ms sine pulse, r	no voltage reapplied		1400	
Maximum I ² t for fusing	l ² t	10 ms sine pulse, r	rated V _{RRM} applied	Initial $T_J = T_J$ maximum	7200	A ² s
waximum i-t for fusing	1-1	10 ms sine pulse, r	no voltage reapplied	maximam	10 200	
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 10 ms, no	102 000	A²√s		
Low level value of threshold voltage	V _{T(TO)1}			0.916	V	
High level value of threshold voltage	V _{T(TO)2}	T = 105 °C		1.21	V	
Low level value of on-state slope resistance	r _{t1}	T _J = 125 °C			4.138	mΩ
High level value of on-state slope resistance	r _{t2}			3.43	1115.2	
Maximum peak on-state voltage	V_{TM}	100 A, T _J = 25 °C	100 A, T _J = 25 °C			
Maximum rate of rise of turned-on current	dI/dt	T _J = 25 °C			150	A/µs
Maximum holding current	I _H	T 05.00			200	
Maximum latching current	ΙL	- T _J = 25 °C		400		
Marian was a said disast lands as a susual	I _{RRM} /I _{DRM}	T _J = 25 °C			1.0	mA
Maximum reverse and direct leakage current		T _J = 125 °C	V _R = Rated V _{RRM} /\	15		
Maximum rate of rise of off-state voltage	dV/dt	T _J = 125 °C			500	V/µs

TRIGGERING							
PARAMETER	SYMBOL		VALUES	UNITS			
Maximum peak gate power	P_{GM}	T = 30 µs	T. 00				
Maximum average gate power	$P_{G(AV)}$	1 = 30 μs		2.5	W		
Maximum peak gate current	I _{GM}			2.5	Α		
Maximum peak negative gate voltage	- V _{GM}			10			
		T _J = - 40 °C		4.0	V		
Maximum required DC gate voltage to trigger	V_{GT}	T _J = 25 °C	Anode supply = 6 V resistive load	1.5			
voluage to trigger		T _J = 125 °C		1.1			
		T _J = - 40 °C		270			
Maximum required DC gate current to trigger	I _{GT}	T _J = 25 °C		100	mA		
		T _J = 125 °C		80			
Maximum DC gate voltage not to trigger	V_{GD}	T _J = 120 °C, V _D	_{RM} = Rated value	0.25	V		
Maximum DC gate current not to trigger	I_{GD}			6	mA		



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THERMAL AND MECH	THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature	range	TJ		- 40 to 125	°C		
Maximum storage temperature r	ange	T _{Stg}		- 40 to 150			
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	0.27			
Maximum thermal resistance, junction to ambient		R _{thJA}		40	°C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.2			
Approximate weight				6	g		
				0.21	OZ.		
Mounting torque	minimum			6 (5)	kgf · cm		
- Woulding torque	maximum			12 (10)	(lbf · in)		
Marking device			Consistua Super 247	70TPS	12		
			Case style Super-247	70TPS	70TPS16		

△R _{thJ-hs} CONDUCTION PER JUNCTION											
DEVICE	SINE HALF WAVE CONDUCTION					RECTANGULAR WAVE CONDUCTION				UNITS	
DEVICE	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
70TPS	0.078	0.092	0.117	0.172	0.302	0.053	0.092	0.125	0.180	0.306	°C/W

Note

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[•] The table above shows the increment of thermal resistance R_{thJ-hs} when devices operate at different conduction angles than DC

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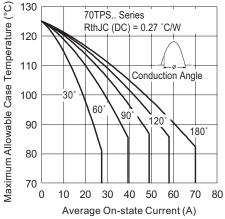


Fig. 1 - Current Rating Characteristics

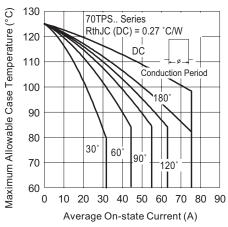


Fig. 2 - Current Rating Characteristics

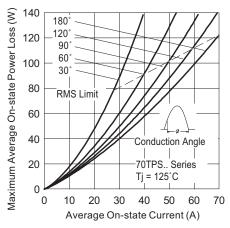


Fig. 3 - On-State Power Loss Characteristics

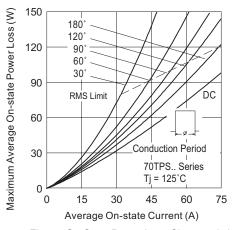


Fig. 4 - On-State Power Loss Characteristics

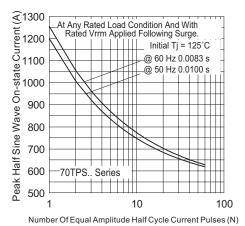


Fig. 5 - Maximum Non-Repetitive Surge Current

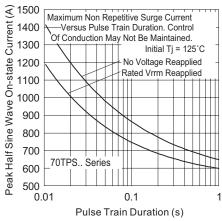


Fig. 6 - Maximum Non-Repetitive Surge Current



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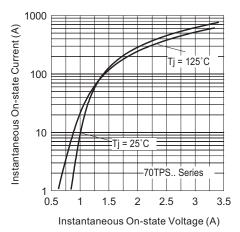


Fig. 7 - On-State Voltage Drop Characteristics

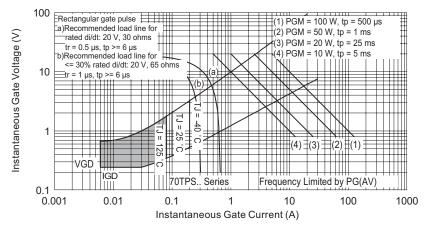


Fig. 8 - Gate Characteristics

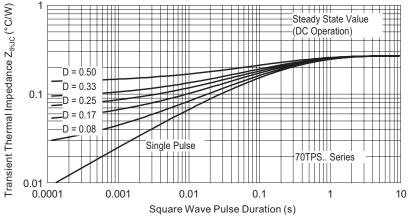


Fig. 9 - Thermal Impedance Z_{thJC} Characteristics

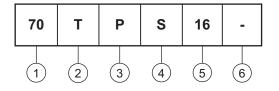
70TPS.. High Voltage Series

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ORDERING INFORMATION TABLE

Device code



1 - Current rating (70 = 70 A)

2 - Circuit configuration:

T = Thyristor

3 - Package:

P = Super-247

4 - Type of silicon:

S = Standard recovery rectifier

5 - Voltage code x 100 = V_{RRM} ____

12 = 1200 V 16 = 1600 V

6 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95073					
Part marking information	http://www.vishay.com/doc?95070				

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