TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC83220-0025

TC83220-0025 Single-Chip CMOS LSI for FL (fluorescent) Calculator

The TOSHIBA printing/display calculator circuit TC83220-0025 is 10- or 12-digit calculator on single-chip CMOS LSI.

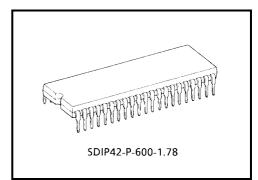
TC83220-0025 can drive the printing machine (M-72T; EPSON) with magnet driver circuit, and can drive the fluorescent display tube with DC-DC converter.

It contains a 4 K-word ROM, a 256×4 -bit RAM.

Features

Operational Features

• Print: 11 or 13 digits of data.



Weight: 4.12 g (typ.)

Display: 10 or 12 digits of data. (including punctuation in each digit.)

2 digits of operational symbol.

- 1 digit of floating minus sign, memory load, error symbol, grand total memory load. 3 digits of commas.
- Decimal output: Decimal set lock key controls output format.

(including decimal point) 1 digit of minus sign.

3 digits of commas. 2-colors printing. (black and red)

- Fixed decimal setting ("0", "1", "2", "3", "4", "6"), full floating decimal, and ADD mode.
- Key input buffer: 8 stages
- Function: 4 basic arithmetic functions $(+, -, \times, \div)$.
 - Repeat addition and subtraction.
 - Automatic constants in multiplication, division, percent calculation, calculations.
 - Automatic percent add-on and percent discount calculation.
 - Memory calculation.
 - Automatic accumulating calculation.
 - Gross margin profit calculation.
 - Delta percent calculation.
 - Tax calculation.
 - Grand total calculation.
 - Two-key rollover
- Item counter: 0~999 count up by depressing of, +, key.
- Punctuation: Commas for thousands on display.
- Kinds of touch key:

0~9, ·, 00, 000, C, CE, C/CE, +/-, #/P, Feed,
+, -, ⊘, ∗, ×, ÷, =, %, MU/D, M+, M-, M◇,
$M*, \ \underline{\Delta\%}, \ M \stackrel{\diamond}{\ast}, \ \rightarrow, \ GT, \ \underline{\pm}, \ \underline{=}, \ +TAX, \ -TAX$

• Kinds of lock key (refer to page 5.): "NP" printing mode selectable switch. ("ON": nonprinting mode. "OFF": printing mode.)

" Σ " summation mode selectable switch.

"5/4" "CUT" "UP" rounding switch. ("5/4": "CUT" and "UP" lock key off.) Fixed point mode selectable switch.

"0", "1", "2", "3", "4", "6", "F", "A". ("A": ADD mode. "F": full floating mode, all decimal setting lock key off.)

"IC+" item counter mode selectable switch.

"GT" grand total memory selectable switch.

"SET/CAL" tax memory selectable switch. (ON: set mode. OFF: normal calculation mode.)

" $10/\overline{12}$ " display digits selectable switch. (refer to page 3.)

- Duty of display: Duty = 1/17.7
- Leading zero suppression
- Trailing zero suppression

Tax calculation (refer to page 5.): | +TAX | key is calculation for included tax.

-TAX key is calculation for excluded tax.

SET/CAL lock key selects set mode or normal calculation mode.

Changing lock key from set mode to normal calculation mode stores number of display to tax memory.

Changing lock key from normal calculation mode to set mode recalls tax rate to display from tax memory.

Depression of +TAX | following data key at normal calculation mode performs the calculating included tax.

Depression of **-**TAX following data key at normal calculation mode performs the calculating excluded tax.

Electrical Features

- P-MOS output buffer with pull down resistor for direct driving of fluorescent display tube.
- Oscillator/clock generator internal to chip.
- Key board encoding internal to chip.
- Shrink dual in line package.

Protection

- (1)Double depression of keys will be inoperative.
- In the overflow condition, all key except "C", "C/CE", "CE" "Feed", "-" key are inoperative. (2)
- (3)Key bouncing protection (at 4 MHz clock)
 - Key read in: 15 ms Key off: 40 ms

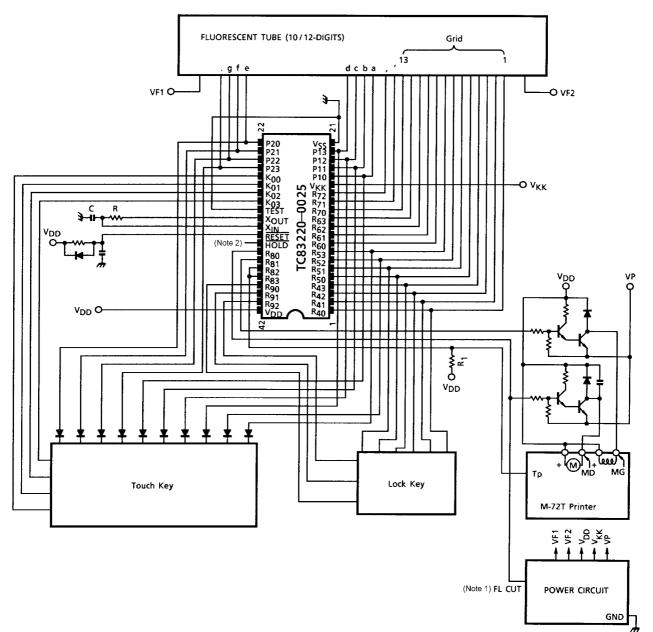
Function Select

" $10/\overline{12}$ " selectable with calculated digits (lock key). (1)ON..... 10-digit calculation OFF..... 12-digit calculation

Pin Assignment (top view)

R40	1	\mathbf{O}	42	VDD
R41	2		41	R92
R42	3		40	R91
R43	4		39	R90
R ₅₀	5		38	R ₈₃
R51	6	ŝ	37	R ₈₂
R52	7	V I EW	36	R81
R53	8		35	R80
R60	9	ТОР	34	HOLD
R61	10	\sim	33	RESET
R62	11	25	32	X _{IN}
R63	12	00	31	XOUT
R70	13	20-	30	TEST
R ₇₁	14	22	29	К ₀₃
R72	15	83	28	к ₀₂
νκκ 🛙	16	τc	27	K01
P10	17		26	к ₀₀
P11	18		25	P23
P12	19		24	P22
P13	20		23	P21
vss	21		22	P20
		-		

System Diagram



C = 100 pF

 $R=1~k\Omega\pm2\%$

 $R_1 = 10 \sim 15 \ k\Omega$

VP: Power source to drive printer.

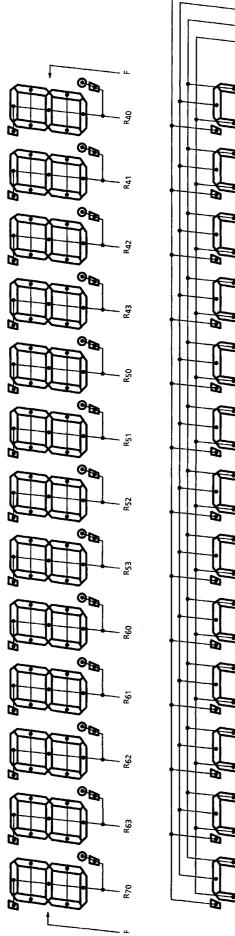
 $V_{\mbox{\scriptsize KK}}$ Power source for display.

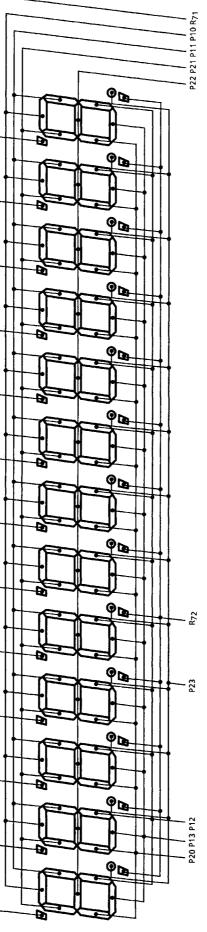
Note 1: FL CUT (R₈₀)

(VF1, VF2 cut at printing)

Note 2: Connection to \overline{HOLD} pin is shown in the following page 15.

Connection of FL

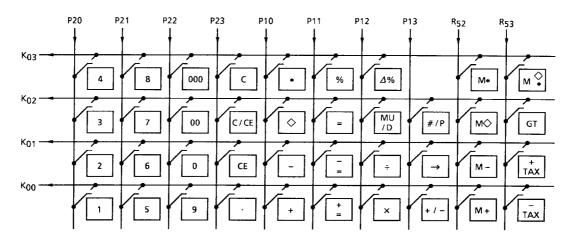




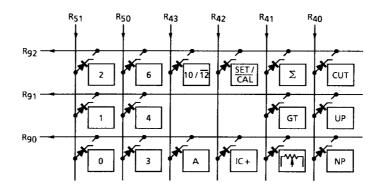
- Note 3: R₇₀ digit (P20) of "E" data
- Note 4: R₇₀ digit (P22) of "-" data Note 5: R₇₀ digit (P23) of "M" data
- Note 6: R₇₀ digit (P21) of "GT" data

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Key Connection



Touch Key



Lock Key

Note 7: Feed

Operation Example

Кеу		Print	Print	Display	
TAB 4/5 IC 10/12 Σ GT MOD	Touch	1 mit	Color	Display	
F 4/5 OFF 10 OFF OFF CAL	POWER ON				
		<pf></pf>			
		С			
		<pf></pf>		0	
	1+	1. +		1	
	2-	2	R	-1	
	\$	-1. ◊	R	-1	
	*	-1. *	R		
		<pf></pf>		-1	
IC+	1+	1. +		1	
TC I	2-	2	R	-1	
	≥-	002	ĸ	-1	
	V	-1. ♦	R	-1	
	*	002	R		
	, A	-1. *	R		
		<pf></pf>	R	-1	
		\rr/			
OFF	3×	3. ×		3	
	4÷	4. ÷		12	
	=	4. =			
		3. *			
		<pf></pf>		3	
	5×	5. ×		Ę	
	6%	6. [%]			
		0.3 *			
		<pf></pf>		0.	
	+	5.3 + %			
		<pf></pf>		5.	
	2÷	2. ÷		2	
	3%	3. %			
		66.666666666666666666666666666666666666			
		<pf></pf>		66.666666	
	2 MU/D	2. G M			
	3=	3. %			
		0.06185567 Δ *			
		2.06185567 *			
		<pf></pf>		2.0618556	
	24%	2. Δ		2	
	3=	3. =			
		1. Δ *			
		50. Δ %			
		<pf></pf>		50	

Note 8: PRINT COLOR R: Red No mark: Black

<PF>.....Paper feed

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Кеу		Print	Print	Display	
TAB 4/5 IC 10/12 Σ GT MOD	Touch		Color	,	
F 4/5 OFF 10 Σ OFF CAL	3×	3. ×		3.	
	4÷	4. ÷		12.	
	=	4. =			
		3. +			
		<pf></pf>		3.	
	5×	5. ×		5.	
	6%	6. %			
		0.3 +			
		<pf></pf>		0.3	
	+	5.3 + %			
		<pf></pf>		5.3	
	2÷	2. ÷		2.	
	3%	3. %			
		66.66666666 +			
		<pf></pf>		66.6666666	
	2 MU/D	2. G M		2.	
	3=	3. %			
		0.06185567 Δ *			
		2.06185567 +			
		<pf></pf>		2.06185567	
	2Δ %	2. Δ		2.	
	3=	3. =			
		1. Δ *			
		50. +			
		<pf></pf>		50.	
	*	122.0285223 *			
		<pf></pf>		122.0285223	
	GT	0. G 🛇		0.	
GT	2+	2. +		2.	
	3+	3. +		5.	
	*	5. G +			
		<pf></pf>		5.	
	3-	3	R	-3.	
	4 -	4. –	R	-7.	
	5-	5	R	-12.	
	*	-12. G +	R		
		<pf></pf>		-12	
	GT	-7. G ◊	R	-7	
	GT	-7. G *	R		
		<pf></pf>		-7	
OFF	M+	-7. M +	R	м -7	
	С	0. C		м 0	

Note 8: PRINT COLOR R: Red

No mark: Black

<PF>Paper feed

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Кеу		Drint	Print	Display		
TAB 4/5 IC 10/12 Σ GT MOD	Touch	Print	Color	Display		
		<pf></pf>		м -7.		
	М\$	-7. M ◊	R			
	M*	-7. M *	R			
		<pf></pf>		-7.		
F 4/5 OFF 10 Σ OFF CAL	#/P	-7. ◊	R	-7.		
	2 #/P	#2		2.		
	#/P	2. ◊		2.		
	0÷	0. ÷		0.		
	=	0. =				
		0. *				
		<pf></pf>		Е 0.		
	С	0. C				
		<pf></pf>		0.		
F CUT OFF 12 OFF OFF CAL	POWER ON	<pf></pf>				
		С				
		<pf></pf>		0.		
SET		0. %		0		
	3	<pf></pf>		0. 3.		
CAL	5	3. %		5.		
		<pf></pf>		0.		
	С	0. C				
	-	<pf></pf>		0.		
SET		3. %				
		<pf></pf>		3.		
CAL				0.		
	1560			1,560.		
	+TAX	1,560.				
		46.8 Δ				
		1,606.8 *				
		<pf></pf>		1,606.8		
	+TAX	1,606.8 ◊				
		48.204 Δ				
		1,655.004 *				
		<pf></pf>		1,655.004		
	1560			1,560.		
	×	1,560. ×		1,560.		
	78900			78,900.		
	+TAX	78,900. =				
		123,084,000. ♦				
		3,692,520. Δ				
		126,776,520. *				

Note 8: PRINT COLOR R: Red

No mark: Black

<PF>Paper feed

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Кеу		Drint	Print	Diaglass							
TAB	4/5	IC	10/1	2 Σ	G	г мс	OD	Touch	Print	Color	Display
									<pf></pf>		126,776,520.
								=			126,776,520.
								5			5.
								×	5. ×		5.
								+TAX			5.
								=	5. =		
									25. *		
									<pf></pf>		25.
F (CUT	OFF	12	OFI	F OF	F CA	AL	+TAX	25. ◊		
									0.75 Δ		
									25.75 *		
									<pf></pf>		25.75
								=			25.75
								С	0. C		
									<pf></pf>		0.
2								1560			1,560.
								+	1,560.00 +		1,560.00
								1100			1,100.
								+	1,100.00 +		2,660.00
								+TAX	2,660.00 ◊		
									79.80 Δ		
									2,739.80 *		
									<pf></pf>		2,739.80
F								+TAX	2,739.80 ♦		
									82.194 A		
									2,821.994 *		0.001.004
								980000000000	<pf></pf>		2,821.994 980,000,000,000.
								+TAX	980,000,000,000.		980,000,000,000.
								TIAX	29,400,000,000. Δ		
									29,400,000,000. A		
									1,00940000000 *		
									<pre><pf></pf></pre>		E 1.00940000000
								С	0. C		1.00010000000
								Ū.	<pf></pf>		0.
								1560			1,560.
								+/-			-1,560.
								+TAX	-1,560.	R	
									-46.8 Δ	R	
									-1,606.8 *	R	
									<pf></pf>		-1,606.8
								1560			1,560.
								-TAX	1,560.		,

Note 8: PRINT COLOR R: Red

No mark: Black

<PF> Paper feed

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Key		Print	Print	Display
TAB 4/5 IC 10/12 Σ GT MOD	Touch	PIIII	Color	Display
F CUT OFF 12 OFF OFF CAL		-45,43689321 Δ	R	
		1,514.56310679 *		
		<pf></pf>		1,514.56310679
	-TAX	1,514.56310679 ◊		
		-44.11348855 Δ	R	
		1,470.44961824 *		
		<pf></pf>		1,470.44961824
SET		3. %		
		<pf></pf>		3.
	С			0.
CAL		0. %		
		<pf></pf>		0.
SET		0. %		
		<pf></pf>		0.
	1234			1,234.
CAL		1,234. %		
		<pf></pf>		0.
	980000000000			980,000,000,000.
	+TAX	980,000,000,000.		
		•••••		
		0. *		
		<pf></pf>		Е О.
	С	0. C		
		<pf></pf>		0.

Note 8: PRINT COLOR......R: Red No mark: Black <PF>Paper feed

Maximum Ratings (V_{SS} = 0 V)

Characteristics	Symbol	Rating	Unit
Supply voltage 1	V _{DD}	-0.5~7	V
Supply voltage 2	V _{KK}	-40~+0.5	V
Input voltage	V _{IN}	-35~V _{DD} + 0.5	V
Output voltage	V _{OUT}	-35~V _{DD} + 0.5	V
Output current	IOUT	-10	mA
Power dissipation (T _{opr} = 70°C)	PD	600	mW
Soldering temperature, time	T _{sld}	260 (10 s)	°C
Storage temperature	T _{stg}	-55~125	°C
Operating temperature	T _{opr}	0~40	°C

Recommended Operating Conditions ($V_{SS} = 0 V$)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Max	Unit
Operating temperature	T _{opr}	_		0	40	°C
Supply voltage	V _{DD}	_		4.5	6	V
Supply voltage (FL)	V _{KK}	_		-30	-15	V
Supply voltage (hold)	V _{DDH}	_		2	6	V
Input high voltage (except schmitt circuit input)	V _{IH1}	_	N	V _{DD} × 0.7	V _{DD}	V
Input high voltage (schmitt circuit input)	V _{IH2}	_	V _{DD} ≧ 4.5 V	V _{DD} × 0.75	V _{DD}	V
Input high voltage	V _{IH3}	_	V _{DD} < 4.5 V	V _{DD} × 0.9	V _{DD}	V
Input low voltage (except schmitt circuit input)	V _{IL1}	_	N>45V	V _{КК}	V _{DD} × 0.3	V
Input low voltage (schmitt circuit input)	V _{IL2}	_	V _{DD} ≧ 4.5 V	Vĸĸ	V _{DD} × 0.25	V
Input low voltage	V _{IL3}	_	V _{DD} < 4.5 V	V _{KK}	V _{DD} × 0.1	V
Output voltage (source open drain)	V _{OUT}	_	_	V _{DD} - 35	V _{DD}	V
Clock high pulse width (Note 9)	T _{WCH}	—	$V_{IN} = V_{IH}$	80		ns
Clock low pulse width (Note 9)	T _{WCL}	—	$V_{IN} = V_{IL}$	80		ns

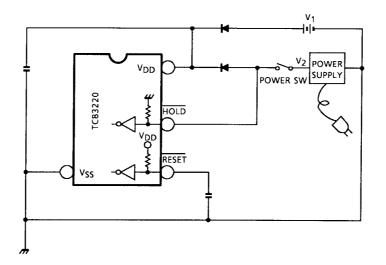
Note 9: In case of the external clock operation.

Electrical Characteristics

DC Characteristics (V_{SS} = 0 V, V_{DD} \pm 10\%, T_{opr} = 0~40^{\circ}C)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Hysteresis voltage (schmitt circuit input)	V _{HS}	_	_	_	0.7	_	V
Input current (RESET, HOLD, TEST)	I _{IN}	_	V _{DD} = 5.5 V, V _{IN} = 5.5/0 V			±50	μΑ
Output leak current (source open drain)	ILO	_	V _{DD} = 5.5 V, V _{OUT} = -32 V	_		-10	μΑ
Output high voltage (P1~P2, R ₄ ~R ₉)	V _{OH}	_	V _{DD} = 4.5 V, I _{OH} = -6 mA	2.4			V
Input pull down resistor (K ₀ , R ₇ ~R ₉)	R _{IN}		V. 55V/V. 20V	_	100	_	kΩ
Pull down resistor (source open drain)	R _{KK}	_	V _{DD} = 5.5 V, V _{KK} = -30 V	50	80	200	kΩ
Operating supply current	I _{DD0}		$V_{DD} (V_{DDH}) = 5.5 V,$ f _c = 4 MHz, V _{IN} = 5.3/0.2 V	_	3	6	mA
Supply current (after clear)	I _{KK1}	_			0.6	0.9	mA
Supply current (shown full digits)	I _{KK2}		$-V_{KK} = -30 \text{ V}, \text{ f}_{c} = 4 \text{ MHz}$		3.5	6	mA
Holding supply current	I _{DDH}		V _{DD} = 5.5 V		0.5	10	μA
Oscillating frequency	Fφ	_	V_{DD} = 5.0 V, C = 100 pF R = 1 k $\Omega \pm 2\%$	2.4	4.0	5.6	MHz

The Proposal of Outer Circuit for Tax Rate Holding with Back-Up Battery.



Note 10: $V_1 = 3$ V: Battery supply

 $V_2 = 5 V: DC supply$

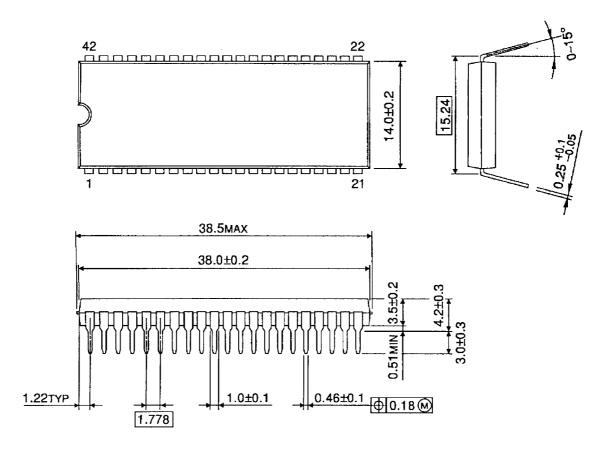
 $\label{eq:hold_state} \begin{array}{l} \overline{HOLD} \\ \overline{RESET} \end{array} \mbox{ pin is pulled down in the LSI, but normally pulled up to V_{DD}. \\ \end{array}$

- (1) Setting POWER SW to ON, V_2 is supplied to V_{DD} pin, and also to \overline{HOLD} pin. Then calculator operates normally.
- (2) Setting POWER SW from ON to OFF, V_1 is supplied to V_{DD} pin and V_{SS} is supplied to \overline{HOLD} pin. Under this connection, TAX RATE is held.
- (3) Setting POWER SW to ON, V_2 is supplied to V_{DD} pin, and also to \overline{HOLD} pin. Then calculator operates normally with TAX RATE to be held.
- Note 11: V₁ (battery) should be supplied to the circuit after V₂ (DC) supply, because of prevention from exhaustion of battery and abnormal operation.

Package Dimensions

SDIP42-P-600-1.78

Unit : mm



Weight: 4.12 g (typ.)

RESTRICTIONS ON PRODUCT USE

Handbook" etc..

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