

# **SRA2202M**

**PNP Silicon Transistor** 

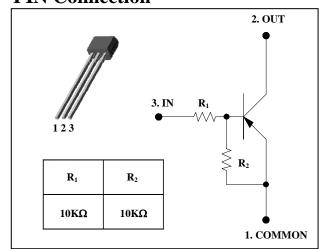
### **Descriptions**

- Switching application
- Interface circuit and driver circuit application

#### **Features**

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

#### **PIN Connection**



## **Ordering Information**

Type NO.	Marking	Package Code	
SRA2202M	2202	TO-92M	_

#### **Absolute Maximum Ratings**

 $(Ta=25^{\circ}C)$ 

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V <sub>I</sub>	-30, 10	V
Output current	Io	-100	mA
Power dissipation	$P_{D}$	400	mW
Junction temperature	TJ	150	°C
Storage temperature range	$T_{stg}$	-55 ~ 150	°C

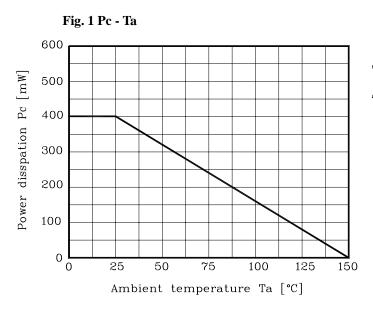
#### **Electrical Characteristics**

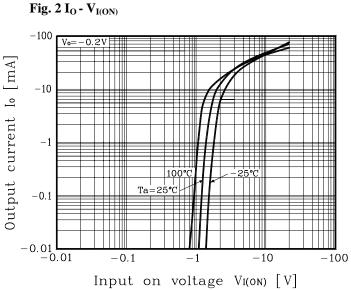
(Ta=25°C)

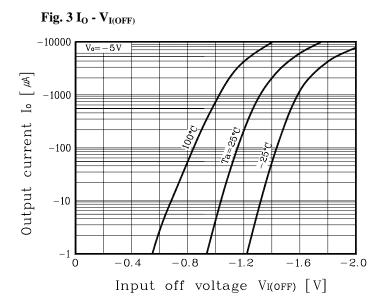
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_0 = -5V$ , $I_0 = -10$ mA	50	80	-	-
Output voltage	$V_{O(ON)}$	I <sub>O</sub> =-10mA, I <sub>I</sub> =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	V <sub>I(ON)</sub>	$V_0 = -0.2V$ , $I_0 = -5mA$	-	-1.8	-2.4	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = -5V$ , $I_0 = -0.1$ mA	-1.0	-1.2	-	V
Transition frequency	$f_{T}^{}^{\star}}$	$V_0 = -10V$ , $I_0 = -5mA$ , $f = 1MHz$	-	200	-	MHz
Input current	$I_1$	$V_1 = -5V, I_0 = 0$	-	-	-0.88	mA
Input resistor (Input to base)	R <sub>1</sub>	-	7	10	13	ΚΩ
Input resistor (Base to common)	R <sub>2</sub>	-	7	10	13	ΚΩ

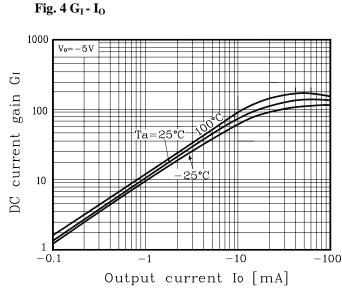
<sup>\* :</sup> Characteristic of transistor only

#### **Electrical Characteristic Curves**







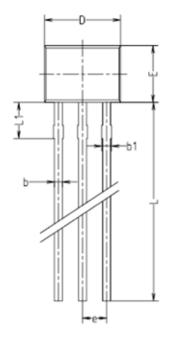


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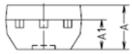
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# **SRA2202M**

## **Outline Dimension**







	TO-92M				
SYMBOL	MINIMUM	NOMINAL	MAXIMUM		
Α	2.25	2.30	2.35		
A1	1.50	1.55	1.60		
b	0.40	0.42	0.44		
b1	0.40	_	0.50		
С	0.40	0.42	0.44		
D	3.93	4.00	4.07		
E	2.93	3.00	3.07		
е	1.17	1.27	1.37		
L	14.30	14.50	14.70		
L1	2.05	2.15	2.25		

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