



PNP Silicon Transistor

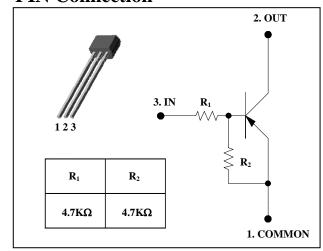
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SRA2201M	2201	TO-92M

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V _I	-20, 10	V
Output current	I _O	-100	mA
Power dissipation	P_{D}	400	mW
Junction temperature	TJ	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_0 = -5V$, $I_0 = -10$ mA	30	55	-	-
Output voltage	$V_{O(ON)}$	I _O =-10mA, I _I =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = -0.2V$, $I_0 = -5mA$	-	-1.5	-2.0	V
Input voltage (OFF)	V _{I(OFF)}	$V_0 = -5V$, $I_0 = -0.1$ mA	-1.0	-1.2	-	V
Transition frequency	$f_{T}^{}^{X}}$	$V_0 = -10V$, $I_0 = -5mA$, $f = 1MHz$	-	200	-	MHz
Input current	I_1	$V_1 = -5V, I_0 = 0$	-	-	-1.8	mA
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	KΩ
Input resistor (Base to common)	R ₂	-	3.3	4.7	6.1	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 P_D - Ta

600

Mu

Gd

400

100

25

50

Ambient temperature Ta [°C]

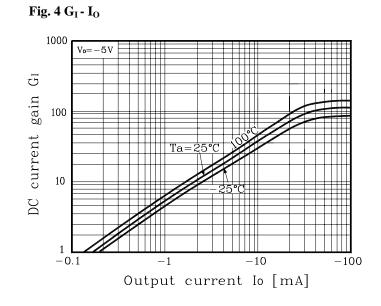
Fig. 2 $I_{\rm O}$ - $V_{I(ON)}$

-0.8

Input off voltage Vi(off) [V]

-0.4

Fig. 3 $I_{\rm O}$ - $V_{\rm I(OFF)}$



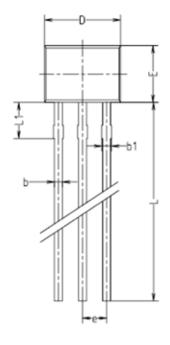
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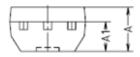
-2.0

SRA2201M

Outline Dimension







	TO-92M				
SYMBOL	MINIMUM	NOMINAL	MAXIMUM		
Α	2.25	2.30	2.35		
A1	1.50	1.55	1.60		
b	0.40	0.42	0.44		
b1	0.40	_	0.50		
С	0.40	0.42	0.44		
D	3.93	4.00	4.07		
E	2.93	3.00	3.07		
е	1.17	1.27	1.37		
L	14.30	14.50	14.70		
L1	2.05	2.15	2.25		

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