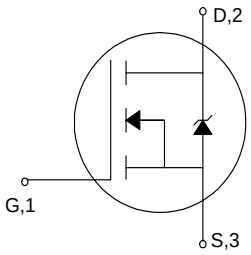
SIEMENS

Preliminary data

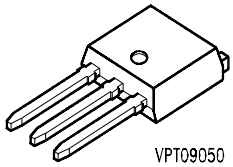
SPU03N60S5
SPD03N60S5

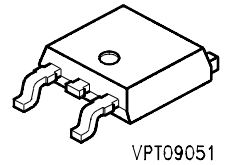
Cool MOS™ Power Transistor

- New revolutionary high voltage technology
- Ultra low gate charge
- Periodic avalanche proved
- Extreme d*v*/d*t* rated
- Optimized capacitances
- Improved noise immunity
- Former development designation:
  SPUx4N60S5/SPDx4N60S5



COOLMOS
Power Semiconductors





Type	V_{DS}	I_D	$R_{DS(on)}$	Package	Marking	Ordering Code
SPU03N60S5	600 V	3.2 A	1.4 Ω	P-TO251-3-1	03N60S5	Q67040-S4227
SPD03N60S5				P-TO252	03N60S5	Q67040-S4187

Maximum Ratings , at T_j = 25 ° C, unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current T_C = 25 °C T_C = 100 °C	I_D	 3.2 2	A
Pulsed drain current, t_p = 1ms [1)] T_C = 25 °C	$I_{D\ puls}$	5.7	
Avalanche energy, single pulse I_D = 3.2 A, V_{DD} = 50 V, R_{GS} = 25 Ω Periodic avalanche energy E_{AR} only limited by T_{jmax}	E_{AS}	100	mJ
Reverse diode d*v*/d*t* I_S = 3.2 A, V_{DS}<V_{DSS}, d*i*/d*t* = 100 A/µs, T_{jmax} = 150 °C	d*v*/d*t*	6	kV/µs
Gate source voltage	V_{GS}	±20	V
Power dissipation T_C = 25 °C	P_{tot}	38	W
Operating and storage temperature	T_j, T_{stg}	-55 ...+150	°C

Electrical Characteristics

Parameter at T_j = 25 °C, unless otherwise specified	Symbol	Values			Unit
		min.	typ.	max.	
Thermal Characteristics					
Thermal resistance, junction - case	R_{thJC}	-	-	3.3	K/W
Thermal resistance, junction - ambient (Leaded and through-hole packages)	R_{thJA}	-	-	75	
SMD version, device on PCB: @ min. footprint @ 6 cm^2 cooling area [2)]	R_{thJA}	 - -	 - -	 75 50	
Static Characteristics					
Drain- source breakdown voltage V_{GS} = 0 V, I_D = 0.25 mA	$V_{(BR)DSS}$	600	-	-	V
Gate threshold voltage, V_{GS} = V_{DS} I_D = 135 µA, T_j = 25 °C	$V_{GS(th)}$	3.5	4.5	5.5	
Zero gate voltage drain current, V_{DS}=V_{DSS} V_{GS} = 0 V, T_j = 25 °C V_{GS} = 0 V, T_j = 150 °C	I_{DSS}	 - -	 0.5 -	 1 70	µA
Gate-source leakage current V_{GS} = 20 V, V_{DS} = 0 V	I_{GSS}	-	-	100	nA
Drain-Source on-state resistance V_{GS} = 10 V, I_D = 2 A	$R_{DS(on)}$	-	1.26	1.4	Ω

[1] current limited by T_{jmax}

[2] Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6 cm2 (one layer, 70µm thick) copper area for drain connection. PCB is vertical without blown air.

Electrical Characteristics

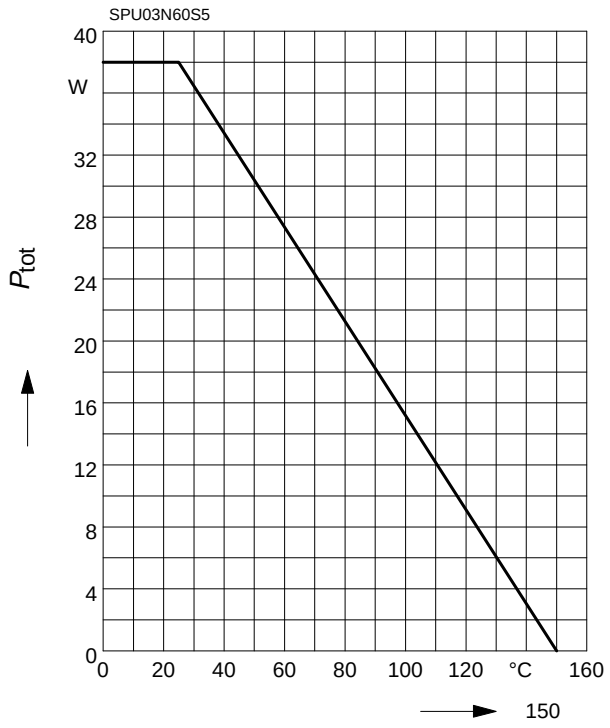
Parameter at T_j = 25 °C, unless otherwise specified	Symbol	Values			Unit
		min.	typ.	max.	
Characteristics					
Transconductance $V_{DS}\geq 2*I_D*R_{DS(on)max}$, I_D = 2 A	g_{fs}	-	1.8	-	S
Input capacitance V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{iss}	-	440		pF
Output capacitance V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{oss}	-	230		
Reverse transfer capacitance V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz	C_{rss}	-	12		
Turn-on delay time V_{DD} = 350 V, V_{GS} = 10 V, I_D = 3.2 A, R_G = 20 Ω	$t_{d(on)}$	-	40		ns
Rise time V_{DD} = 350 V, V_{GS} = 10 V, I_D = 3.2 A, R_G = 20 Ω	t_r	-	30	-	
Turn-off delay time V_{DD} = 350 V, V_{GS} = 10 V, I_D = 3.2 A, R_G = 20 Ω	$t_{d(off)}$	-	60		
Fall time V_{DD} = 350 V, V_{GS} = 10 V, I_D = 3.2 A, R_G = 20 Ω	t_f	-	30	-	

Electrical Characteristics

Parameter at T_j = 25 °C, unless otherwise specified	Symbol	Values			Unit
		min.	typ.	max.	
Gate Charge Characteristics					
Gate-source charge I_D = 3.2 A, V_{DD} = 350 V	Q_{gs}	-	3	-	nC
Gate-drain charge I_D = 3.2 A, V_{DD} = 350 V	Q_{gd}	-	7.5	-	
Total gate charge V_{DD} = 350 V, I_D = 3.2 A, V_{GS} = 0 to 10 V	Q_g	-	12.8	-	
Reverse Diode					
Inverse diode continuous forward current T_C = 25 °C	I_S	-	-	3.2	A
Inverse diode direct current,pulsed T_C = 25 °C	I_{SM}	-	-	5.7	
Inverse diode forward voltage V_{GS} = 0 V, I_F = 3.2 A	V_{SD}	-	1	1.2	V
Reverse recovery time V_R = 350 V, I_F=I_S , di_F/dt = 100 A/µs	t_{rr}	-	1000	-	ns
Reverse recovery charge V_R = 350 V, I_F=I_S , di_F/dt = 100 A/µs	Q_{rr}	-	2.3	-	µC

Power Dissipation

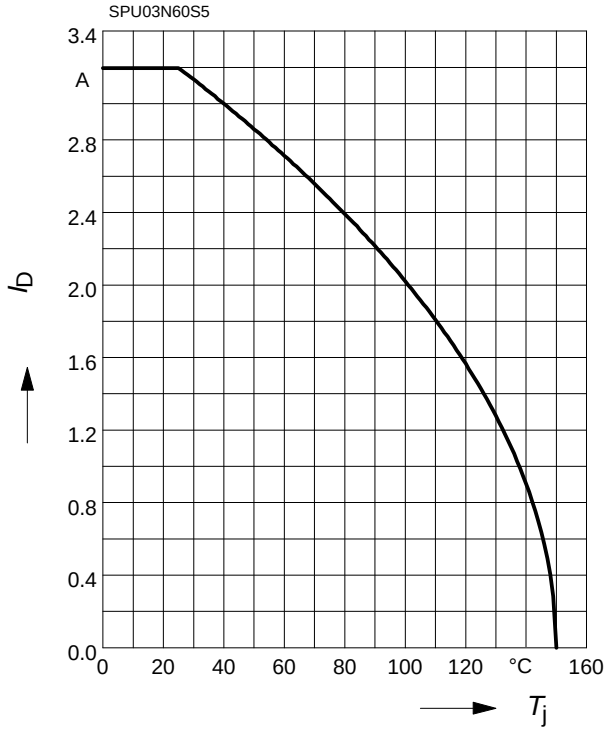
$P_{tot} = f(T_C)$



Drain current

$I_D = f(T_C)$

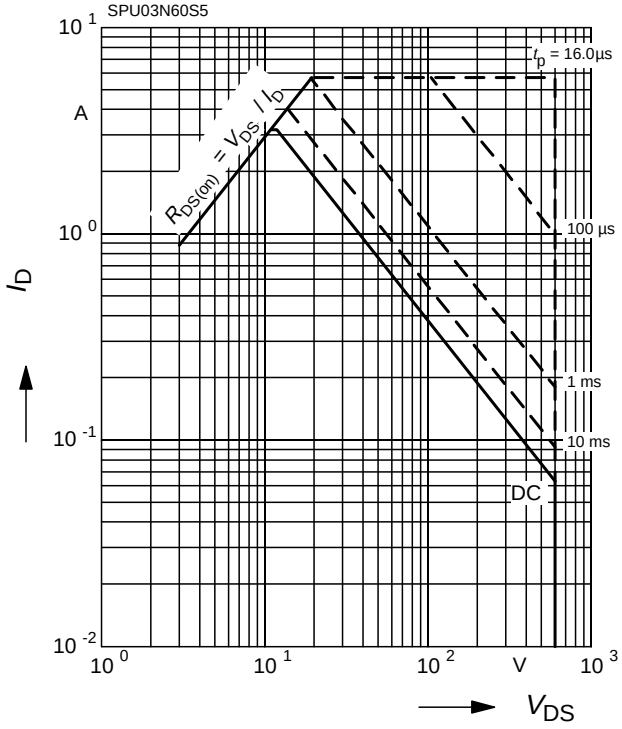
parameter: $V_{GS} \geq 10$ V



Safe operating area

$I_D = f(T_C)$

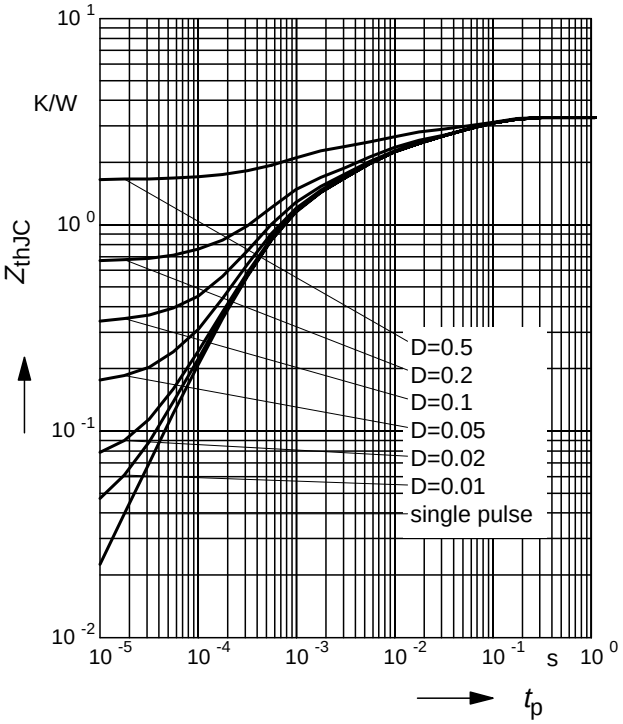
parameter: D=0.01, T_C=25°C



Transient thermal impedance

$Z_{thJC} = f(t_P)$

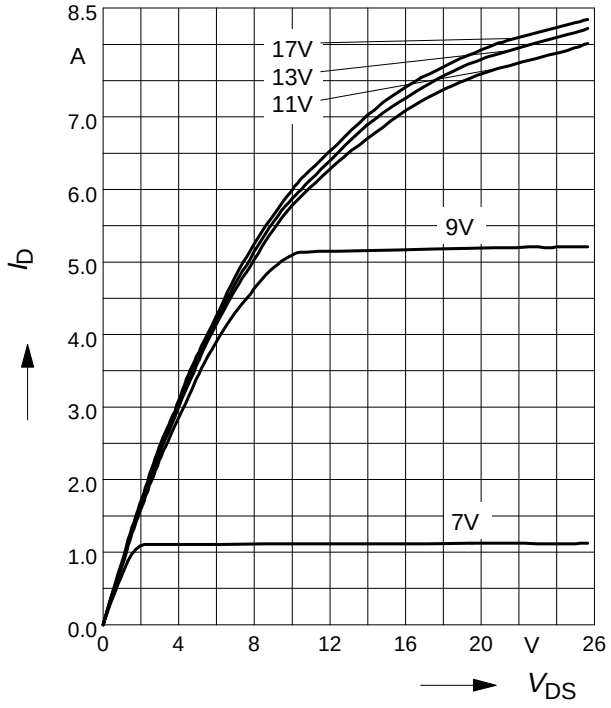
Parameter: $D = t_P/T$



Typ. output characteristic

$I_D = f(V_{DS})$; T_j=25°C

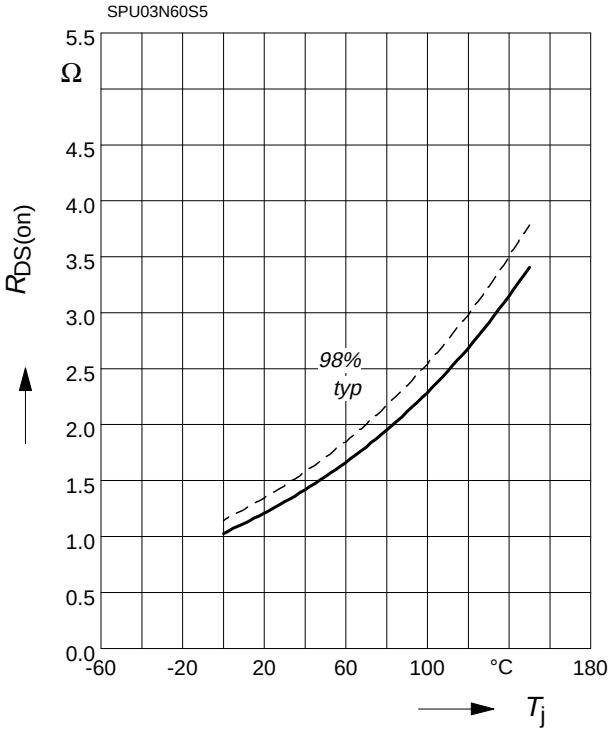
Parameter: V_{GS}



Drain-source on-resistance

$R_{DS(on)} = f(T_j)$

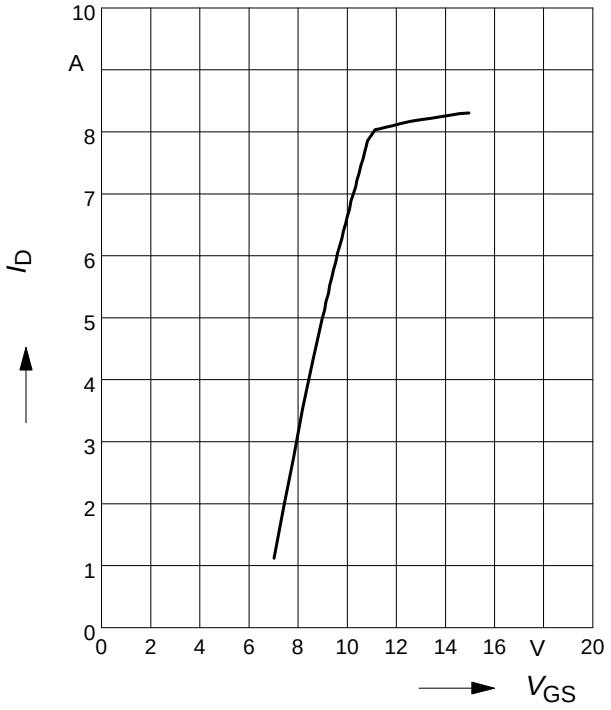
parameter : I_D = 2 A, V_{GS} = 10 V



Typ. transfer characteristics $I_D = f(V_{GS})$

parameter: t_p = 80 µs

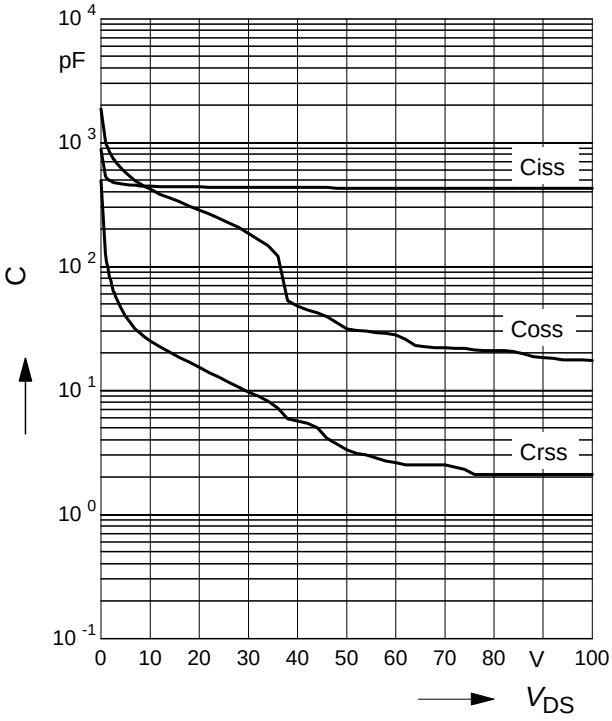
$V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$



Typ. capacitances

$C = f(V_{DS})$

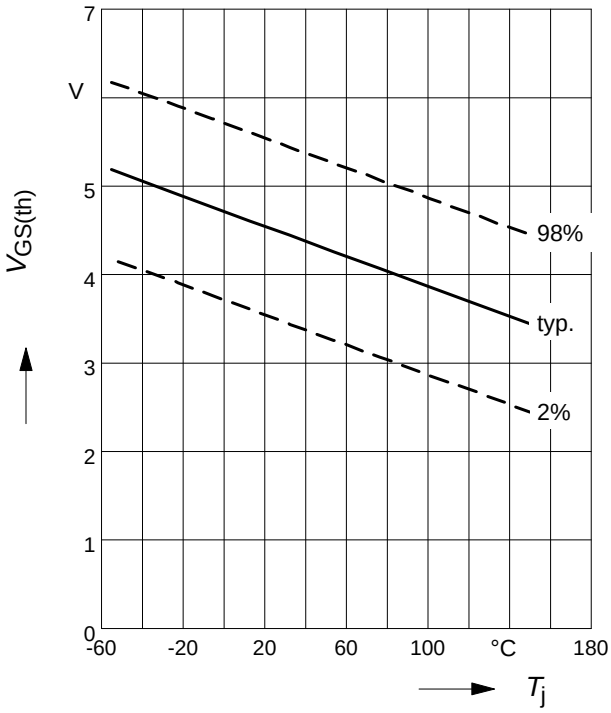
Parameter: V_{GS}=0 V, f=1 MHz



Gate threshold voltage

$V_{GS(th)} = f(T_j)$

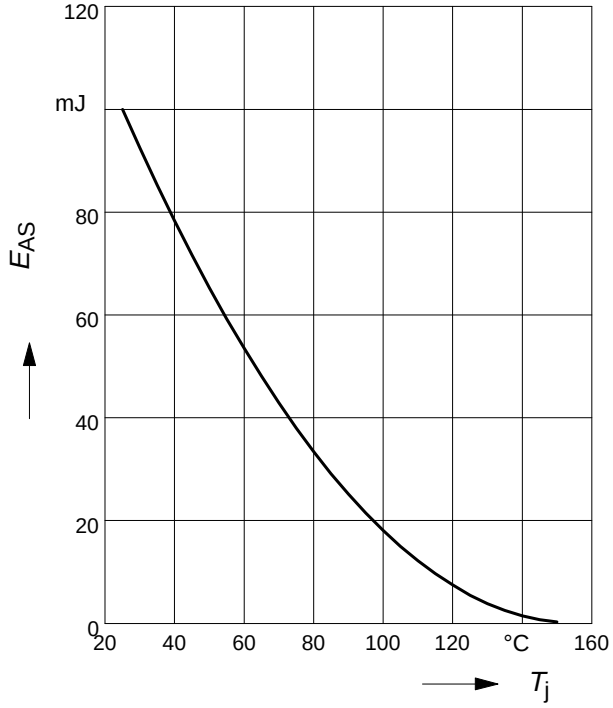
parameter: $V_{GS} = V_{DS}$, I_D = 135 µA



Avalanche Energy $E_{AS} = f(T_j)$

parameter: I_D = 3.2 A, V_{DD} = 50 V

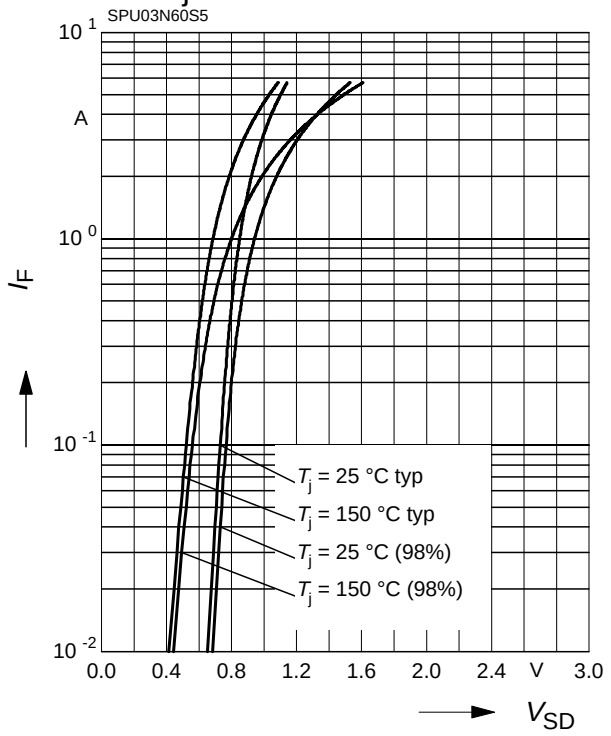
R_{GS} = 25 Ω



Forward characteristics of reverse diode

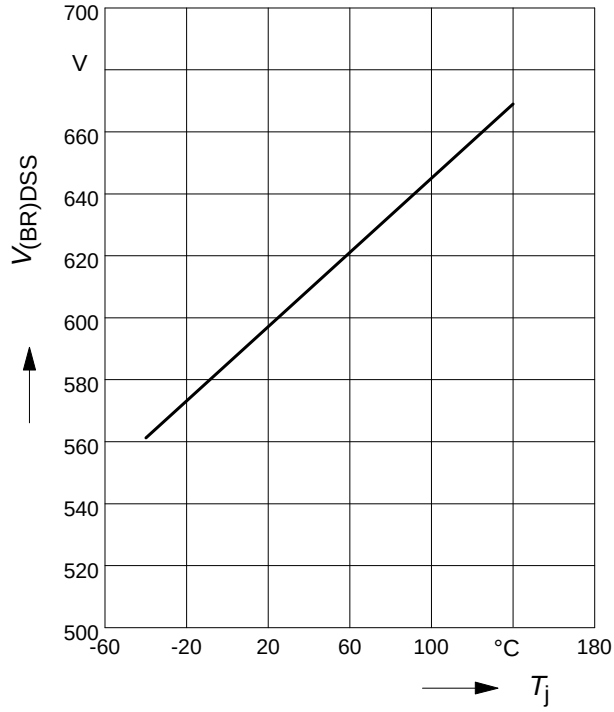
$I_F = f(V_{SD})$

parameter: T_j , tp = 80 µs



Drain-source break down voltage

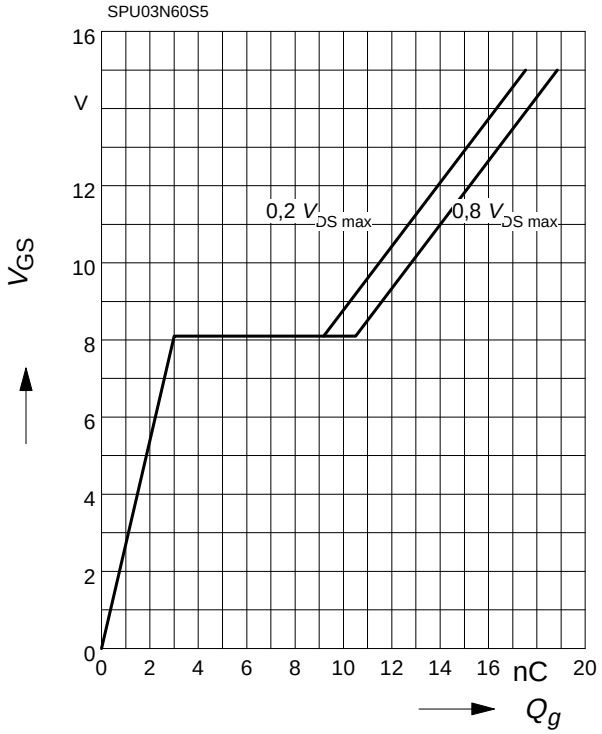
$V_{(BR)DSS} = f(T_j)$



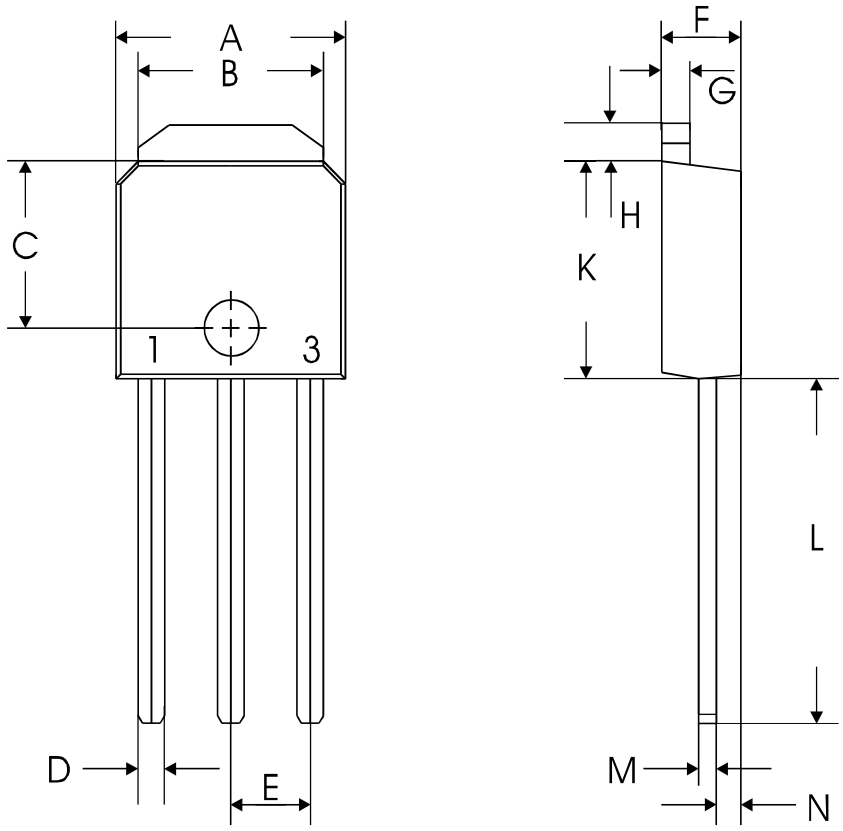
Typ. gate charge

$V_{GS} = f\ (Q_{Gate})$

parameter: I_{Dpuls} = 3.2 A

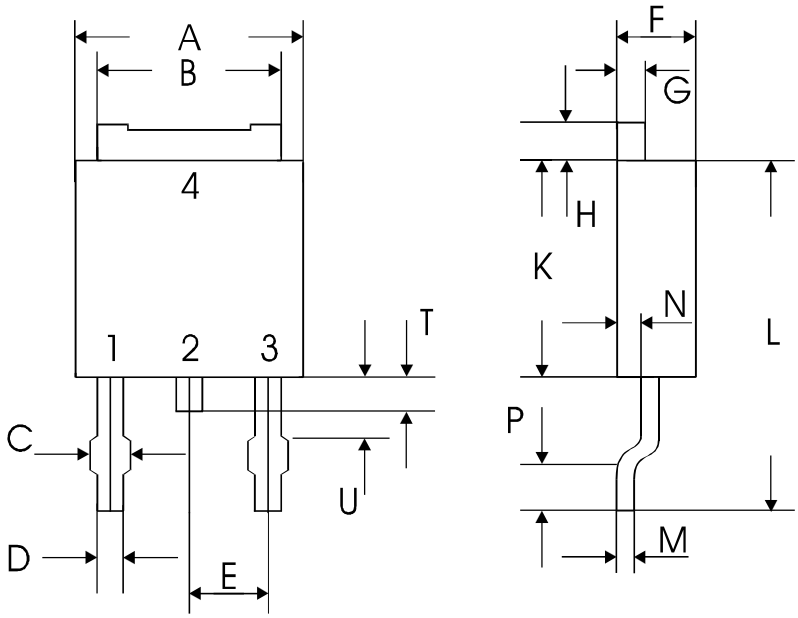


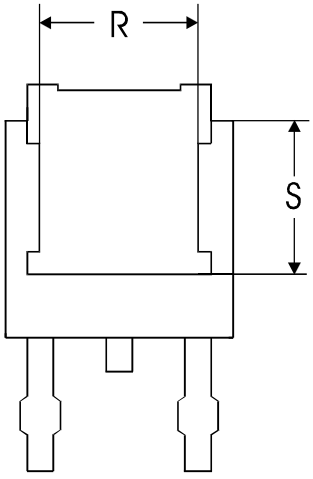
P-TO251-3-1



	dimensions [mm]	
symbol	min	max
A	6.47	6.73
B	5.25	5.41
C	4.19	4.43
D	0.63	0.89
E	2.29 typ.	
F	2.18	2.39
G	0.76	0.86
H	1.01	1.11
K	5.97	6.23
L	9.14	9.65
M	0.46	0.56
N	0.98	1.15

P-TO252





BACK VIEW

	dimensions [mm]	
symbol	min	max
A	6.40	6.73
B	5.25	5.50
C	(0.65)	(1.15)
D	0.63	0.89
E	2.28	
F	2.19	2.39
G	0.76	0.98
H	0.90	1.21
K	5.97	6.23
L	9.40	10.40
M	0.46	0.58
N	0.87	1.15
P	0.51	-
R	5.00	-
S	4.17	-
T	0.26	1.02
U	-	-

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