Pletronics

Crystal Controlled Oscillators PECL OUTPUT

RoHS 6/6 Compliant

PE55D Series

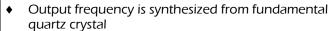
LCC Ceramic Package 3.2 x 5.0 x 1.35 mm

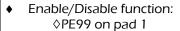
- 3rd Overtone crystal
- ♦ Enable/Disable function on pad 1
- Small SMD package for high density board designs



PE9xD Series

LCC Ceramic Package 5.0 x 7.0 x 1.7 mm





♦PE93 on pad 2



LCC Ceramic Package 5.0 x 7.0 x 1.7 mm

- 3rd Overtone crystal
- ♦ Enable/Disable function on pad 1





VPU7 Series - vcxo

LCC Ceramic Package 5.0 x 7.0 x 2.97 mm



- Output frequency is synthesized from fundamental quartz crystal
- Enable/Disable function on pad 2
- +/- 50 PPM APR

FEATURES

- Excellent phase noise characteristics
- ♦ All have Tri-State with Power Standby modes
- ♦ Temp range: 10° to +70°C Standard, -40° to +85°C Extended
- ♦ Available Tolerances: +/- 20PPM, +/- 25PPM, and +/- 50PPM
- ♦ Most common frequencies in stock

APPLICATIONS

- Ideal reference clocks for high-speed applications requiring low jitter, including:
 - ♦ 1/10 Gigabit Ethernet
 - ♦ 2/4/10G Fibre Channel
 - ♦ Server & Storage platforms
 - ♦ SONET/SDH linecards.

Pletronics Part Series	Package Size	Frequency Range (MHz)	Tr/Tf Maximum (pS)	Duty Cycle %	RMS Jitter (pS)		Current	Supply Voltage		Supply
					12KHz- 20MHz	10Hz- 20MHz	with Load (ma)	2.5V	3.3V	Noise Rejection
PE55	3.2×5 mm	40-250	700	45-55	0.6	2.8	90	*	*	Good
PE77	5x7 mm	40-325	700	45-55	0.6	2.8	90	*	*	Good
PE99 PE93	5×7 mm	10.9-766 876-1,170	300	47-53	0.6	2.8	90		*	Excellent
VPU7	5x7 mm	10.9-766 876-1,170	300	47-53	0.8	3.2	110		*	Good

See individual data sheets and standard part number builder on www.pletronics.com for allowable configurations and complete specifications

Standard Part Number Generation

