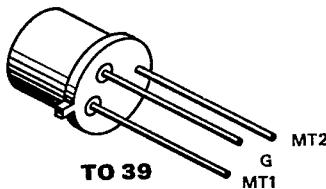


8834750 TAG SEMICONDUCTORS LTD

63C 00783 D T-25-13

TAG SEMICONDUCTORS LTD


**Z0302BG –  
Z0302MG TRIACS**
**3.0 A 200–800 V  
3/3/3 mA**

The Z0302 series of TRIAC's are high performance PNPN devices diffused with TAG's proprietary Top Glass™ Process. These parts are intended for general purpose applications where logic compatible gate sensitivity is required.

**Absolute Maximum Ratings**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Parameter	Part Nr.	Symbol	Min.	Max.	Unit	Test Conditions
Repetitive Peak Off State Voltage	Z0302BG	$V_{DRM}$	200		V	
	Z0302DG		400		V	$T_j = -40^\circ\text{C} \text{ to } 125^\circ\text{C}$
	Z0302MG		600		V	$R_{GK} = 1\text{ k}\Omega$
On-State Current		$I_T(\text{RMS})$	3.0		A	All Conduction Angles $T_C = 85^\circ\text{C}$
Nonrept. On-State Current		$I_{TSM}$	22		A	Half Cycle, 60 Hz
Nonrept. On-State Current		$I_{TSM}$	20		A	Half Cycle, 50 Hz
Fusing Current		$I^2t$	2		$\text{A}^2\text{s}$	$t = 10\text{ ms}$
Peak Gate Current		$I_{GM}$	1.2		A	10 $\mu\text{s}$ max.
Peak Gate Dissipation		$P_{GM}$	3		W	10 $\mu\text{s}$ max.
Gate Dissipation		$P_G(\text{AV})$	0.2		W	20 ms max.
Operating Temperature		$T_j$	-55	125	$^\circ\text{C}$	
Storage Temperature		$T_{stg}$	-65	150	$^\circ\text{C}$	
Soldering Temperature		$T_{sld}$		250	$^\circ\text{C}$	1.6 mm from case, 10 s max.

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Min.	Max.	Unit	Test Conditions
Off-State Leakage Current	$I_{DRM}$	200	$\mu\text{A}$	$V_D = V_{DRM}$	$R_{GK} = 1\text{ k}\Omega$ $T_j = 125^\circ\text{C}$
Off-State Leakage Current	$I_{DRM}$	5	$\mu\text{A}$	$V_D = V_{DRM}$	$R_{GK} = 1\text{ k}\Omega$ $T_j = 25^\circ\text{C}$
On-State Voltage	$V_T$	1.85	V		at $I_T = 4.5\text{ A}$ , $T_j = 25^\circ\text{C}$
On-State Threshold Voltage	$V_{T(\text{TO})}$	0.95	V		$T_j = 125^\circ\text{C}$
On-State Slope Resistance	$r_T$	200	$\text{m}\Omega$		$T_j = 125^\circ\text{C}$
Gate Trigger Current	$I_{GT\text{I+}}$ (1)	3	$\text{mA}$	$V_D = 12\text{ V}$	
	$I_{GT\text{I-}}$ (2)	3	$\text{mA}$	$V_D = 12\text{ V}$	
	$I_{GT\text{III-}}$ (3)	3	$\text{mA}$	$V_D = 12\text{ V}$	
	$I_{GT\text{III+}}$ (4)	3	$\text{mA}$	$V_D = 12\text{ V}$	
Gate Trigger Voltage	$V_{GT}$	2	V	$V_D = 12\text{ V}$	All Quadrants
Holding Current	$I_H$	3	$\text{mA}$		$R_{GK} = 1\text{ k}\Omega$
Critical Rate of Voltage Rise	$dv/dt$	30		$\text{V}/\mu\text{s}$	$V_D = .67 \times V_{DRM}$ $R_{GK} = 1\text{ k}\Omega$ $T_j = 125^\circ\text{C}$
Critical Rate of Rise, Off-State	$dv/dt_c$	1		$\text{V}/\mu\text{s}$	$I_T = 0.8\text{ A}$ $di/dt = 0.35\text{ A/ms}$ $T_C = 85^\circ\text{C}$
Thermal Resistance junc. to case	$R_{\Theta jc}$	9		K/W	
Thermal Resistance junc. to amb.	$R_{\Theta ja}$	160		K/W	

Z03