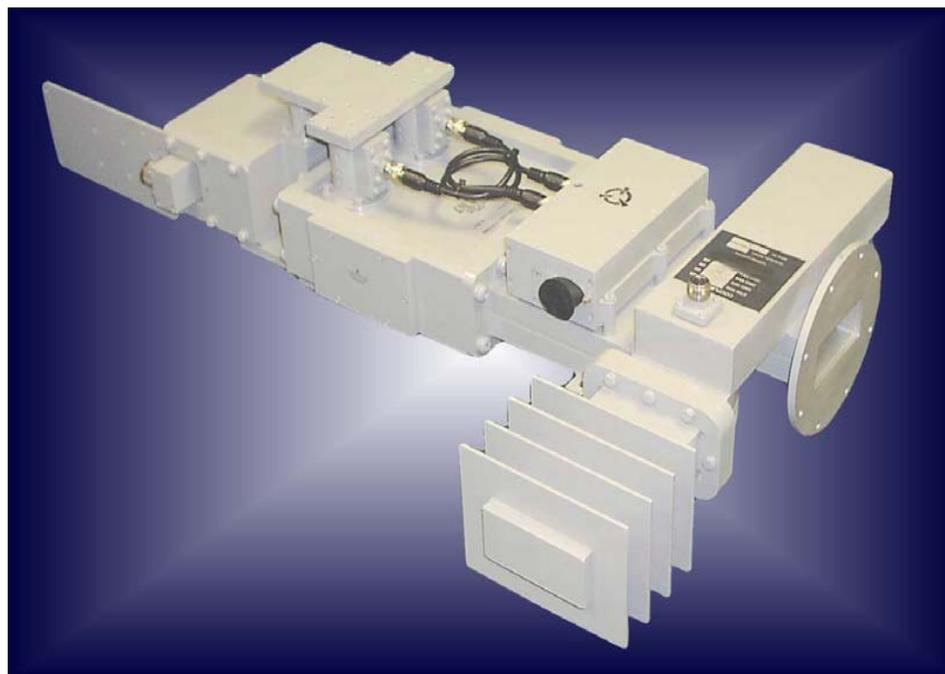


VDS1793 Solid State Receiver Protector



Features:

- Frequency: 2.7 to 3.0 GHz
- Power: 100 kW peak
- Pulsewidth: 4.5 μ Sec max
- Duty Cycle: .002 max
- Insertion Loss: 1.0 dB max
- Input VSWR: 1.5:1 max
- Flat Leakage: 35 mW max
- Spike Leakage: 100 mW max
- Recovery Time: 2.0 μ S max
- Input coupler: 20 dB
- Includes switch driver with BITE output feature.
- Operating Temperature: -40 to +55 $^{\circ}$ C
- Input Waveguide: WR284
- Output: Type N Male

Description:

High power quasi-active receiver protector consisting of an active high power input stage followed by a passive limiter. In addition, there is a 20 dB directional coupler on the input. The unit is non-reflective.

150 Sohier Road
Beverly, Massachusetts 01915



Phone: 978-922-6000 Fax: 978-922-2736
E-Mail: marketing@bmd.cpii.com
Internet: www.cpii.com/bmd