

Dual N-channel MOSFET

ELM38800BA-S

■ General description

ELM38800BA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds}=20V$
- $I_d=6.3A$
- $R_{ds(on)} < 22m\Omega$ ($V_{gs}=4.5V$)
- $R_{ds(on)} < 26m\Omega$ ($V_{gs}=2.5V$)
- $R_{ds(on)} < 34m\Omega$ ($V_{gs}=1.8V$)

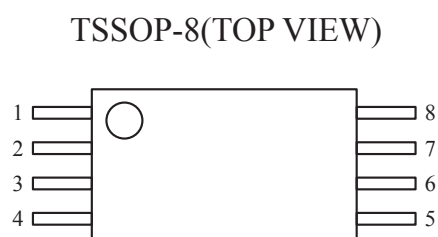
■ Maximum absolute ratings

Parameter	Symbol	Limit	Unit	Note	
Drain-source voltage	V_{ds}	20	V		
Gate-source voltage	V_{gs}	± 12	V		
Continuous drain current	I_d	$T_a=25^\circ C$	6.3	A	4
		$T_a=70^\circ C$	5.0		
Pulsed drain current	I_{dm}	50	A	3, 4	
Avalanche current	I_{as}	22	A		
Avalanche energy	E_{as}	23	mJ		
Power dissipation	P_d	$T_a=25^\circ C$	1.4	W	
		$T_a=70^\circ C$	0.9		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	$^\circ C$		

■ Thermal characteristics

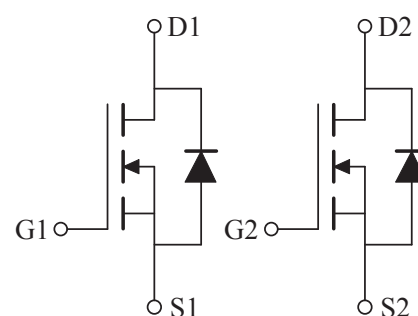
Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R\theta_{ja}$		90	$^\circ C/W$	5

■ Pin configuration



Pin No.	Pin name
1	GATE1
2	SOURCE1
3	SOURCE1
4	DRAIN1
5	DRAIN2
6	SOURCE2
7	SOURCE2
8	GATE2

■ Circuit



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■Electrical characteristics

Ta=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Id=250μA, Vgs=0V	20			V	
Zero gate voltage drain current	Idss	Vds=16V, Vgs=0V			1	μA	
		Vds=10V, Vgs=0V, Tj=70°C			10		
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V			±100	nA	
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250μA	0.5	0.7	1.0	V	
On state drain current	Id(on)	Vgs=5V, Vds=5V	25			A	1
Static drain-source on-resistance	Rds(on)	Vgs=4.5V, Id=6A		15	22	mΩ	1
		Vgs=2.5V, Id=5A		18	26		
		Vgs=1.8V, Id=4A		24	34		
Forward transconductance	Gfs	Vds=5V, Id=6A		35		S	1
Diode forward voltage	Vsd	If=6A, Vgs=0V			1	V	1
Max.body-diode continuous current	Is				6.3	A	
DYNAMIC PARAMETERS							
Input capacitance	Ciss			917		pF	
Output capacitance	Coss	Vgs=0V, Vds=10V, f=1MHz		134		pF	
Reverse transfer capacitance	Crss			122		pF	
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz		3		Ω	
SWITCHING PARAMETERS							
Total gate charge	Qg			12.7		nC	2
Gate-source charge	Qgs	Vgs=4.5V, Vds=10V, Id=6A		1.5		nC	2
Gate-drain charge	Qgd			4.4		nC	2
Turn-on delay time	td(on)			18.0		ns	2
Turn-on rise time	tr	Vgs=4.5V, Vds=10V, Id≈6A Rgen=6Ω		1.5		ns	2
Turn-off delay time	td(off)			4.7		ns	2
Turn-off fall time	tf			49.0		ns	2
Body diode reverse recovery time	trr	If=6A, dl/dt=100A/μs		13		ns	
Body diode reverse recovery charge	Qrr			4		nC	

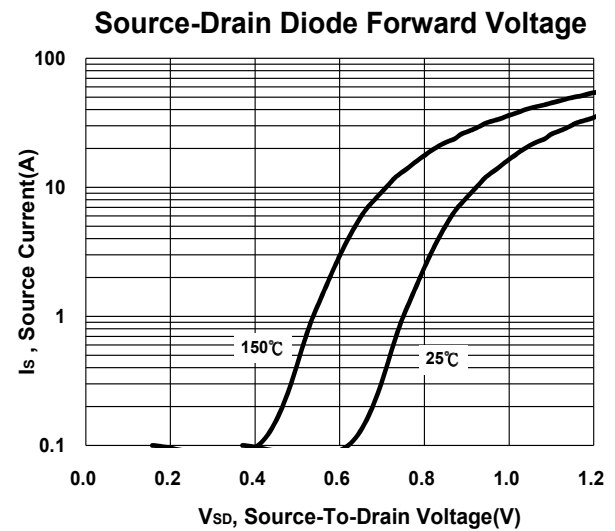
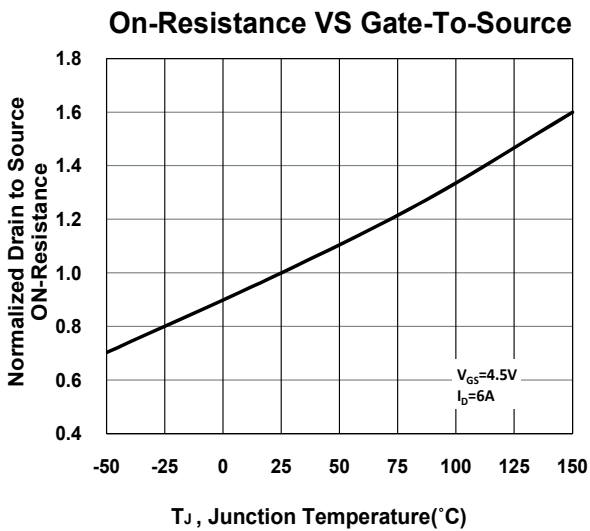
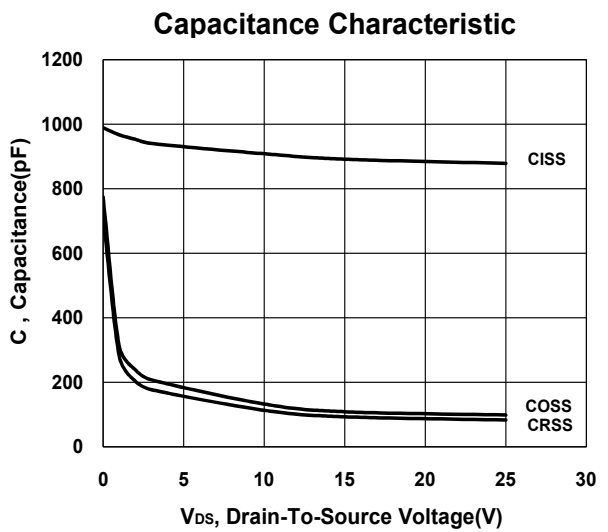
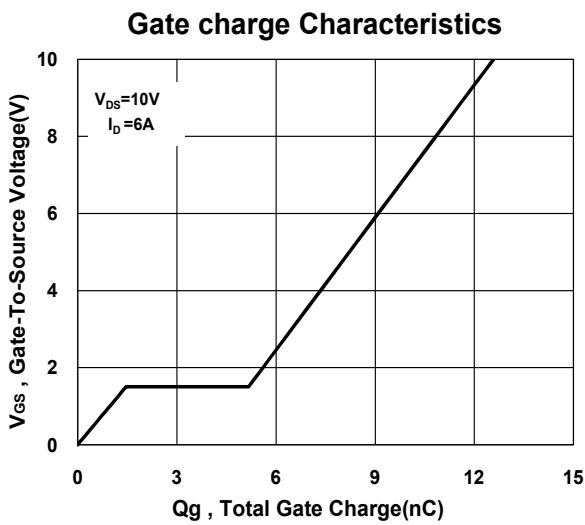
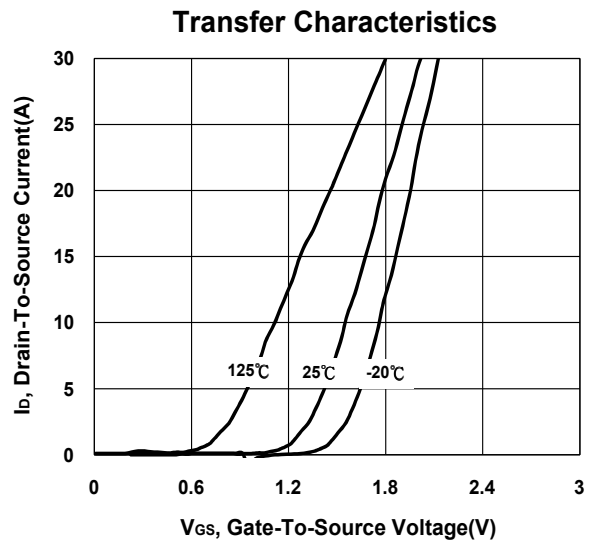
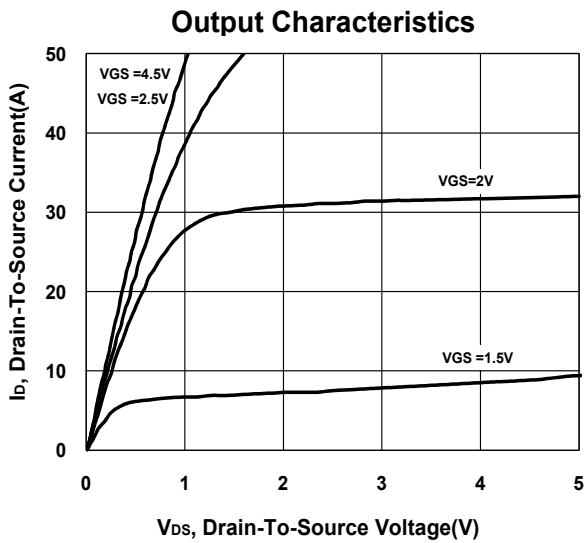
NOTE :

1. Pulsed width≤300μsec and Duty cycle≤2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Limited only by allowed maximum temperature.
5. The value of Rθja is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with Ta =25°C. The value in any given application depends on the user's specific board design.

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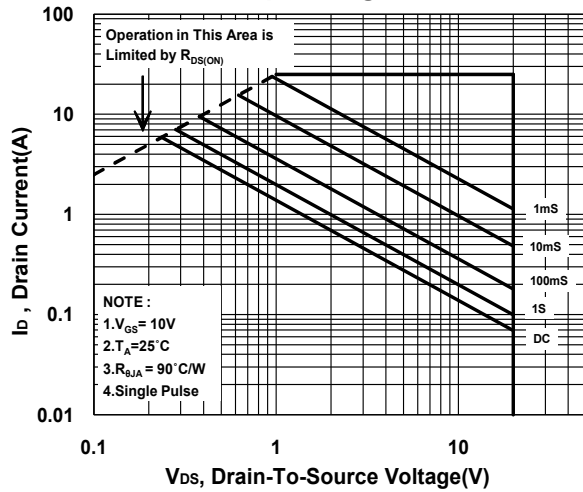
■ Typical electrical and thermal characteristics



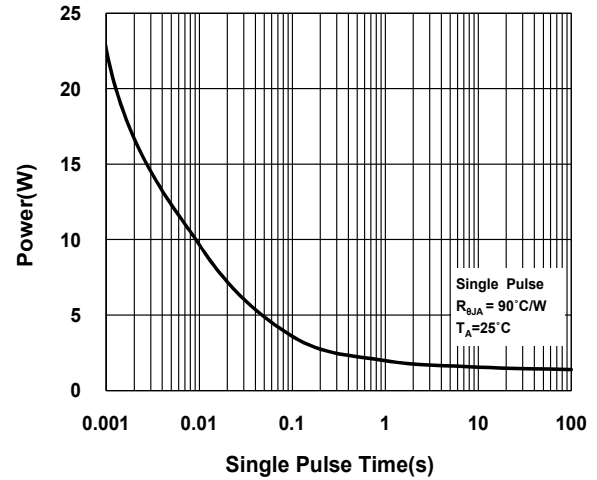
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Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

