

# **STC405D**

**NPN Silicon Transistor** 

### **Applications**

- Power amplifier application
- High current switching application

#### **Features**

- Low saturation switching application
- Voltage regulator application
- High Voltage : V<sub>CEO</sub>=60V Min.

## **Ordering Information**

B C E	C C E		
TO-252			

**PIN Connection** 

Type NO.	Marking	Package Code
	STC	
STC405D	405	TO-252
	YWW	

YWW(Y: Year code, WW: Weekly code)

#### **Absolute maximum ratings**

Characteristic	Symbol	Rating	Unit
Collector-Base voltage	$V_{CBO}$	80	V
Collector-Emitter voltage	$V_{\sf CEO}$	60	V
Emitter-base voltage	$V_{EBO}$	5	V
Collector current	I <sub>C</sub>	5	A(DC)
Collector current	I <sub>CP</sub> *	10	A(Pulse)
Collector Power dissipation (Tc=25℃)	P <sub>C</sub>	15	W
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	$T_{stg}$	-55~150	°C

<sup>\*:</sup> Single pulse, tp= 300  $\mu$ s

#### **Electrical Characteristics**

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 80V, I_{E} = 0$	-	1	10	μΑ
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = 5V$ , $I_{C} = 0$	-	ı	10	μΑ
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=1$ mA, $I_B=0$	60	ı	-	V
DC gurrent gain	h <sub>FE</sub>	$V_{CE}=5V$ , $I_{C}=1A$	200	ı	400	-
DC current gain		$V_{CE}=5V$ , $I_{C}=3A$	80	1		-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	$I_{C}=3A$ , $I_{B}=300mA$	1	1	1	V
Base-Emitter saturation voltage	$V_{BE(SAT)}$	$I_{C}=3A$ , $I_{B}=300mA$	-	1	1.5	V
Transition frequency	f <sub>T</sub>	$V_{CB}=5V$ , $I_{C}=50mA$	-	8	1	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB}=10V$ , $I_{E}=0$ , $f=1MHz$	-	25	1	рF

<sup>\*</sup> HFE rank : 200~400 Only

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#### **Electrical Characteristic Curves**

Fig. 1  $P_C$  - Ta

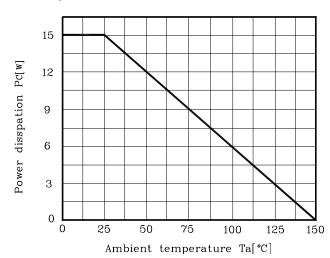


Fig. 2  $V_{CE(sat)}$  -  $I_{C}$ 

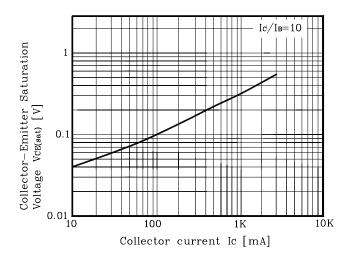


Fig. 3 h<sub>FE</sub>-I<sub>C</sub>

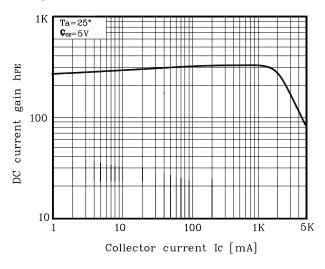


Fig. 4  $I_C$  -  $V_{CE}$ 

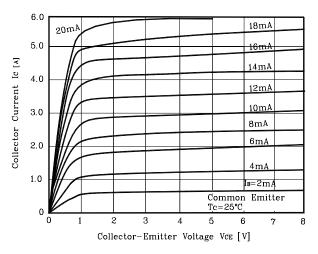
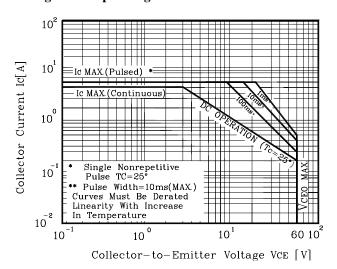


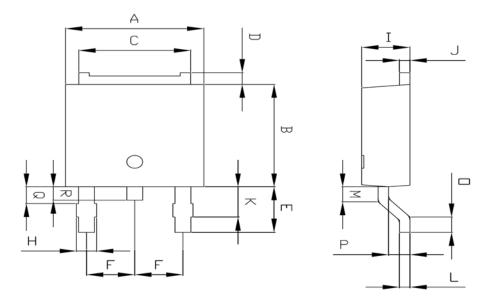
Fig. 5 Safe operating Area



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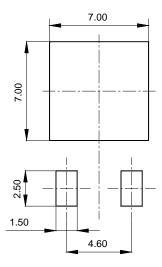
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# **Outline Dimension**



	1	NOTE		
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	INOTE
А	6.40	6.60	6.80	
В	5.90	6.10	6.30	
С	5.04	5.34	5.64	
D	0.50	0.70	0.90	
E	2.50	2.70	2.90	
F	2.10	2.30	2.50	
Н	0.96 MAX			
- 1	2.20	2.30	2.40	
J	0.40	0.50	0.60	
K	1.60	1.80	2.00	
L	0.40	0.50	0.60	
М	0.81	0.91	1.01	
0	0.80	0.90	1.00	
Р	0.90	1.00	1.10	
Q		0.95 MAX		
R	0.60	0.80	1.00	

### \*Recommend PCB solder land [Unit: mm]



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