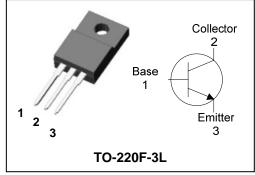


STC405 NPN Silicon Transistor

#### Features

- Low saturation switching application
- Voltage regulator application
- High Voltage : V<sub>CEO</sub>=60V Min.

#### **PIN Connection**



### **Ordering Information**

Type NO.	Marking	Package Code	
STC405	STC405	TO-220F-3L	

## **Marking Diagram**

0	Column 1 : Manufacturer
AUK ∆YMDD STC405 ◯	Column 2 : Production Information - △ : Factory Management Code - YMDD : Date Code (Year, Month, Date)
	Column 3 : Device Code

### Absolute maximum ratings

Characteristic	Symbol	Rating	Unit	
Collector-Base voltage	V <sub>CBO</sub>	80	V	
Collector-Emitter voltage	V <sub>CEO</sub>	60	V	
Emitter-base voltage	V <sub>EBO</sub>	5	V	
Collector current	Ι <sub>C</sub>	5	А	
Collector Power dissipation (Tc=25 $^{\circ}$ C)		20	W	
Collector Power dissipation (Ta=25℃)	P <sub>C</sub>	2		
Junction temperature	nperature T <sub>j</sub>		°C	
Storage temperature	T <sub>stg</sub>	-55~150	°C	

Characteristic		Symbol	Тур.	Max.	Unit
Thermal resistance	Junction-case	R <sub>th(J-C)</sub>	-	6.25	
	Junction-ambient	R <sub>th(J-A)</sub>	-	62.5	°C/W

## **Electrical Characteristics**

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 80V, I_{E} = 0$	-	-	10	μΑ
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB}=5V$ , $I_{C}=0$	-	-	10	μA
Collector-Emitter breakdown voltage	V <sub>(BR)CEO</sub>	$I_{c}=1mA$ , $I_{B}=0$	60	-	-	V
DC current gain	h <sub>FE</sub> *	$V_{CE}$ =5V, $I_{C}$ =1A	200	-	400	-
		$V_{CE}=5V$ , $I_{C}=3A$	80	-		-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =3A, I <sub>B</sub> =300mA	-	-	1	V
Base-Emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =3A, I <sub>B</sub> =300mA	-	-	1.5	V
Transition frequency	$f_{T}$	$V_{CB}$ =5V, $I_{C}$ =50mA	-	8	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB}$ =10V, $I_E$ =0, f=1MHz	-	25	-	pF

\* hFE rank : 200~400 Only

# **STC405**

## **Electrical Characteristic Curves**

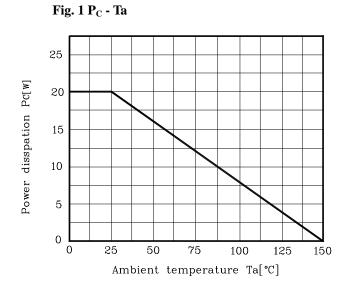
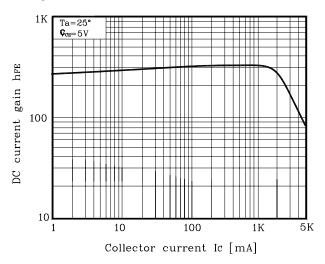


Fig. 3 h<sub>FE</sub>.I<sub>C</sub>





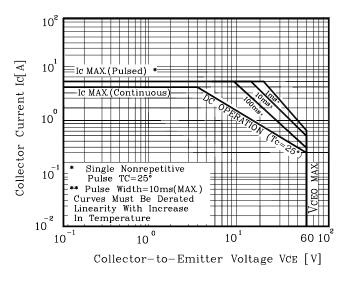
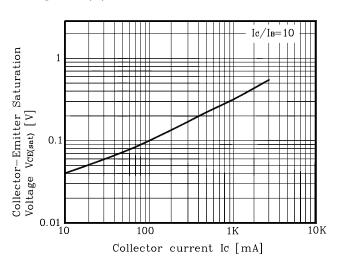
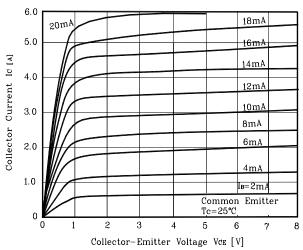


Fig. 2 V<sub>CE(sat)</sub> - I<sub>C</sub>

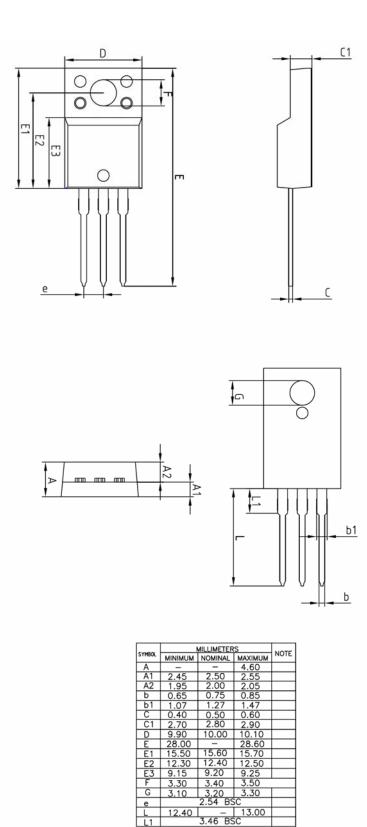






**STC405** 

## **Outline Dimension**



# **STC405**

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