

PMV20XN 30 V, 4.8 A N-channel Trench MOSFET Rev. 1 — 5 April 2011

Product data sheet

Product profile 1.

1.1 General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Low threshold voltage
- Very fast switching

1.3 Applications

- Relay driver
- High-speed line driver

- Trench MOSFET technology
- Low-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V_{DS}	drain-source voltage	T _j = 25 °C		-	-	30	V
V_{GS}	gate-source voltage			-12	-	12	V
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C	<u>[1]</u>	-	-	4.8	А
Static char	acteristics						
R _{DSon}	drain-source on-state resistance	$V_{GS} = 4.5 \text{ V}; I_D = 4.8 \text{ A};$ $T_j = 25 \text{ °C}$		-	19	25	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².



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2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	S	source		B
3	D	drain		G (EA)
			SOT23 (TO-236AB)	mbb076 S

3. Ordering information

Table 3. Orderin	g information		
Type number	Package		
	Name	Description	Version
PMV20XN	TO-236AB	plastic surface-mounted package; 3 leads	SOT23

4. Marking

Table 4.Marking codes

Type number	Marking code ^[1]
PMV20XN	KW%

[1] % = placeholder for manufacturing site code

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5. Limiting values

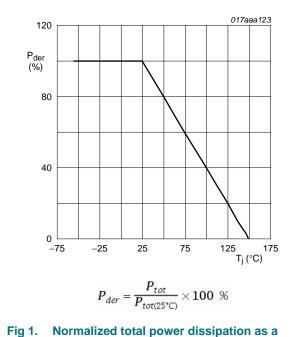
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

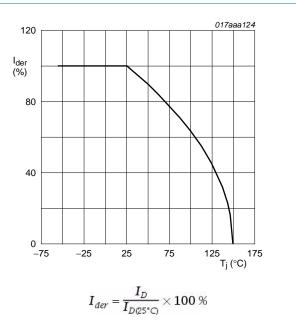
Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	30	V
V _{GS}	gate-source voltage			-12	12	V
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C	<u>[1]</u>	-	4.8	А
		V_{GS} = 4.5 V; T_{amb} = 100 °C	<u>[1]</u>	-	3	А
I _{DM}	peak drain current	$T_{amb} = 25 \text{ °C}$; single pulse; $t_p \le 10 \mu\text{s}$		-	20	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	510	mW
			<u>[1]</u>	-	930	mW
		T _{sp} = 25 °C		-	4170	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	in diode					
Is	source current	T _{amb} = 25 °C	<u>[1]</u>	-	1	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

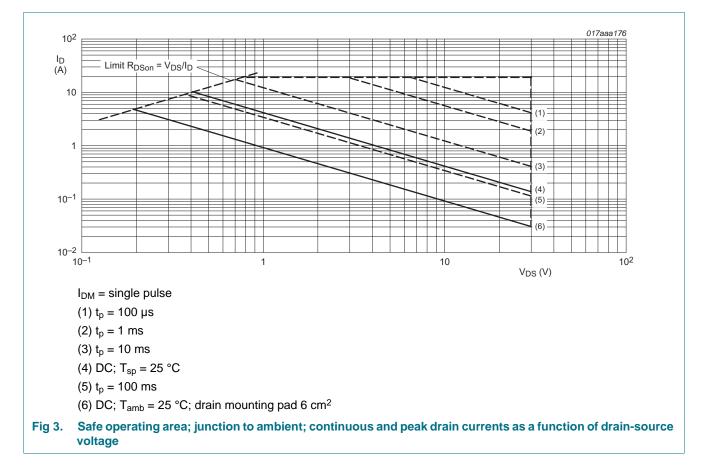


function of junction temperature





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6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	in free air	<u>[1]</u>	-	207	245	K/W
	from junction to ambient		[2]	-	116	135	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	20	30	K/W

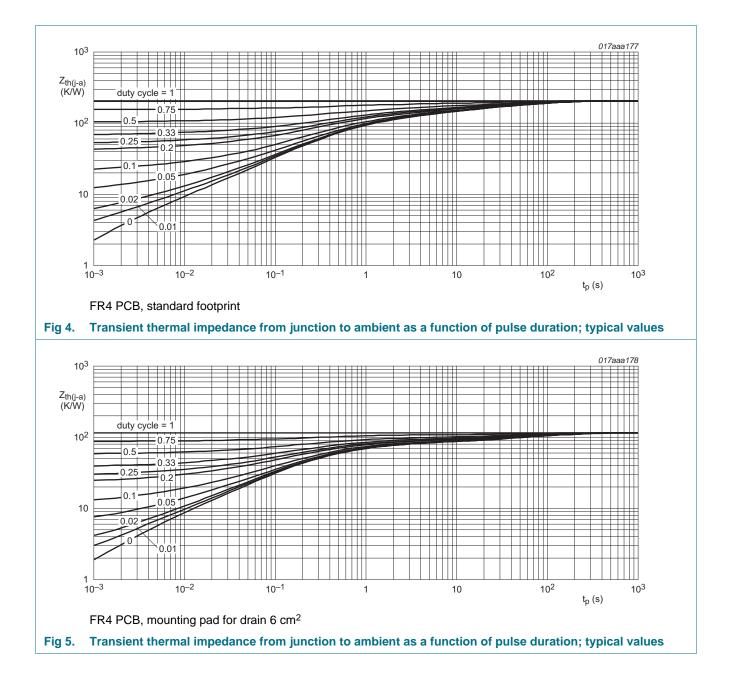
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

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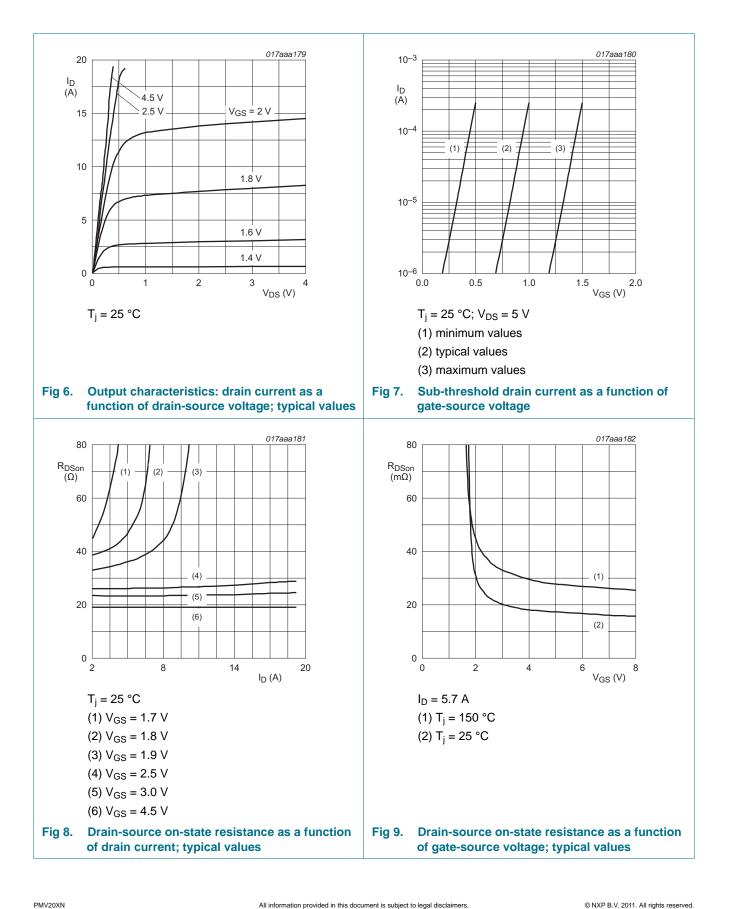


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7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = 25 \ ^{\circ}C$	30	-	-	V
V _{GSth}	gate-source threshold voltage	$I_D = 250 \ \mu A; \ V_{DS} = V_{GS}; \ T_j = 25 \ ^{\circ}C$	0.5	1	1.5	V
I _{DSS}	drain leakage current	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μΑ
		$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 150 \text{ °C}$	-	-	20	μΑ
I _{GSS}	gate leakage current	$V_{GS} = 12 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	100	nA
		V_{GS} = -12 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
R _{DSon}	drain-source on-state	V_{GS} = 4.5 V; I _D = 4.8 A; T _j = 25 °C	-	19	25	mΩ
resistance	resistance	V_{GS} = 4.5 V; I _D = 4.8 A; T _j = 150 °C	-	31	40	mΩ
		$V_{GS} = 2.5 \text{ V}; \text{ I}_{D} = 4 \text{ A}; \text{ T}_{j} = 25 \text{ °C}$	-	26	35	mΩ
9 _{fs}	forward transconductance	$V_{DS} = 5 \text{ V}; \text{ I}_{D} = 3 \text{ A}; \text{ T}_{j} = 25 \text{ °C}$	-	8	-	S
Dynamic	characteristics					
Q _{G(tot)}	total gate charge	$I_D = 3 \text{ A}; V_{DS} = 15 \text{ V}; V_{GS} = 4.5 \text{ V};$	-	6.4	10	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	1.8	-	nC
Q _{GD}	gate-drain charge		-	1.1	-	nC
C _{iss}	input capacitance	$V_{GS} = 0 V; V_{DS} = 15 V; f = 1 MHz;$	-	585	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	110	-	pF
C _{rss}	reverse transfer capacitance		-	55	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 15 \text{ V}; V_{GS} = 5.4 \text{ V}; \text{R}_{G(ext)} = 6 \Omega;$	-	12	-	ns
t _r	rise time	T _j = 25 °C; I _D = 4.8 A	-	27	-	ns
t _{d(off)}	turn-off delay time		-	128	-	ns
t _f	fall time		-	68	-	ns
Source-di	rain diode					
V _{SD}	source-drain voltage	I _S = 1 A; V _{GS} = 0 V; T _i = 25 °C	-	0.75	1.2	V

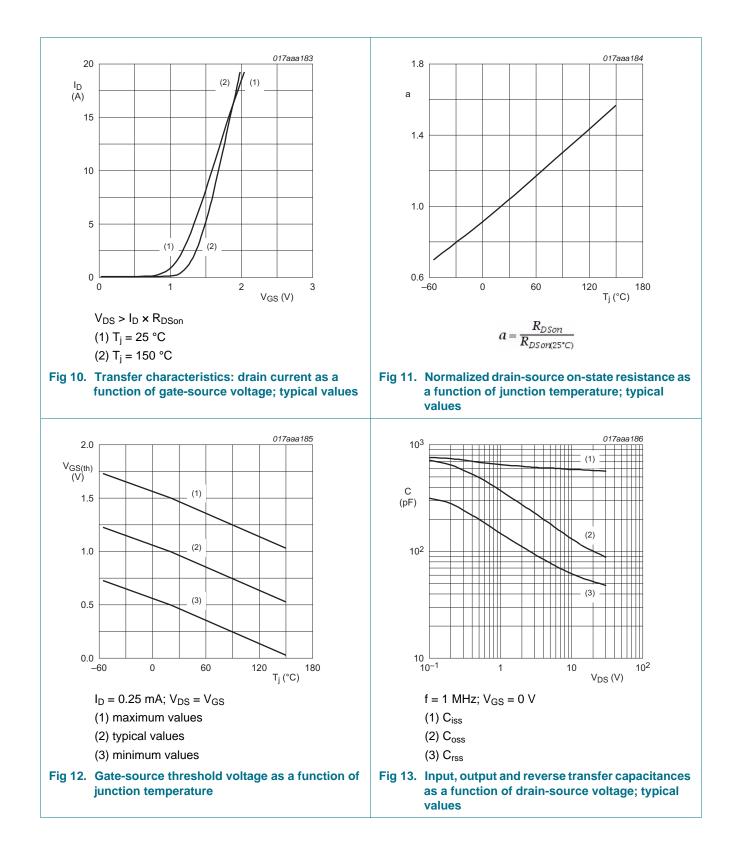
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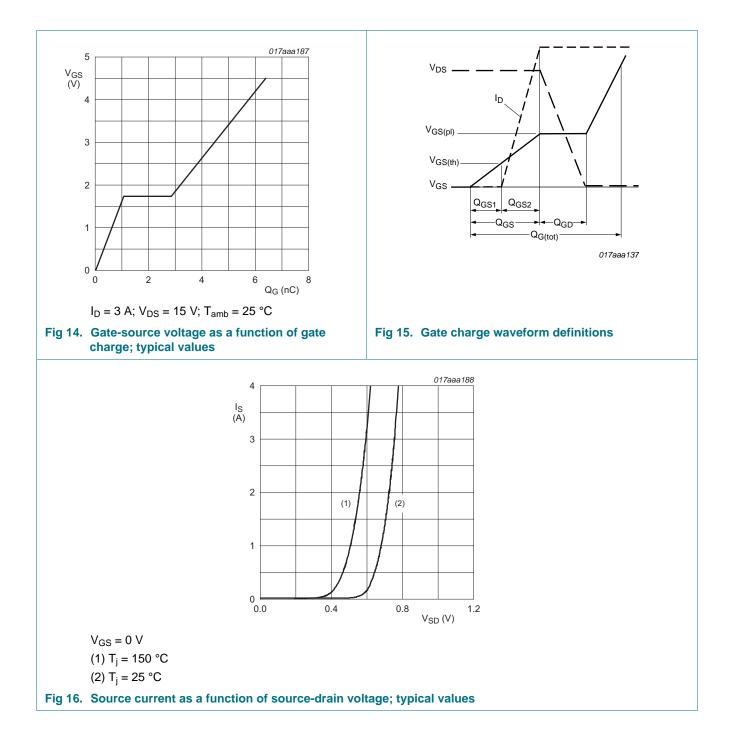
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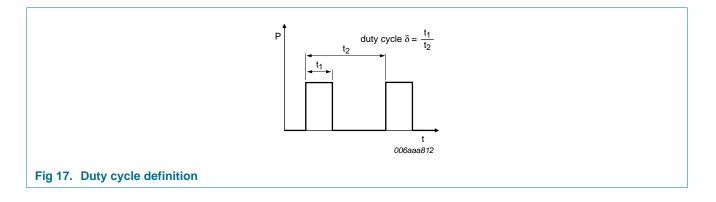
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8. Test information



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9. Package outline

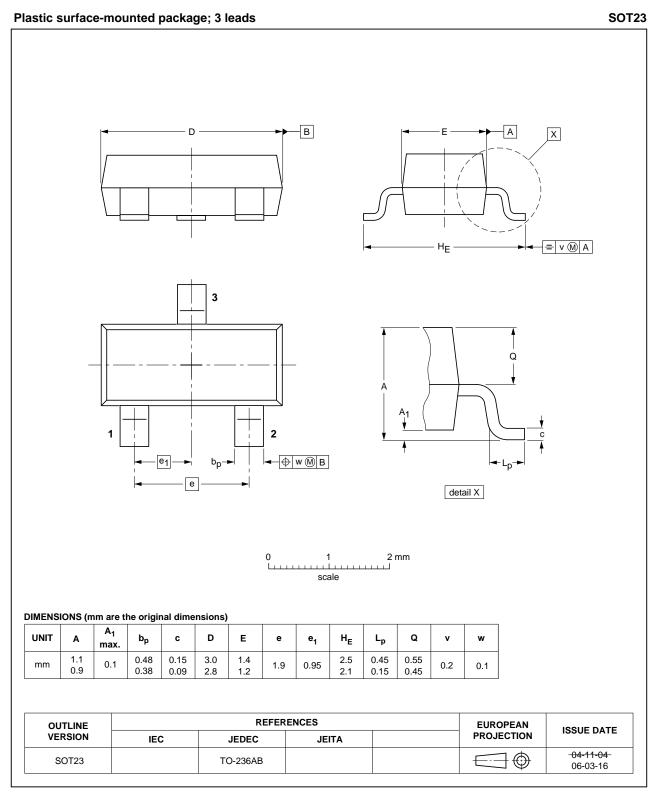
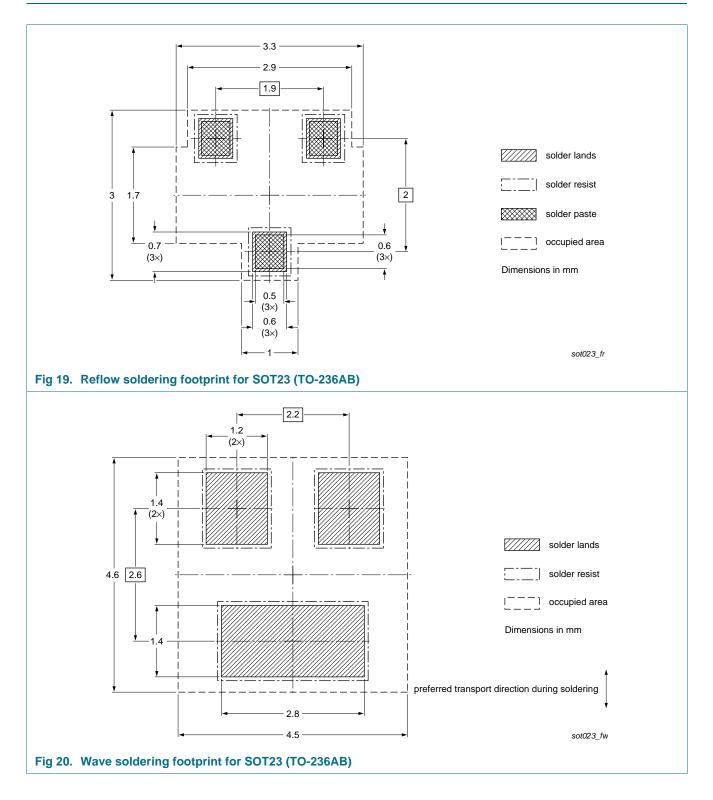


Fig 18. Package outline SOT23 (TO-236AB)

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10. Soldering



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11. Revision history

Table 8.	Revision history						
Document I	D	Release date	Data sheet status	Change notice	Supersedes		
PMV20XN v.	.1	20110405	Product data sheet	-	-		

12. Legal information

12.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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