

FEATURES

- HIGH RELIABILITY FOR LOW COST
- PECL OUTPUT SIGNAL
- COMPLEMENTARY OUTPUT OPTION
- FREQUENCY STABILITY TO ± 20 PPM
- JITTER OPTIMIZED DIP 14 PECL CLOCK OSCILLATOR
- EXCELLENT CLOCK SIGNAL GENERATOR FOR TELECOM AND TRANSMISSION SYSTEMS
- EXTENDED TEMPERATURE RANGE TO $-40/+85^{\circ}\text{C}$

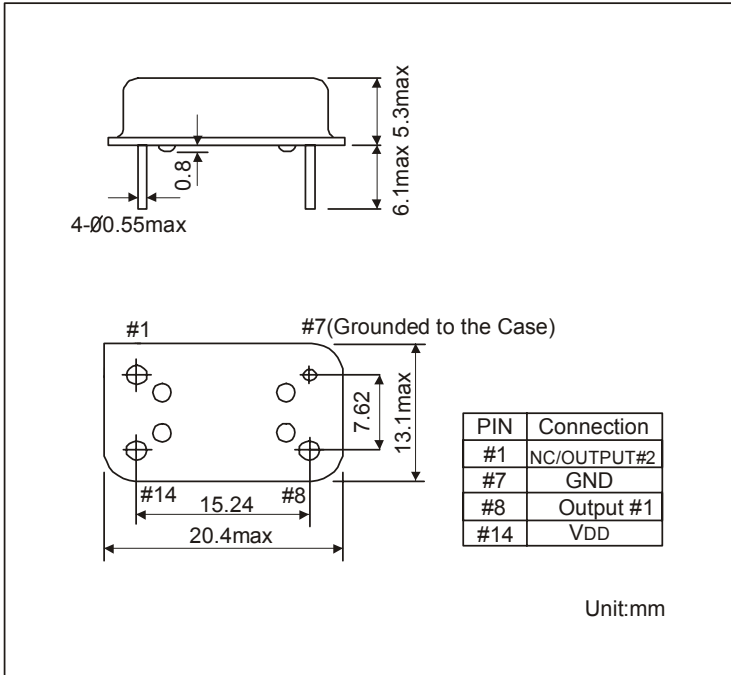
SERIES		M6502
PACKAGE		14 PIN DIP
FREQUENCY RANGE		96.0 ~ 160.0 MHz
FREQUENCY STABILITY		$\pm 20 \sim \pm 100$ ppm
AGING		+5 ppm after first year
OPERATING TEMPERATURE RANGE		$0/+70^{\circ}\text{C} \sim -40/+85^{\circ}\text{C}$
STORAGE TEMPERATURE RANGE		$-55/+125^{\circ}\text{C}$
INPUT		VOLTAGE +5.0 VDC $\pm 5\%$
		CURRENT 100 mA max.
OUTPUT	SYMMETRY	STANDARD 40/60%
		OPTION 45/55%
	RISE AND FALL TIME PECL 1.5 ns max. (20 ~ 80% PECL)	
	"0" LEVEL	PECL 5.0 VOLT VDD -1.62V max.
	"1" LEVEL	VDD -1.02V min.
LOAD	PECL	no load
PIN CONNECTION		SEE OUTLINE DRAWINGS
START-UP TIME		10 ms max.
PERIOD JITTER RMS		25 ps max.
OTHER PARAMETERS ARE AVAILABLE ON REQUEST / CREATE HERE YOUR SPECIFICATION		

PART NUMBERING SYSTEM

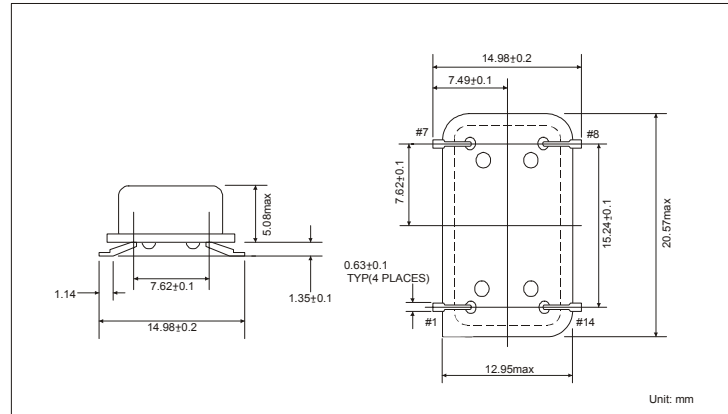
EXAMPLE	M6502-25-W-S-C-G-155.520MHz
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SERIES	M6502
FREQUENCY STABILITY	BLANK FOR 100 PPM ANY 50 ~ 15 PPM
TEMPERATURE RANGE	BLANK FOR $0/+70^{\circ}\text{C}$ N = $-10/+60^{\circ}\text{C}$ M = $-20/+70^{\circ}\text{C}$ W = $-40/+85^{\circ}\text{C}$
SYMMETRY	BLANK FOR 40/60% S FOR 45/55%
COMPLEMENTARY OUTPUT	BLANK PIN 1 NOT CONNECTED C FOR COMPLEMENTARY OUTPUT
PIN CONFIGURATION	BLANK FOR DIP G FOR GULL WING
FREQUENCY	REQUIRED FREQUENCY

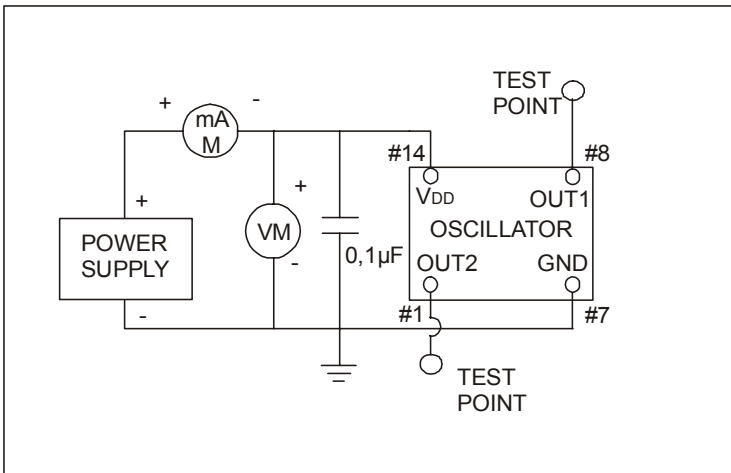
OUTLINE DRAWING OF M6502



OUTLINE DRAWING OF M6502 GULL WING



TEST CIRCUIT FOR PECL



PECL OUTPUT WAVE FORM

