

UNISONIC TECHNOLOGIES CO., LTD

15N60 **Preliminary Power MOSFET**

15A, 600V N-CHANNEL POWER MOSFET

DESCRIPTION

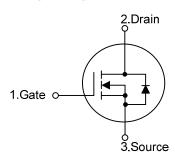
The UTC 15N60 is an N-channel mode power MOSFET using UTC's advanced technology to provide costumers with planar stripe and DMOS technology. This technology is specialized in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC 15N60 is universally applied in active power factor correction and high efficient switched mode power supplies.



- * $R_{DS(ON)}$ =0.65 Ω @ V_{GS} =10V
- * Typically 23.6pF low C_{RSS}
- * High switching speed
- * Improved dv/dt capability

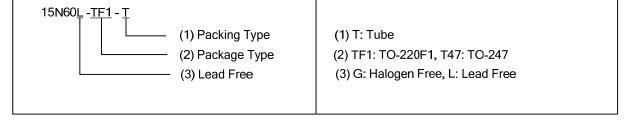
SYMBOL

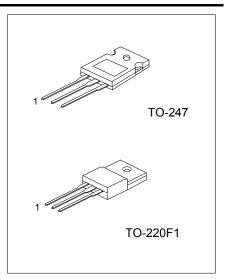


ORDERING INFORMATION

Ordering Number		Deekees	Pin Assignment			Dankina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
15N60L-TF1-T	15N60G-TF1-T	TO-220F1	G	D	S	Tube	
15N60L-T47-T	15N60G-T47-T	TO-247	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source





■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT		
Drain to Source Voltage		V_{DSS}	600	V		
Gate to Source Voltage		V_{GSS}	±30	V		
Avalanche Current (Note 2)		I _{AR}	15	Α		
Continuous Drain Current		Continuous	I _D	15	Α	
		Pulsed (Note 2)	I _{DM}	60	Α	
IAValanche Energy	Single	Pulsed (Note 3)	E _{AS}	637	mJ	
	Repet	titive (Note 2)	E_{AR}	25.0	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns		
Power Dissipation		TO-220F1	D	38.5	10/	
		TO-247	P_{D}	312	W	
Junction Temperature	nction Temperature		TJ	+150	°C	
Storage Temperature		T _{STG}	-55 ~ +150	°C		

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L=5.23mH, I_{AS} =15A, V_{DD} = 50V, R_{G} =25 Ω , Starting T_{J} =25 $^{\circ}$ C
- 4. $I_{SD} \le 15A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220F1	0	62.5	°C/W	
	TO-247	θ_{JA}	40		
Junction to Case	TO-220F1	0	3.3	°C/W	
	TO-247	$ heta_{ extsf{JC}}$	0.4		

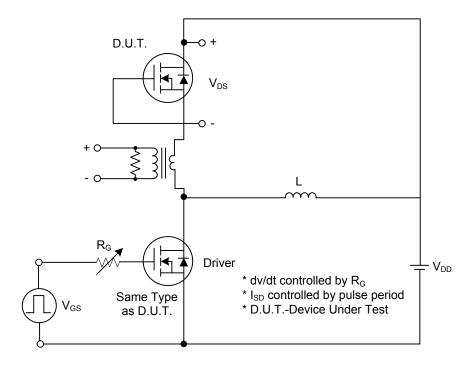
■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS		1		ı	ı	Į.	
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA, T _J =25°C	600			V
Breakdown Voltage Temperature Coefficient			I _D =250μA,Referenced to 25°C		0.65		V/°C
Drain-Source Leakage Current		l lnee	V _{DS} =600V, V _{GS} =0V			1	μΑ
			V _{DS} =520V, T _C =125°C			10	μΑ
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse		V_{GS} =-30V, V_{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$	2.0		4.0	V
Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =7.5A		0.5	0.65	Ω
DYNAMIC PARAMETERS		_		-	-	a	
Input Capacitance		C _{ISS}			2380	3095	pF
Output Capacitance		Coss	V_{DS} =25V, V_{GS} =0V,f=1.0MHz		295	385	pF
Reverse Transfer Capacitance		C _{RSS}			23.6	35.5	pF
SWITCHING PARAMETERS		_		-	-	a	
Total Gate Charge		Q_{G}	\\ _520\\ \\ _40\\		48.5	63.0	nC
Gate-Source Charge		Q_GS	V _{DS} =520V, V _{GS} =10V, I _D =15A (Note 1, 2)		14.0		nC
Gate-Drain Charge		Q_{GD}	ID-TSA (Note 1, 2)		21.2		nC
Turn-ON Delay Time		t _{D(ON)}			65	140	ns
Turn-ON Rise Time		t _R	V_{DD} =325V, I_{D} =15A,		125	260	ns
Turn-OFF Delay Time		t _{D(OFF)}	R _G =21.7Ω (Note 1, 2)		105	220	ns
Turn-OFF Fall Time		t _F			65	140	ns
SOURCE- DRAIN DIODE RATII	NGS AND CH	ARACTERIS	псѕ				
Maximum Body-Diode Continuous Current		I _S				15	Α
Maximum Body-Diode Pulsed Current		I _{SM}				60	Α
Drain-Source Diode Forward Voltage		V_{SD}	I _S =15A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time		t _{rr}	V _{GS} =0V, I _S =15A,		496		ns
Body Diode Reverse Recovery Charge		Q_{RR}	dI _F /dt=100A/μs (Note 1)		5.69	-	μC

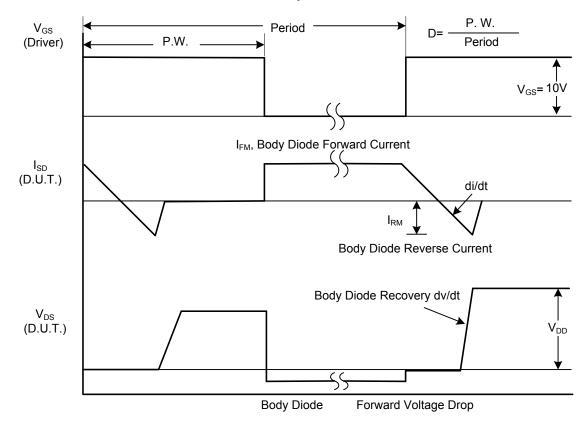
Notes: 1. Pulse Test : Pulse width ≤ 300µs, Duty cycle ≤ 2%

- 2. Essentially independent of operating temperature
- 3. Drain current limited by maximum junction temperature

■ TEST CIRCUITS AND WAVEFORMS

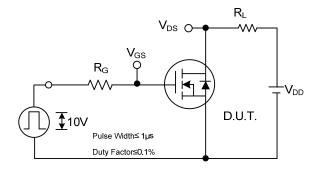


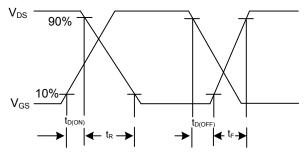
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

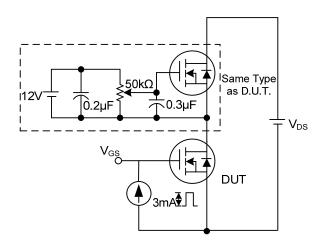
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

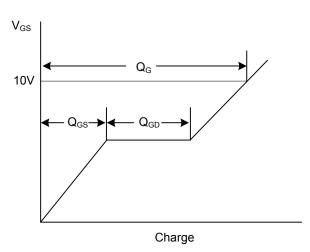




Switching Test Circuit

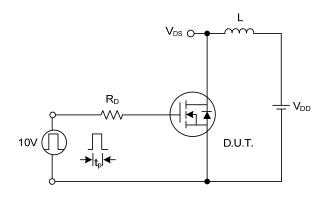
Switching Waveforms

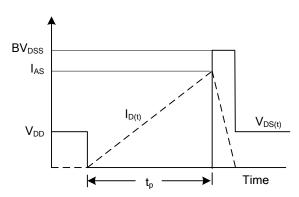




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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