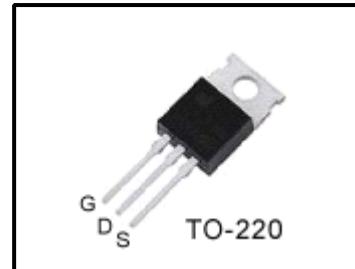


### ***High Voltage Fast-Switching NPN Power Transistor***

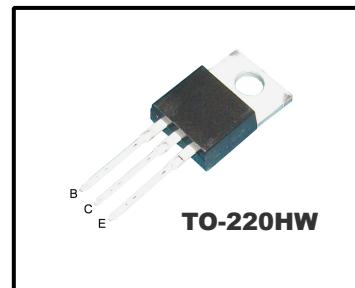
#### **Features**

- Very High Switching Speed
- High Voltage Capability
- Wide Reverse Bias SOA



#### **General Description**

This Device is designed for high voltage, High speed switching characteristics required such as lighting system, switching mode power supply.



#### **Absolute Maximum Ratings**

Symbol	Parameter	Test Conditions	Value	Units
$V_{CES}$	Collector-Emitter Voltage	$V_{BE} = 0$	700	V
$V_{CEO}$	Collector-Emitter Voltage	$I_B = 0$	400	V
$V_{EBO}$	Emitter-Base Voltage	$I_C = 0$	9.0	V
$I_C$	Collector Current		4.0	A
$I_{CP}$	Collector pulse Current		8.0	A
$I_B$	Base Current		2.0	A
$I_{BM}$	Base Peak Current	$t_P = 5\text{ms}$	4.0	A
$P_C$	Total Dissipation at $T_c^* = 25^\circ\text{C}$		75	W
	Total Dissipation at $T_a^* = 25^\circ\text{C}$		2.0	
$T_J$	Operation Junction Temperature		- 40 ~ 150	°C
$T_{STG}$	Storage Temperature		- 40 ~ 150	°C

Tc: Case temperature (good cooling)

Ta: Ambient temperature (without heat sink)

#### **Thermal Characteristics**

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance Junction to Case	1.67	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	62.5	°C/W

#### **Ordering Information**

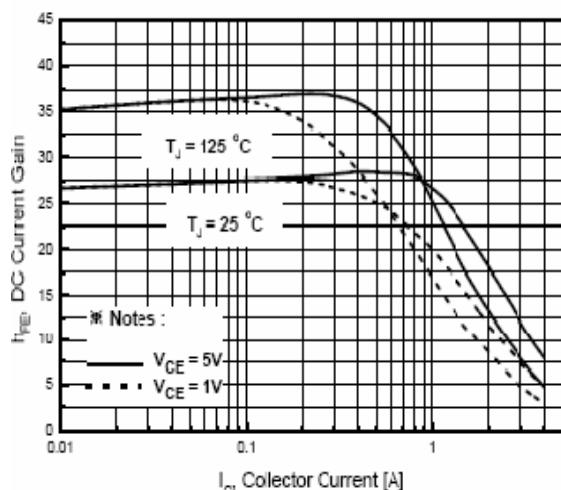
Order codes	Package	Marking	Halogen Free	Packaging
SBP13005-O	TO220C	P13005-O	NO	Tube
SBP13005-O-HW	TO220HW	P13005-O	NO	Tube

**Electrical Characteristics ( $T_c=25^\circ\text{C}$  unless otherwise noted)**

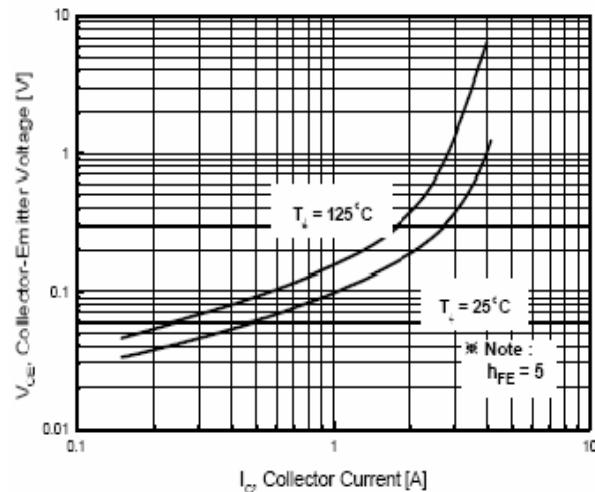
Symbol	Parameter	Test Conditions	Value			Units
			Min	Typ	Max	
$V_{CEO(\text{sus})}$	Collector-Emitter Breakdown Voltage	$I_C=10\text{mA}, I_B=0$	400	-	-	V
$V_{CE(\text{sat})}$	Collector-Emitter Saturation Voltage	$I_C=1.0\text{A}, I_B=0.2\text{A}$ $I_C=2.0\text{A}, I_B=0.5\text{A}$ $I_C=4\text{A}, I_B=1.0\text{A}$	-	-	0.3 0.5 1.0	V
$V_{BE(\text{sat})}$	Base-Emitter Saturation Voltage	$I_C=1.0\text{A}, I_B=0.2\text{A}$ $I_C=2.0\text{A}, I_B=0.5\text{A}$	-	-	1.2 1.6	V
$I_{CBO}$	Collector-Base Cutoff Current ( $V_{be}=-1.5\text{V}$ )	$V_{cb}=700\text{V}$ $V_{cb}=700\text{V}, T_c=100^\circ\text{C}$	-	-	1.0 5.0	mA
$h_{FE}$	DC Current Gain	$V_{ce}=2\text{V}, I_C=1.0\text{A}$ $V_{ce}=2\text{V}, I_C=2.0\text{A}$	10 8	-	40 40	
ts tf	<b>Resistive Load</b> Storage Time Fall Time	$V_{CC}=125\text{V}, I_C=2.0\text{A}$ $I_{B1}=0.4\text{A}, I_{B2}=-1.0\text{A}$ $T_p=25\mu\text{s}$	-	2.5 0.15	4.0 0.4	$\mu\text{s}$
ts tf	<b>Inductive Load</b> Storage Time Fall Time	$V_{CC}=15\text{V}, I_C=1\text{A}$ $I_{B1}=0.4\text{A}, I_{B2}=-0.1\text{A}$ $L=0.35\text{mH}, V_{clamp}=300\text{V}$	- -	1.2 0.12	4.0 0.3	$\mu\text{s}$
ts tf	<b>Inductive Load</b> Storage Time Fall Time	$V_{CC}=15\text{V}, I_C=1\text{A}$ $I_{B1}=0.4\text{A}, I_{B2}=-1.0\text{A}$ $L=0.35\text{mH}, V_{clamp}=300\text{V}$ $T_c=100^\circ\text{C}$	- -	1.2 0.08	3.0 0.4	$\mu\text{s}$

**Note:**

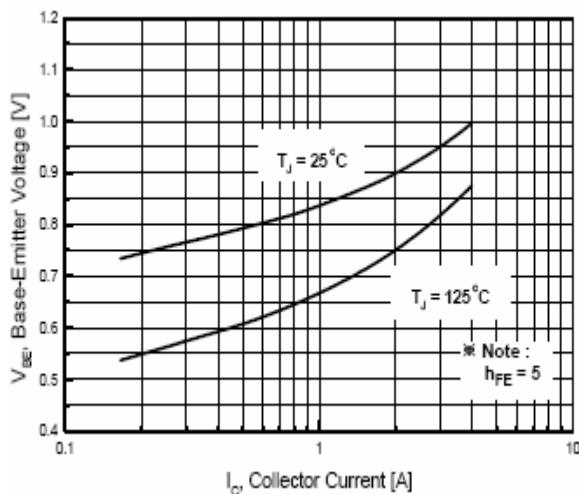
Pulse Test : Pulse width 300, Duty cycle 2%



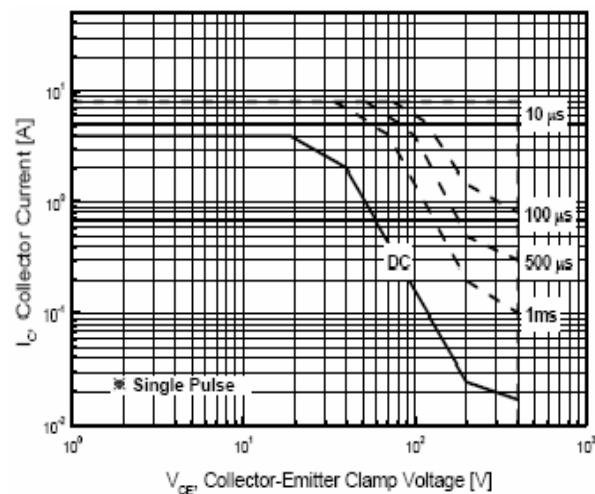
**Fig. 1 DC Current Gain**



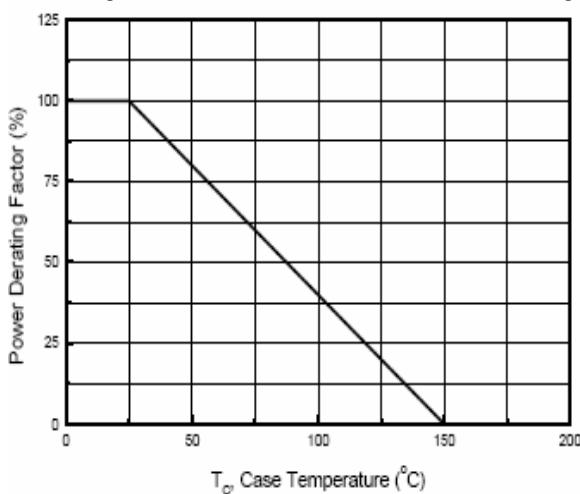
**Fig. 2 Collector-Emitter Saturation Voltage**



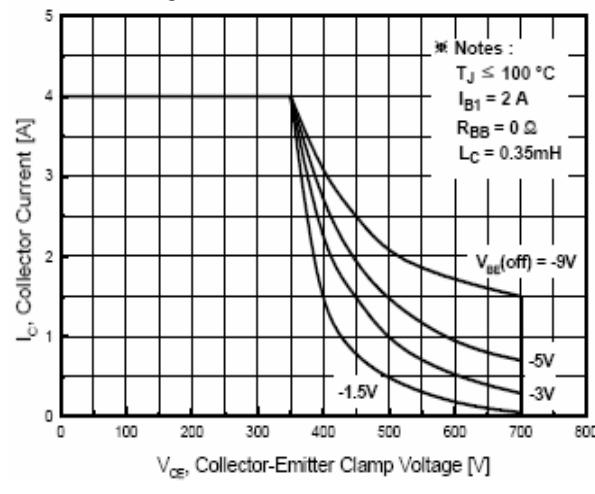
**Fig. 3 Base-Emitter Saturation Voltage**



**Fig. 4 Safe Operation Area**

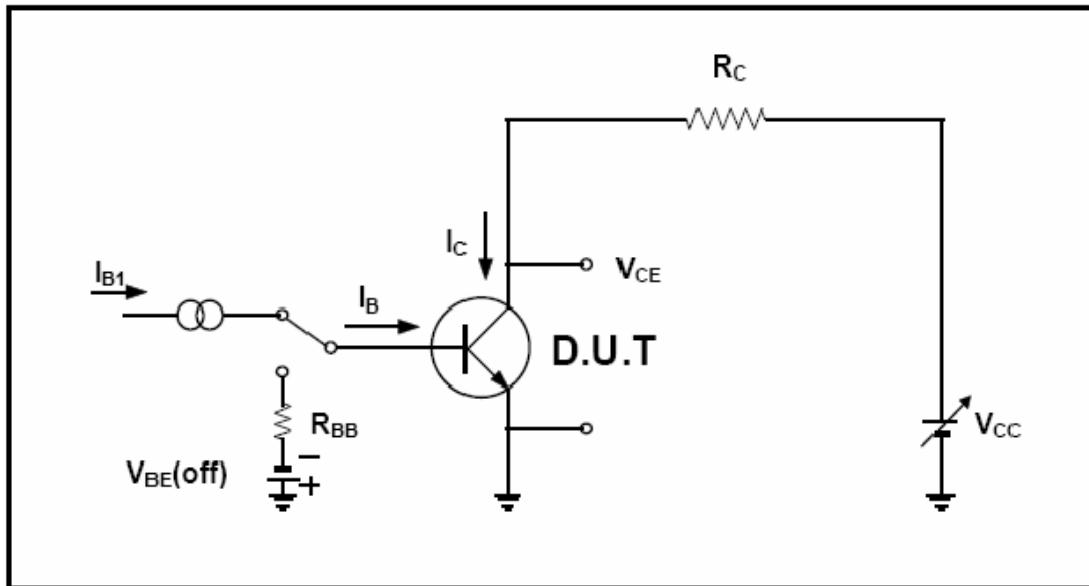


**Fig. 5 Power Derating**

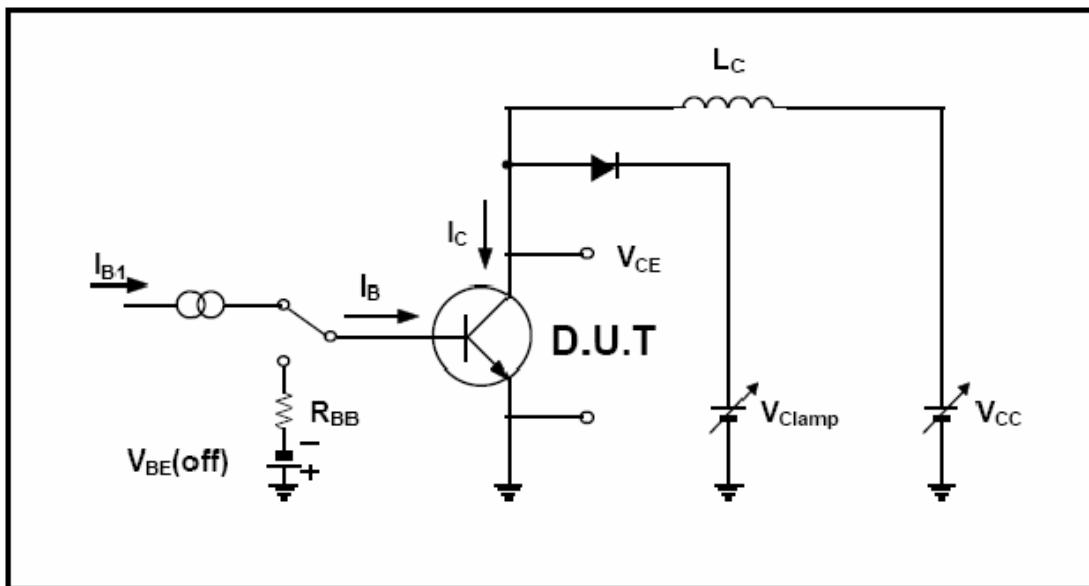


**Fig. 6 Reverse Biased Safe Operation Area**

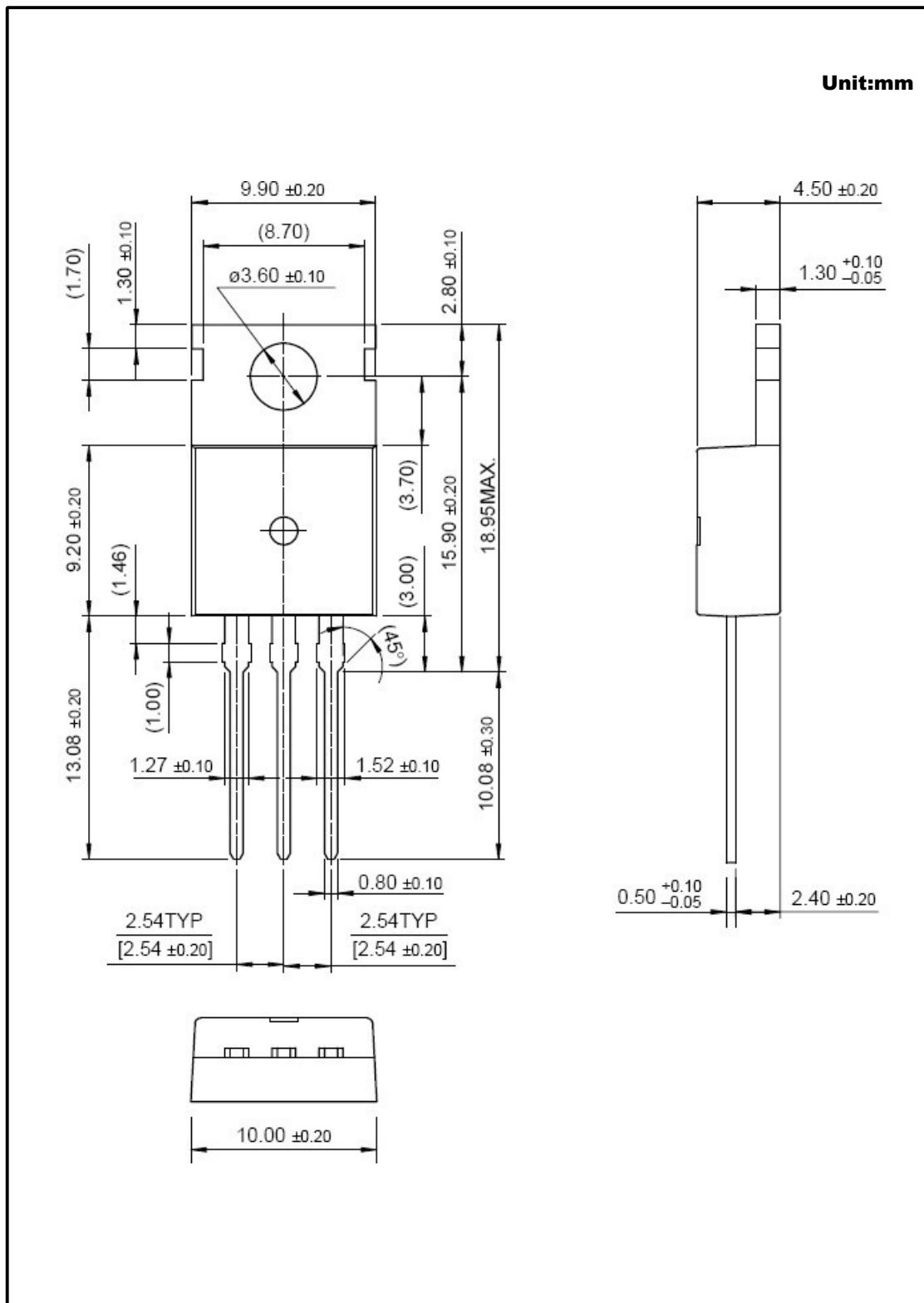
### Resistive Load Switching Test Circuit



### Inductive Load Switching & RBSOA Test Circuit



**TO-220C Package Dimension**



**TO-220HW Package Dimension**

