

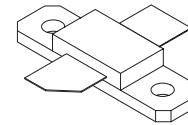
**The RF MOSFET Line**  
**RF Power Field Effect Transistors**  
**N-Channel Enhancement-Mode Lateral MOSFETs**

**MRF21010R1**  
**MRF21010SR1**

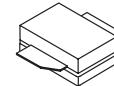
Designed for W-CDMA base station applications with frequencies from 2110 to 2170 MHz. Suitable for FM, TDMA, CDMA and multicarrier amplifier applications. To be used in Class AB for PCN-PCS/cellular radio and WLL applications.

- Typical W-CDMA Performance: -45 dBc ACPR, 2140 MHz, 28 Volts, 5 MHz Offset/4.096 MHz BW, 15 DTCH
  - Output Power — 2.1 Watts
  - Power Gain — 13.5 dB
  - Efficiency — 21%
- High Gain, High Efficiency and High Linearity
- Integrated ESD Protection
- Designed for Maximum Gain and Insertion Phase Flatness
- Capable of Handling 10:1 VSWR @ 28 Vdc, 2170 MHz, 10 Watts CW Output Power
- Excellent Thermal Stability
- Characterized with Series Equivalent Large-Signal Impedance Parameters
- In Tape and Reel. R1 Suffix = 500 Units per 32 mm, 13 Inch Reel.

2170 MHz, 10 W, 28 V  
LATERAL N-CHANNEL  
BROADBAND  
RF POWER MOSFETs



CASE 360B-05, STYLE 1  
NI-360  
MRF21010R1



CASE 360C-05, STYLE 1  
NI-360S  
MRF21010SR1

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	65	Vdc
Gate-Source Voltage	V <sub>GS</sub>	-0.5, +15	Vdc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	43.75 0.25	W W/°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C
Operating Junction Temperature	T <sub>J</sub>	200	°C

#### ESD PROTECTION CHARACTERISTICS

Test Conditions	Class
Human Body Model	1 (Minimum)
Machine Model	M1 (Minimum)

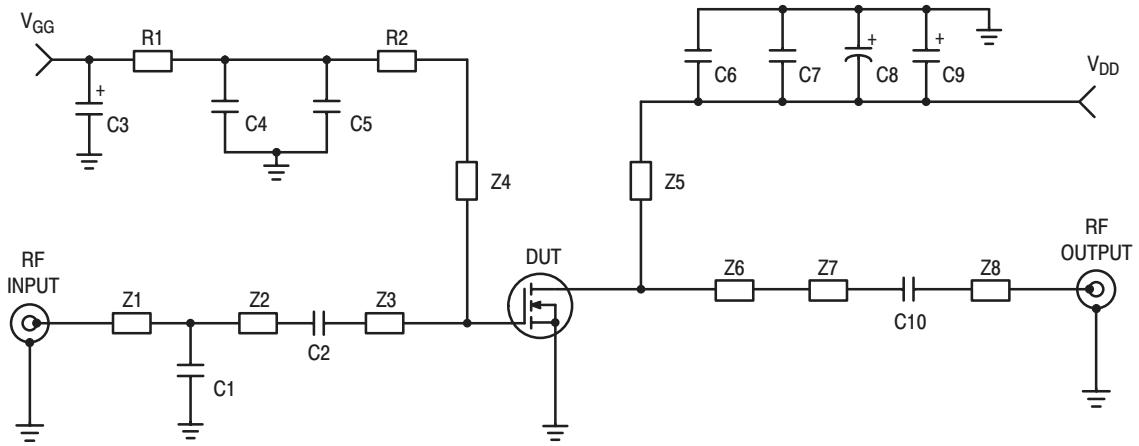
#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	5.5	°C/W

NOTE – **CAUTION** – MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.

**ELECTRICAL CHARACTERISTICS** ( $T_C = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Drain–Source Breakdown Voltage ( $V_{GS} = 0 \text{ Vdc}$ , $I_D = 10 \mu\text{A}$ )	$V_{(BR)DSS}$	65	—	—	Vdc
Zero Gate Voltage Drain Current ( $V_{DS} = 28 \text{ Vdc}$ , $V_{GS} = 0 \text{ Vdc}$ )	$I_{DSS}$	—	—	10	$\mu\text{A}_{dc}$
Gate–Source Leakage Current ( $V_{GS} = 5 \text{ Vdc}$ , $V_{DS} = 0 \text{ Vdc}$ )	$I_{GSS}$	—	—	1	$\mu\text{A}_{dc}$
<b>ON CHARACTERISTICS</b>					
Gate Threshold Voltage ( $V_{DS} = 10 \text{ V}$ , $I_D = 50 \mu\text{A}$ )	$V_{GS(th)}$	2.5	3	4	Vdc
Gate Quiescent Voltage ( $V_{DS} = 28 \text{ V}$ , $I_D = 100 \text{ mA}$ )	$V_{GS(Q)}$	2.5	4	4.5	Vdc
Drain–Source On–Voltage ( $V_{GS} = 10 \text{ V}$ , $I_D = 0.5 \text{ A}$ )	$V_{DS(on)}$	—	0.4	0.5	Vdc
Forward Transconductance ( $V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ A}$ )	$g_{fs}$	—	0.95	—	S
<b>DYNAMIC CHARACTERISTICS</b>					
Reverse Transfer Capacitance ( $V_{DS} = 28 \text{ Vdc}$ , $V_{GS} = 0$ , $f = 1 \text{ MHz}$ )	$C_{rss}$	—	1	—	pF
<b>FUNCTIONAL TESTS</b> (In Motorola Test Fixture, 50 ohm system)					
Two-Tone Common Source Amplifier Power Gain ( $V_{DD} = 28 \text{ Vdc}$ , $P_{out} = 10 \text{ W PEP}$ , $I_{DQ} = 100 \text{ mA}$ , $f_1 = 2110 \text{ MHz}$ , $f_2 = 2170 \text{ MHz}$ , Tone Spacing = 100 KHz)	$G_{ps}$	12	13.5	—	dB
Two-Tone Drain Efficiency ( $V_{DD} = 28 \text{ Vdc}$ , $P_{out} = 10 \text{ W PEP}$ , $I_{DQ} = 100 \text{ mA}$ , $f_1 = 2110 \text{ MHz}$ , $f_2 = 2170 \text{ MHz}$ , Tone Spacing = 100 KHz)	$\eta$	31	35	—	%
Third Order Intermodulation Distortion ( $V_{DD} = 28 \text{ Vdc}$ , $P_{out} = 10 \text{ W PEP}$ , $I_{DQ} = 100 \text{ mA}$ , $f_1 = 2110 \text{ MHz}$ , $f_2 = 2170 \text{ MHz}$ , Tone Spacing = 100 KHz)	IMD	—	-35	-30	dBc
Input Return Loss ( $V_{DD} = 28 \text{ Vdc}$ , $P_{out} = 10 \text{ W PEP}$ , $I_{DQ} = 100 \text{ mA}$ , $f_1 = 2110 \text{ MHz}$ , $f_2 = 2170 \text{ MHz}$ , Tone Spacing = 100 KHz)	IRL	—	-12	-10	dB
Output Power, 1 dB Compression Point, CW ( $V_{DD} = 28 \text{ Vdc}$ , $I_{DQ} = 100 \text{ mA}$ , $f = 2170 \text{ MHz}$ )	$P_{1dB}$	—	11	—	W
Common–Source Amplifier Power Gain ( $V_{DD} = 28 \text{ Vdc}$ , $P_{out} = 10 \text{ W CW}$ , $I_{DQ} = 100 \text{ mA}$ , $f = 2170 \text{ MHz}$ )	$G_{ps}$	—	12	—	dB
Drain Efficiency ( $V_{DD} = 28 \text{ Vdc}$ , $P_{out} = 10 \text{ W CW}$ , $I_{DQ} = 100 \text{ mA}$ , $f = 2170 \text{ MHz}$ )	$\eta$	—	42	—	%
Output Mismatch Stress ( $V_{DD} = 28 \text{ Vdc}$ , $P_{out} = 10 \text{ W CW}$ , $I_{DQ} = 100 \text{ mA}$ , $f = 2170 \text{ MHz}$ , VSWR = 10:1, All Phase Angles at Frequency of Tests)	$\Psi$	No Degradation In Output Power Before and After Test			



Z1	0.964" x 0.087" Microstrip	Z5	0.752" x 0.087" Microstrip
Z2	0.905" x 0.087" Microstrip	Z6	0.453" x 1.118" Microstrip
Z3	0.433" x 0.512" Microstrip	Z7	0.921" x 0.154" Microstrip
Z4	1.068" x 0.087" Microstrip	Z8	0.925" x 0.087" Microstrip

Figure 1. MRF21010 Test Circuit Schematic

Table 1. MRF21010 Test Circuit Component Designations and Values

Part	Description	Value, P/N or DWG	Manufacturer
C1 * (eared)	2.2 pF Chip Capacitor, B Case	100B2R2BW	ATC
(earless)	1.8 pF Chip Capacitor, B Case	100B1R8BW	ATC
C2	0.5 pF Chip Capacitor, B Case	100B0R5BW	ATC
C3, C9	10 µF, 35 V Tantalum Chip Capacitors	293D106X9035D2T	Sprague-Vishay
C4, C7	1 nF Chip Capacitors, B Case	100B102JW	ATC
C5, C6	5.6 pF Chip Capacitors, B Case	100B5R6BW	ATC
C8	470 µF, 63 V Electrolytic Capacitor		
C10	10 pF Chip Capacitor, B Case	100B100GW	ATC
N1, N2	Type N Connector Flange Mounts	3052-1648-10	Macom
R1	1.0 kΩ Chip Resistor (0805)		
R2	12 Ω Chip Resistor (0805)		
PCB	Etched Circuit Board	C-XM-00-001-01	Cibel
Raw PCB material	0.030" Glass Teflon® ( $\epsilon_r = 2.55$ )	TLX8-0300	Taconic

\* Piece part depending on eared / earless version of the device.

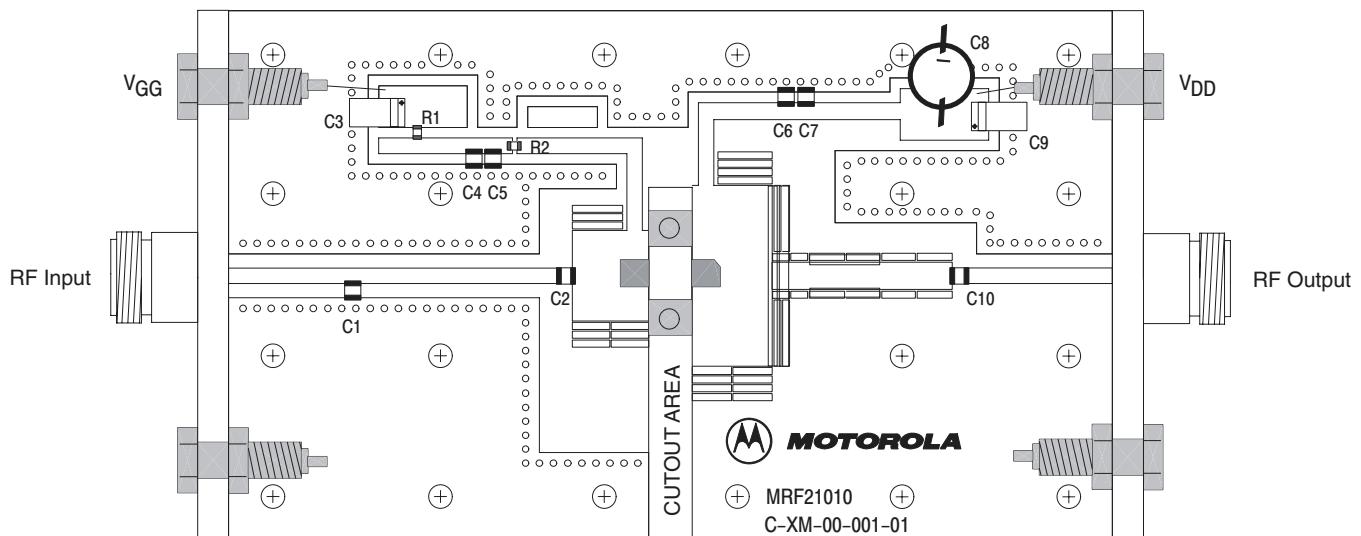
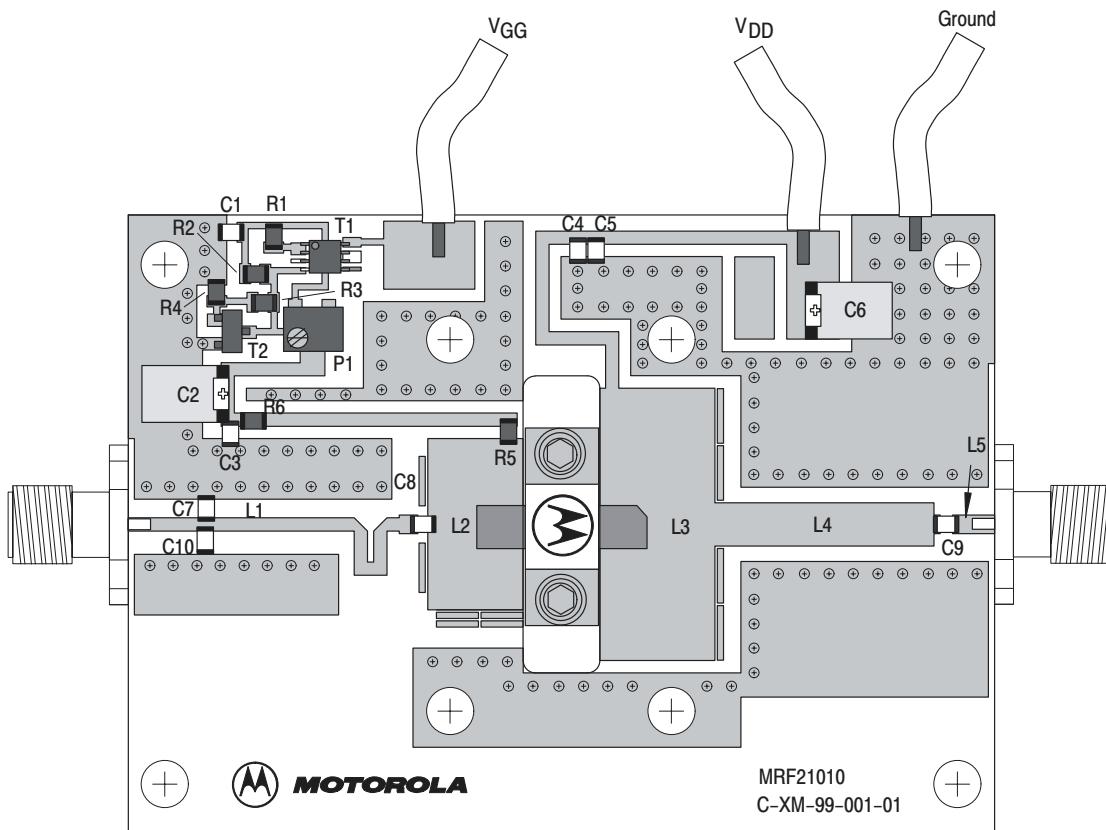


Figure 2. MRF21010 Test Circuit Component Layout

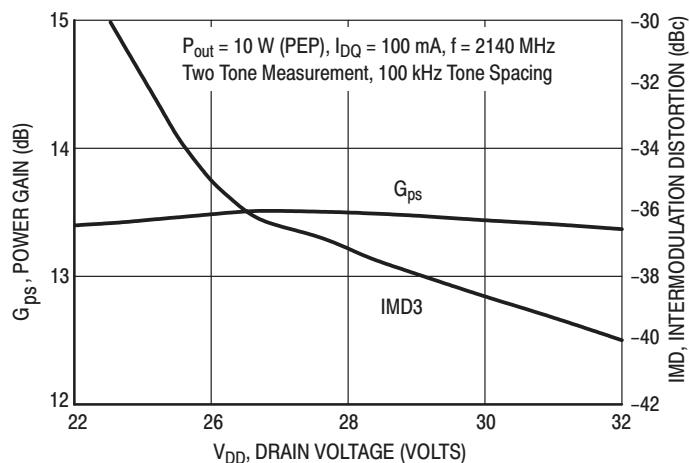
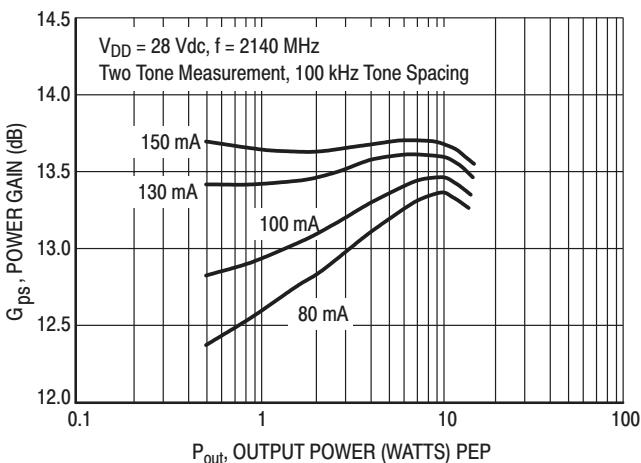
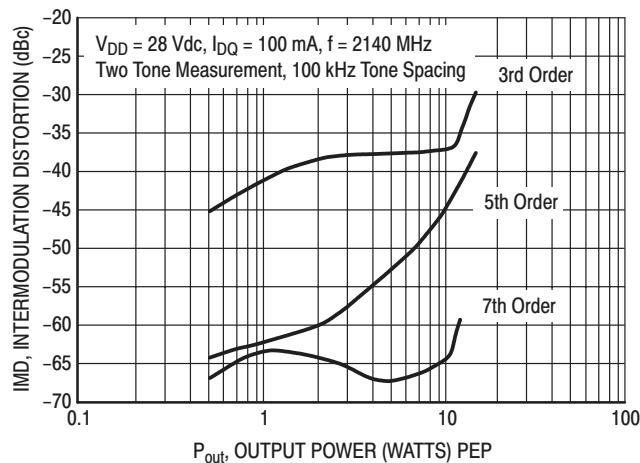
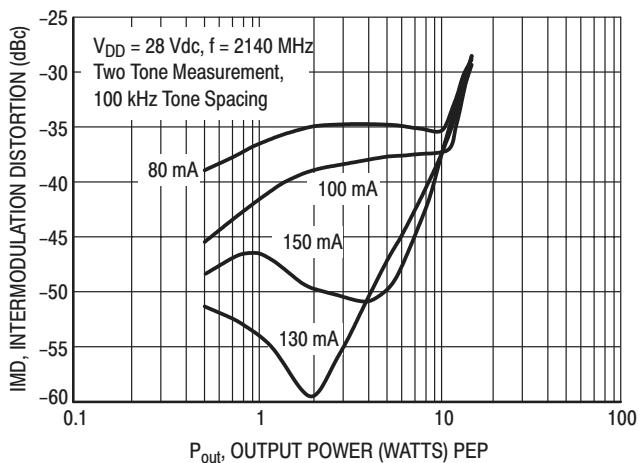
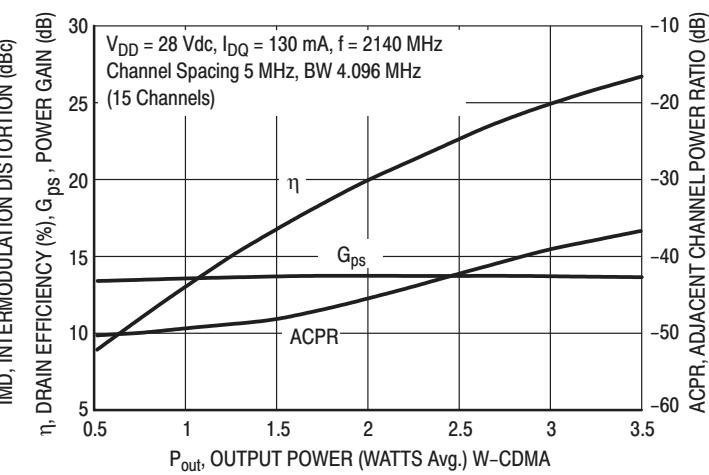
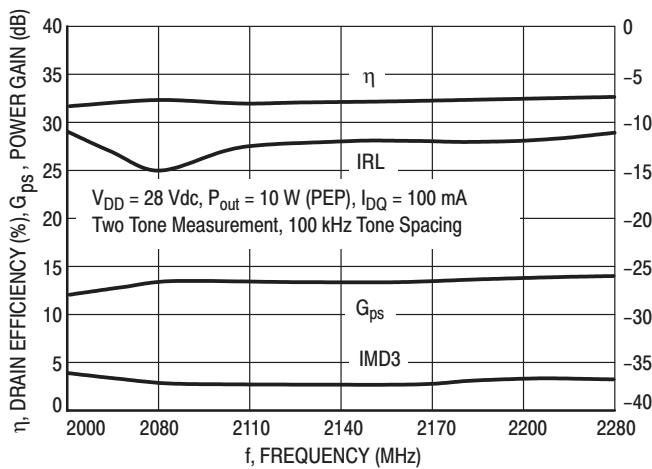


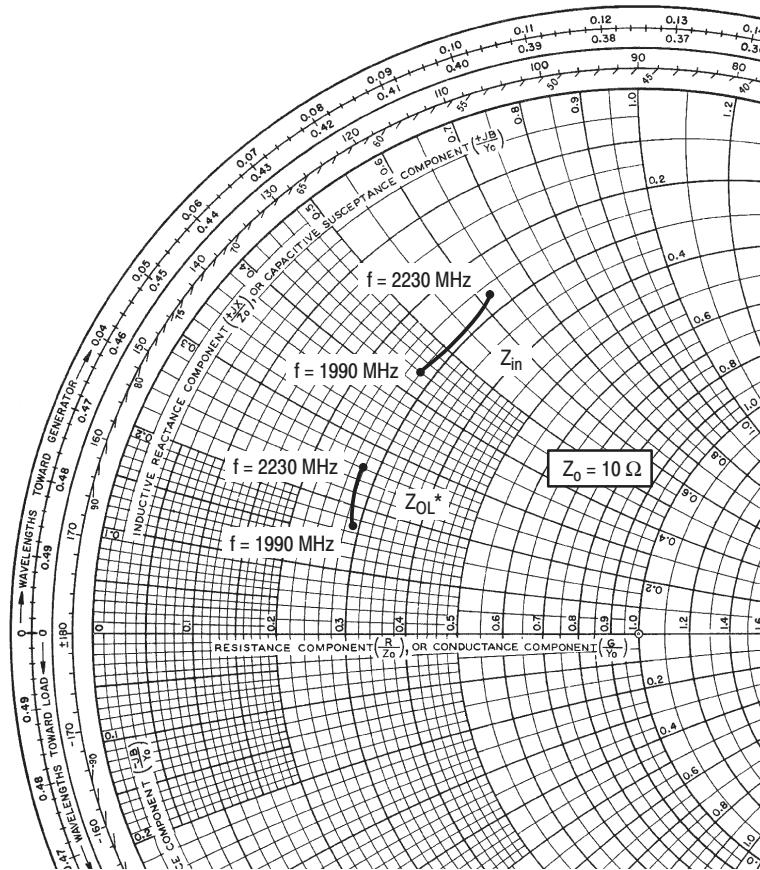
**Figure 3. MRF21010 Demonstration Board Component Layout**

**Table 2. MRF21010 Demonstration Board Component Designations and Values**

Designators	Description
C1	1 $\mu$ F Chip Capacitor (0805), AVX #08053G105ZATEA
C2, C6	10 $\mu$ F, 35 V Tantalum Capacitors, Vishay–Sprague #293D106X9035D
C3, C4	6.8 pF Chip Capacitors, ACCU–P (0805), AVX #08051J6R8CBT
C5	10 nF Chip Capacitor (0805), AVX #08055C103KATDA
C7	1.5 pF Chip Capacitor, ACCU–P (0805), AVX #08051J2R2BBT
C8, C10	0.5 pF Chip Capacitors, ACCU–P (0805), AVX #08051J0R5BBT
C9	10 pF Chip Capacitor, ACCU–P (0805), AVX #08055J100GBT
L1	19 mm $\times$ 1.07 mm
L2	7.7 mm $\times$ 13.8 mm
L3	9.3 mm $\times$ 22 mm
L4	17.7 mm $\times$ 3.5 mm
L5	3.4 mm $\times$ 1.5 mm
R1, R6	10 $\Omega$ , 1/8 W Chip Resistors (0805)
R2, R3	1 k $\Omega$ , 1/8 W Chip Resistors (0805)
R4	2.2 k $\Omega$ , 1/8 W Chip Resistor (0805)
R5	0 $\Omega$ , 1/8 W Chip Resistor (0805)
P1	5 k $\Omega$ Potentiometer CMS Cermet Multi–Turn, Bourns #3224W
T1	Voltage Regulator, Micro–8, Motorola #LP2951
T2	Bipolar NPN Transistor, SOT–23, Motorola #BC847
	RF Connectors Type SMA, Johnson #142–0701–631
	Substrate: Rogers RO4350, Thickness 0.5 mm, $\epsilon_r = 3.53$

## TYPICAL CHARACTERISTICS





$V_{DD} = 28 \text{ V}, I_{DQ} = 100 \text{ mA}, P_{out} = P_{1dB\ CW}$

$f$ MHz	$Z_{in}$ $\Omega$	$Z_{OL^*}$ $\Omega$
1990	$2.85 + j4.38$	$2.93 + j1.71$
2110	$2.89 + j5.04$	$2.76 + j2.28$
2230	$2.73 + j6.19$	$2.83 + j2.59$

$Z_{in}$  = Complex conjugate of source impedance.

$Z_{OL^*}$  = Complex conjugate of the optimum load impedance at a given output power, voltage, IMD, bias current and frequency.

Note:  $Z_{OL^*}$  was chosen based on tradeoffs between gain, output power, drain efficiency and intermodulation distortion.

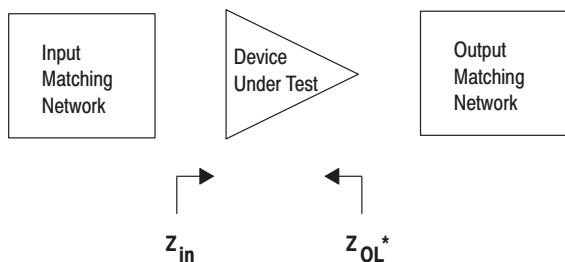
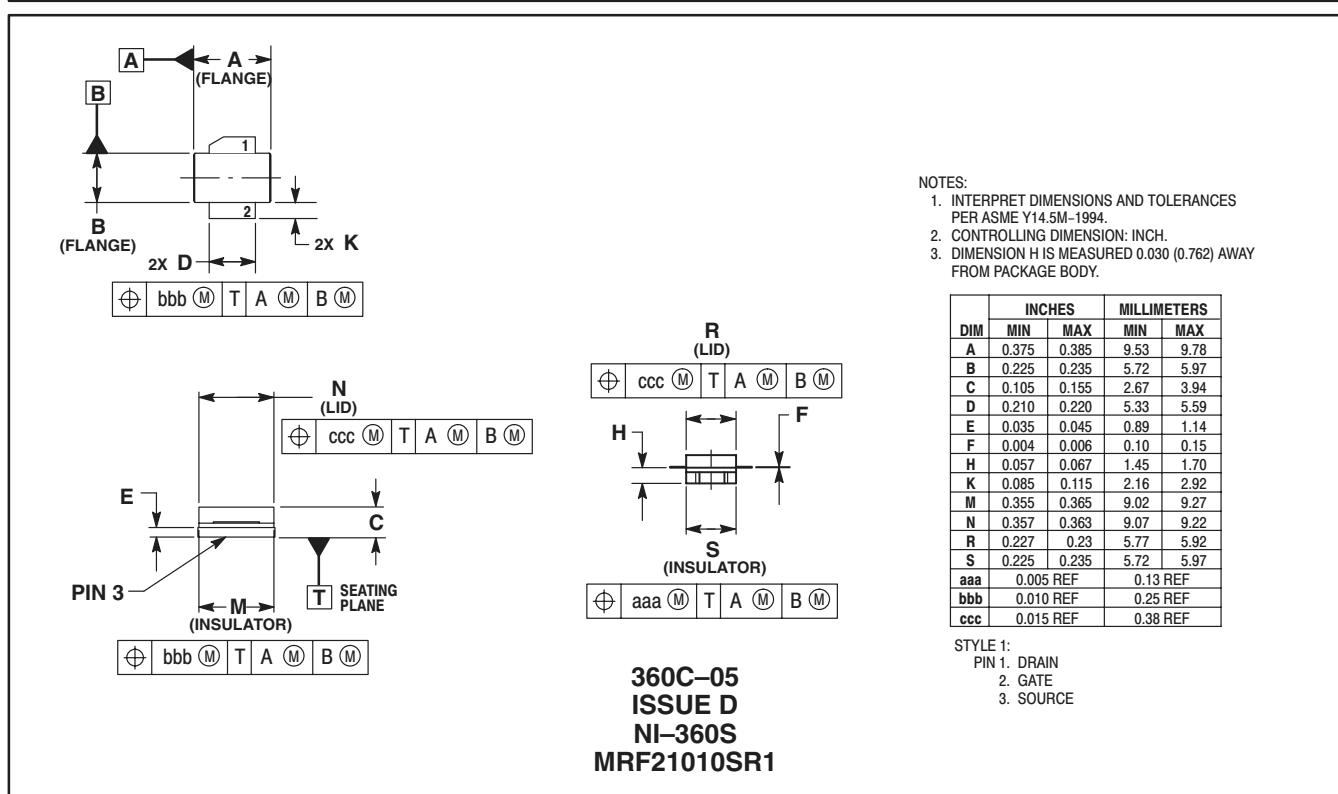
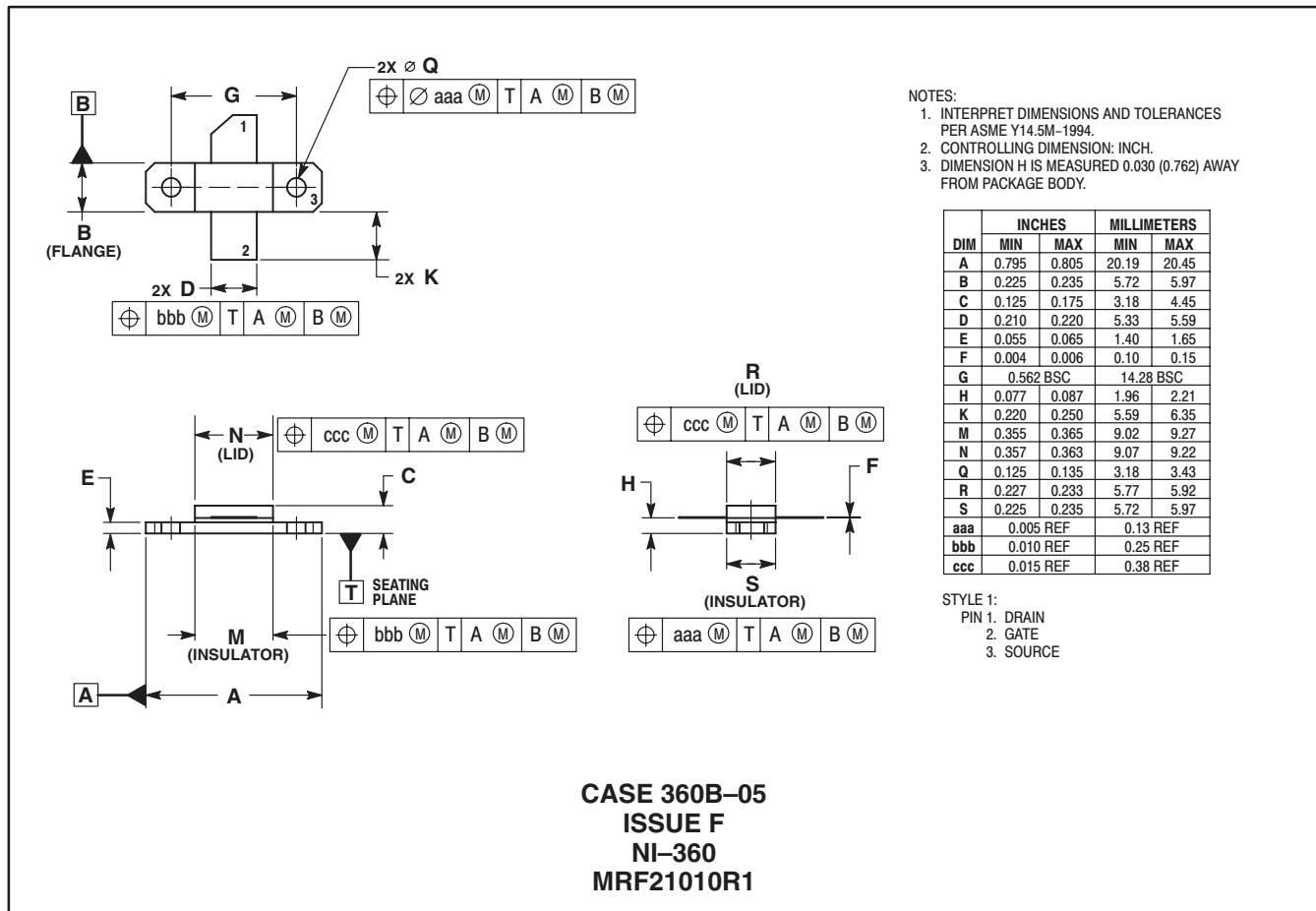


Figure 10. Series Equivalent Input and Output Impedance

## PACKAGE DIMENSIONS



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**ASIA/PACIFIC:** Motorola Semiconductors H.K. Ltd.; Silicon Harbour Centre, 2 Dai King Street, Tai Po Industrial Estate, Tai Po, N.T., Hong Kong. 852-26668334

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