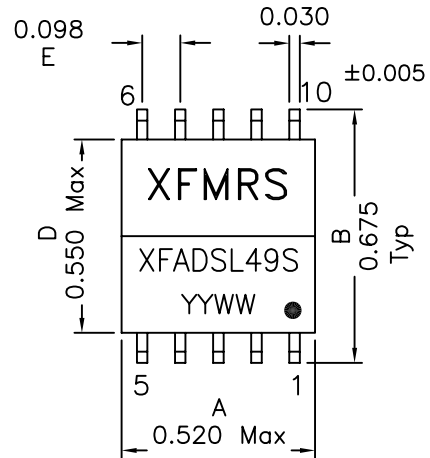
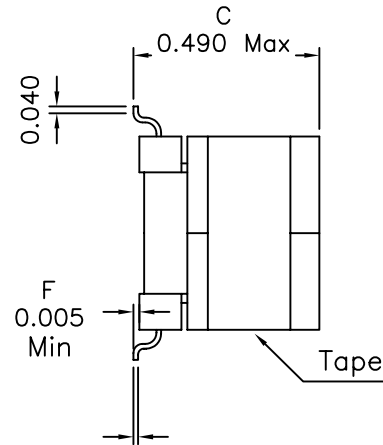


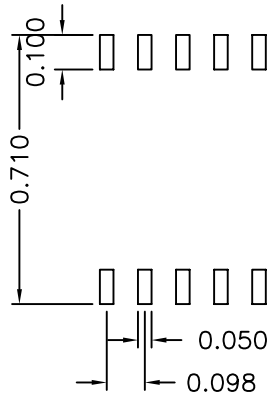
1. Dimensions:



CO-PLANARITY: 0.004 Max



0.020 ± 0.005



Suggested PCB Layout

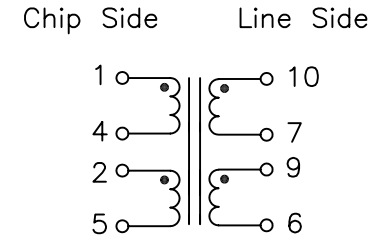
Notes:

- Solderability: Leads shall meet MIL-STD-202, Method 208D for solderability.
- Flammability: UL94V-0
- ASTM oxygen index: $> 28\%$
- Insulation System: Class F 155°C . UL file E151556
- Operating Temperature Range: All listed parameters are to be within tolerance from -40°C to $+85^{\circ}\text{C}$
- Designed to reflect 100 Ohms on the Line side with 39 Ohms on the Chip side.

Note: Designed to meet UL1950 Supplementary Insulation requirements for a working voltage up to 250V .

DOC. REV A/1

2. Schematic:



3. Electrical Specifications: @ 25°C

OCL: Pins 10-6 $440\mu\text{H} \pm 5\%$ @ 10KHz 0.1V , Tie Pins 7+9

Leakage L: Pins 10-6 $10\mu\text{H}$ Max @ 100KHz 0.1V
(Short Pins 1-4 & 7+9)

CW/W: Pins 1-10 50pF Max @ 100KHz 0.1V
(Short Chip, Short Line)

DCR: Pins 1-4,2-5 0.480 Ohms $\pm 10\%$

DCR: Pins 10-7,9-6 0.780 Ohms $\pm 10\%$

Turns Ratio: $(10-6):(1-5) = 1.6:1 \pm 1\%$, Tie Pins 4+2, 7+9

Long. Bal.: 45dB Min @ 30KHz - 1.1MHz

THD: -90dB Max @ 100KHz 2.5Vrms , 7.07Vp-p , Line:Chip

THD: -75dB Max @ 10KHz 2.0Vrms , 5.66Vp-p , Line:Chip

Insertion Loss: 0.5dB Max @ 772KHz

Isolation: 1875Vac , Pins 1-10, Tie Pins 4+2, 7+9

XFMRS Inc		Title: ADSL TRANSFORMER		
UNLESS OTHERWISE SPECIFIED TOLERANCES:		P/N: XFADSL49S		REV. A
.xxx ± 0.010		DWN.	廖玉坤	Jul-19-02
Dimensions in INCH		CHK.	李清儿	Jul-19-02
SHEET 1 OF 1		APP.	Isaiah M	Jul-19-02