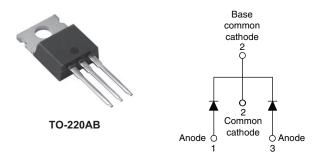


VS-STPS30L30CTPbF, VS-STPS30L30CT-N3

Vishay Semiconductors

Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY						
Package	TO-220AB					
I _{F(AV)}	2 x 15 A					
V _R	30 V					
V _F at I _F	0.37 V					
I _{RM} max.	350 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	15 mJ					

FEATURES

- 150 °C T_J operation
- · Very low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN FREE
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	UNITS					
I _{F(AV)}	Rectangular waveform	2 × 15	A			
V _{RRM}		30	N/			
V _F	15 A_{pk} , T_J = 125 °C (per leg)	0.37	, v			
TJ	Range	- 55 to 150	°C			

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-STPS30L30CTPbF	VS-STPS30L30CT-N3	UNITS				
Maximum DC reverse voltage	V _R	30	30	V				
Maximum working peak reverse voltage	V _{RWM}		50	v				

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	TIONS	VALUES	UNITS		
Maximum average forward current		$I_{F(AV)}$ 50 % duty cycle at T _C = 140 °C, rectangular waveform		30			
per leg	IF(AV)			15			
Maximum peak one cycle	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and	1450	A		
non-repetitive surge current		10 ms sine or 6 ms rect. pulse	with rated V _{RRM} applied	220			
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 7.5 mH		15	mJ		
Repetitive avalanche current per leg I_{AR} Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R t			2	А			

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
		15 A	T.I = 25 °C	0.46	V		
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	1j=25 C	0.57			
	VFM ⁽¹⁾	15 A	T.I = 125 °C	0.37			
		30 A	1j = 125 C	0.50			
Maximum reverse leakage current per leg	I _{RM}	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1.50	mA		
Maximum reverse leakage current per leg		T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	350			
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1500	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,\,duty\,cycle$ < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		Р		1.5	°C/W	
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.8	0,00	
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Maximum minimum				6 (5)	kgf ⋅ cm	
Mounting torque –	maximum			12 (10)	(lbf ⋅ in)	
Marking device			Case style TO-220AB	STPS30	DL30CT	



VS-STPS30L30CTPbF, VS-STPS30L30CT-N3

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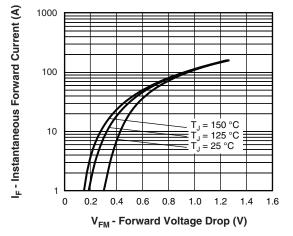


Fig. 1 - Maximum Forward Voltage Drop Characteristics

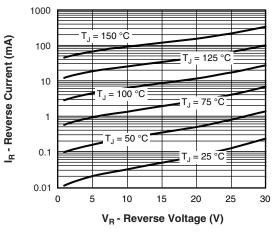


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

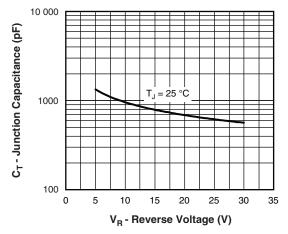
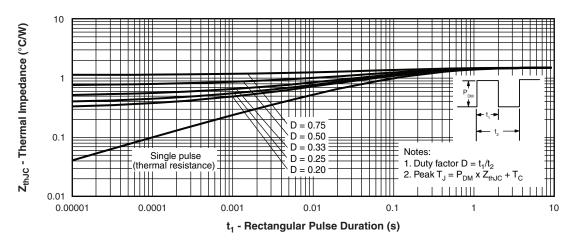
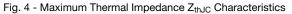


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



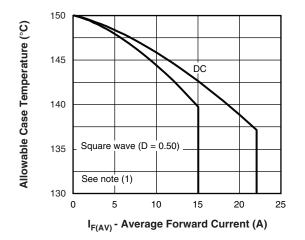


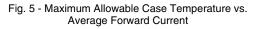
Revision: 30-Aug-11 3 Document Number: 94328 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

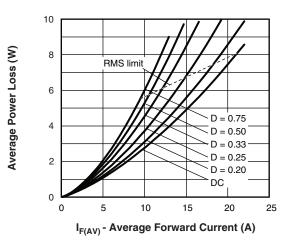


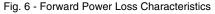
VS-STPS30L30CTPbF, VS-STPS30L30CT-N3

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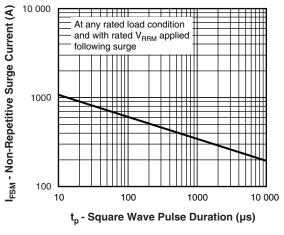
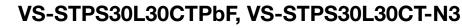


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

- ⁽¹⁾ Formula used: $T_C = T_J Pd \times R_{thJC}$;
 - $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6)





Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code	VS-	STPS	30	L	30	СТ	PbF	
	1	2	3	4	5	6	7	
	 Vishay Semiconductors product Schottky STPS series Current rating (30 = 30 A) L = Low voltage drop 							
	5 - Voltage rating (30 = 30 V)							
	 6 - CT = Essential part number 7 - Environmental digit • PbF = Lead (Pb)-free and RoHS complia 							
		• 1		ead (Pb	,			

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION							
VS-STPS30L30CTPbF	50	1000	Antistatic plastic tube				
VS-STPS30L30CT-N3	50	1000	Antistatic plastic tube				

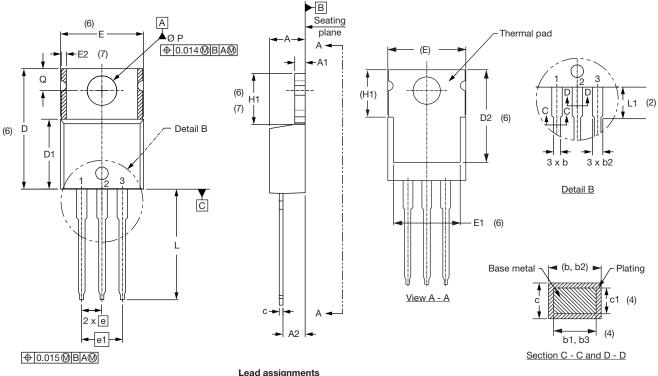
LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95222						
Daut variation information	TO-220AB PbF	www.vishay.com/doc?95225				
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028				
SPICE model		www.vishay.com/doc?95287				

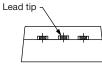


Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





.ead	assignments

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MAX. MIN.		NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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