# UNISONIC TECHNOLOGIES CO., LTD

6N60 Power MOSFET

# **6.2A, 600V N-CHANNEL POWER MOSFET**

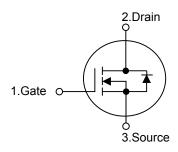
#### DESCRIPTION

The UTC 6N60 is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in switching power supplies and adaptors.

#### **FEATURES**

- \*  $R_{DS(ON)} = 1.5\Omega @V_{GS} = 10V$
- \* Ultra low gate charge (typical 20 nC)
- \* Low reverse transfer Capacitance ( C<sub>RSS</sub> = typical 10pF )
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

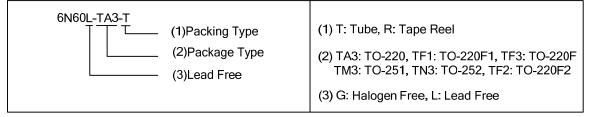
#### **SYMBOL**



### **ORDERING INFORMATION**

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
6N60L-TA3-T	6N60G-TA3-T	TO-220	G	D	S	Tube	
6N60L-TF1-T	6N60G-TF1-T	TO-220F1	G	D	S	Tube	
6N60L-TF2-T	6N60G-TF2-T	TO-220F2	G	D	S	Tube	
6N60L-TF3-T	6N60G-TF3-T	TO-220F	G	D	S	Tube	
6N60L-TM3-T	6N60G-TM3-T	TO-251	G	D	S	Tube	
6N60L-TN3-R	6N60G-TN3-R	TO-252	G	D	S	Tape Reel	
6N60L-TN3-T	6N60G-TN3-T	TO-252	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



TO-220F1 TO-220F2 TO-251 TO-252

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#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	600	V
Gate-Source Voltage		$V_{GSS}$	±30	V
Avalanche Current (Note 2)		I <sub>AR</sub>	6.2	Α
Continuous Drain Current		I <sub>D</sub>	6.2	Α
Pulsed Drain Current (Note 2)		I <sub>DM</sub>	24.8	Α
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	440	mJ
	Repetitive (Note 2)	E <sub>AR</sub>	13	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	ns
	TO-220	P <sub>D</sub>	125	
Power Dissipation	TO-220F/TO-220F1		40	14/
	TO-220F2		42	W
	TO-251/TO-252		55	
Junction Temperature		TJ	+150	°C
Operating Temperature		T <sub>OPR</sub>	-55 ~ <b>+</b> 150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by  $T_{\mathsf{J}}$
- 3. L = 14mH,  $I_{AS}$  = 6A,  $V_{DD}$  = 90V,  $R_{G}$  = 25  $\Omega$ , Starting  $T_{J}$  = 25°C
- 4.  $I_{SD} \le 6.2 \text{A}$ , di/dt  $\le 200 \text{A}/\mu \text{s}$ ,  $V_{DD} \le \text{BV}_{DSS}$ , Starting  $T_J = 25^{\circ}\text{C}$

# **■ THERMAL DATA**

PARAMETER		SYMBOL	RATING	UNIT	
Junction to Ambient	TO-220/ TO-220F2		62.5	°C/W	
	TO-220F/TO-220F1	$\theta_{JA}$	62.5		
	TO-251/TO-252		110		
Junction to Case	TO-220		1.0		
	TO-220F/TO-220F1	0	3.2	°C/W	
	TO-220F2	$\theta_{ extsf{JC}}$	2.97	C/VV	
	TO-251/TO-252		2.27		

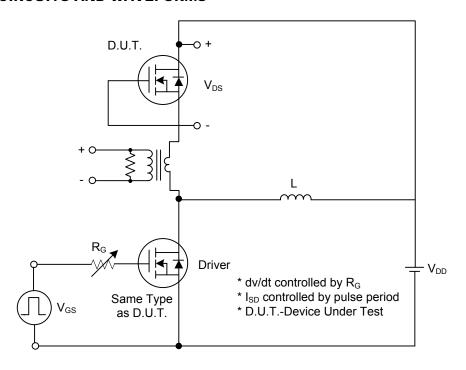
# ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	$V_{GS} = 0V, I_D = 250\mu A$	600			V
Drain-Source Leakage Current		I <sub>DSS</sub>	$V_{DS} = 600V, V_{GS} = 0V$			10	μΑ
For	rward		$V_{GS} = 30V, V_{DS} = 0V$			100	nA
Gate- Source Leakage Current Re	verse		$V_{GS} = -30V, V_{DS} = 0V$			-100	nA
Breakdown Voltage Temperature Coe	fficient	$\triangle BV_{DSS} / \triangle T_J$	I <sub>D</sub> =250μA, Referenced to 25°C		0.53		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Resistar	Static Drain-Source On-State Resistance		$V_{GS} = 10V, I_D = 3.1A$		1.0	1.5	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		$C_{ISS}$	V 05V V 0V		770	1000	pF
Output Capacitance	Output Capacitance		V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0 MHz		95	120	pF
Reverse Transfer Capacitance		C <sub>OSS</sub> C <sub>RSS</sub>	TI- I.U IVIMZ		10	13	pF
SWITCHING CHARACTERISTICS							
Turn-On Delay Time		$t_{D(ON)}$			20	50	ns
Turn-On Rise Time		$t_R$	$V_{DD}$ =300V, $I_{D}$ =6.2A,		70	150	ns
Turn-Off Delay Time		$t_{D(OFF)}$	$R_G = 25\Omega$ (Note 1, 2)		40	90	ns
Turn-Off Fall Time		t <sub>F</sub>			45	100	ns
Total Gate Charge	$Q_G$		V <sub>DS</sub> =480V, I <sub>D</sub> =6.2A,		20	25	nC
Gate-Source Charge		$Q_GS$	V <sub>GS</sub> =400V, I <sub>D</sub> =6.2A, V <sub>GS</sub> =10 V (Note 1, 2)		4.9		nC
Gate-Drain Charge	Sate-Drain Charge		V <sub>GS</sub> -10 V (Note 1, 2)		9.4		nC
DRAIN-SOURCE DIODE CHARACTE	ERISTIC	S AND MAXI	MUM RATINGS				
Drain-Source Diode Forward Voltage		$V_{SD}$	$V_{GS} = 0 \text{ V}, I_{S} = 6.2 \text{ A}$			1.4	V
Maximum Continuous Drain-Source Diode		Is				6.2	Α
Forward Current		ıs				0.2	^
Maximum Pulsed Drain-Source Diode		$I_{SM}$				24.8	Α
Forward Current		ISIVI				27.0	, · ·
Reverse Recovery Time		t <sub>rr</sub>	$V_{GS} = 0 \text{ V}, I_{S} = 6.2 \text{ A},$		290		ns
Reverse Recovery Charge		$Q_{RR}$	$dI_F/dt = 100 A/\mu s$ (Note 1)		2.35		μC

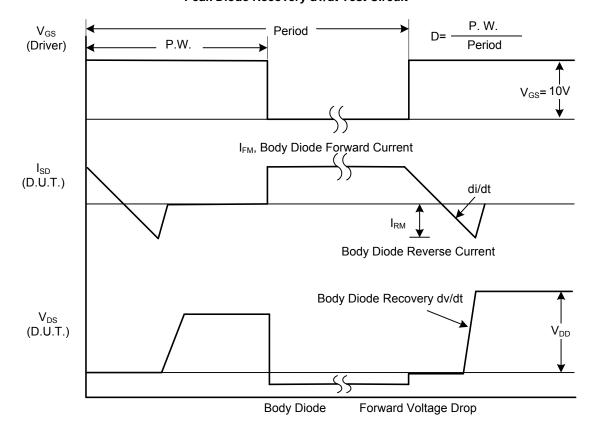
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

<sup>2.</sup> Essentially independent of operating temperature

#### ■ TEST CIRCUITS AND WAVEFORMS

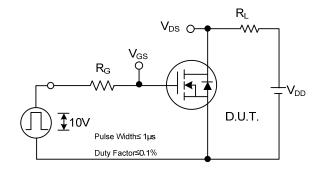


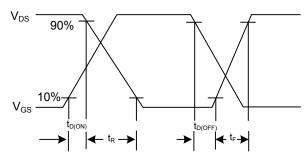
# Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

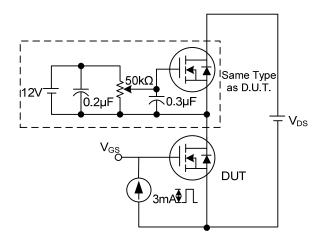
# ■ TEST CIRCUITS AND WAVEFORMS (Cont.)

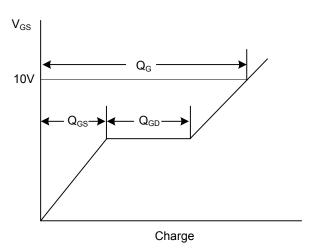




**Switching Test Circuit** 

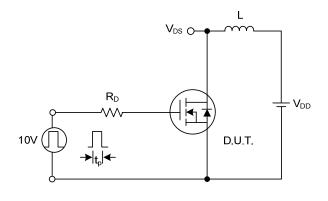
**Switching Waveforms** 

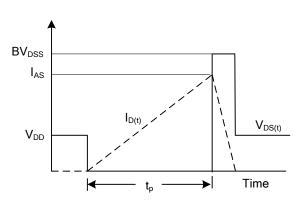




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 

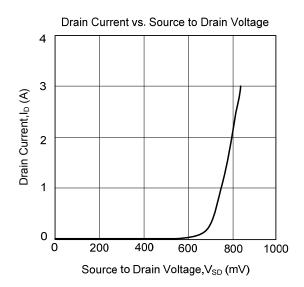


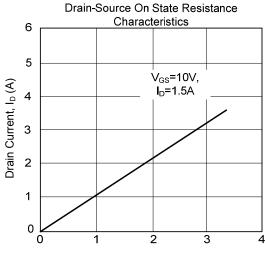


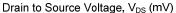
**Unclamped Inductive Switching Test Circuit** 

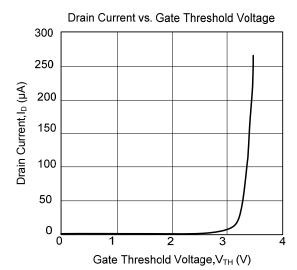
**Unclamped Inductive Switching Waveforms** 

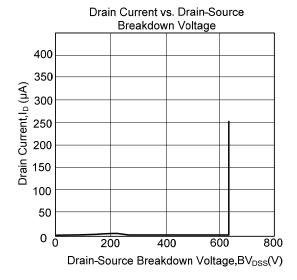
#### **■ TYPICAL CHARACTERISTICS**











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